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Behavioural languages – **STANDARD PREVIEW**
Part 6: VHDL Analog and Mixed-Signal Extensions
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IEC 61691-6:2021

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Part 6: VHDL Analog and Mixed-Signal Extensions

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1076.1 (2017)	91/1715/FDIS	91/1727/RVD

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Keywords: analog design, computer languages, hardware design, IEEE 1076™, IEEE 1076.1™, mixed-signal design, VHDL

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