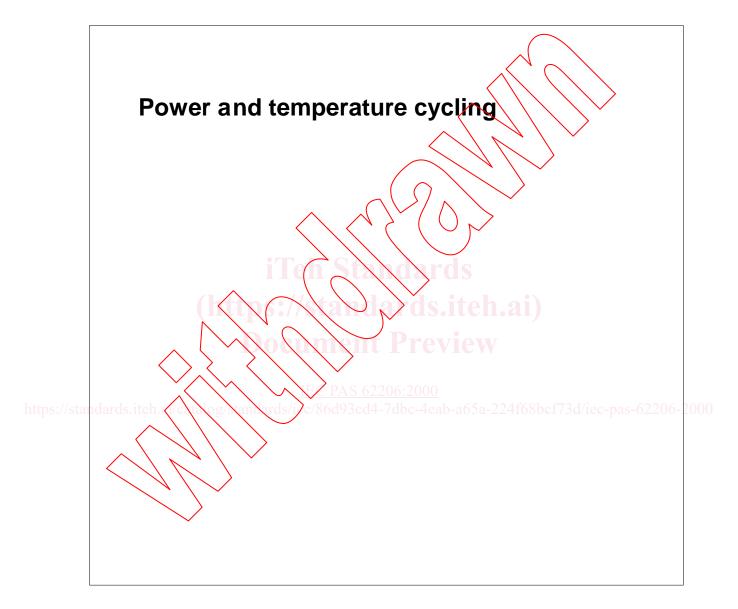
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EIA/JEDEC STANDARD

Test Method A105-B

Power and Temperature Cycling

EIA/JESD22-A105-B

(Revision of Test Method A105-A)

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FEBRUARY 1996

ELECTRONIC INDUSTRIES ASSOCIATION ENGINEERING DEPARTMENT







INTERNATIONAL ELECTROTECHNICAL COMMISSION

POWER AND TEMPERATURE CYCLING

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IEC-PAS 62206 was submitted by JEDEC and has been processed by IEC technical committee 47: Semiconductor devices.

The text of this PAS is based on the following document:

This PAS was approved for publication by the R-members of the committee concerned as indicated in the following document:

Draft PAS		7		/	Repo	ort on	vo	tin	g
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