

IEC TS 62878-2-10

Edition 1.0 2024-01

TECHNICAL SPECIFICATION



Device embedding assembly technology – Part 2-10: Design specification for cavity substrate

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CONTENTS

FΟ	REWORD	3
1	Scope	5
2	Normative references	5
3	Terms and definitions	5
4	Objective	5
5	Cavity formation	5
6	Design guideline	6
Anı	nex A (informative) Cavity formation examples	8
	A.1 General	8
	A.2 Etching type example	8
	A.3 Laser type example	9
Fig	gure 1 – Schematic of cavity substate	6
Fig	gure A.1 – Etching type	8
Fig	gure A.2 – Laser type	9
	ble 1 – Cavity formation types	
Tal	ble 2 – Cavity substrate drawing designators	7

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

DEVICE EMBEDDING ASSEMBLY TECHNOLOGY -

Part 2-10: Design specification for cavity substrate

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The text of this Technical Specification is based on the following documents:

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Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this Technical Specification is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/standardsdev/publications.

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