

INTERNATIONAL STANDARD

NORME INTERNATIONALE



**Integrated circuits – Measurement of electromagnetic emissions –
Part 8: Measurement of radiated emissions – IC stripline method**

**Circuits intégrés – Mesure des émissions électromagnétiques –
Partie 8: Mesure des émissions rayonnées – Méthode de la ligne TEM à plaques
(stripline) pour circuit intégré**

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

INTEGRATED CIRCUITS – MEASUREMENT OF ELECTROMAGNETIC EMISSIONS –

Part 8: Measurement of radiated emissions – IC stripline method

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IEC 61967-8 has been prepared by subcommittee 47A: Integrated circuits, of IEC technical committee 47: Semiconductor devices. It is an International Standard.

This second edition cancels and replaces the first edition published in 2011. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) frequency range of 150 kHz to 3 GHz was deleted from the scope;
- b) extension of upper usable frequency to 6 GHz or higher as long as the defined requirements are fulfilled.

The text of this International Standard is based on the following documents:

Draft	Report on voting
47A/1152/FDIS	47A/1153/RVD

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this International Standard is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/publications.

A list of all parts of the IEC 61967 series, under the general title *Integrated circuits – Measurement of electromagnetic emissions* can be found on the IEC website.

Future standards in this series will carry the new general title as cited above. Titles of existing standards in this series will be updated at the time of the next edition.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under webstore.iec.ch in the data related to the specific document. At this date, the document will be

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INTEGRATED CIRCUITS – MEASUREMENT OF ELECTROMAGNETIC EMISSIONS –

Part 8: Measurement of radiated emissions – IC stripline method

1 Scope

This part of IEC 61967 defines a method for measuring the electromagnetic radiated emission from an integrated circuit (IC) using an IC stripline. The IC being evaluated is mounted on an EMC test board (PCB) between the active conductor and the ground plane of the IC stripline arrangement.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60050-131, *International Electrotechnical Vocabulary (IEV) – Part 131: Circuit theory*

IEC 60050-161, *International Electrotechnical Vocabulary (IEV) – Chapter 161: Electromagnetic compatibility*

IEC 61967-1, *Integrated circuits – Measurement of electromagnetic emissions – Part 1: General conditions and definitions*

IEC 61000-4-20, *Electromagnetic compatibility (EMC) – Part 4-20: Testing and measurement techniques – Emission and immunity testing in transverse electromagnetic (TEM) waveguides*

3 Terms and definitions

For the purposes of this document, the terms and definitions given in IEC 61967-1, IEC 60050-131 and IEC 60050-161, and the following apply.

ISO and IEC maintain terminology databases for use in standardization at the following addresses:

- IEC Electropedia: available at <https://www.electropedia.org/>
- ISO Online browsing platform: available at <https://www.iso.org/obp>

3.1

transverse electromagnetic mode

TEM mode

waveguide mode in which the components of the electric and magnetic fields in the propagation direction are much less than the primary field components across any transverse cross-section

3.2

TEM waveguide

open or closed transmission line system, in which a wave is propagating in the transverse electromagnetic mode to produce a specified field for testing purposes

3.3 IC stripline

TEM waveguide, consisting of an active conductor placed on a defined spacing over an enlarged ground plane, connected to a port structure on either end and an optional shielded enclosure

Note 1 to entry: This arrangement guides a wave propagation in the transverse electromagnetic mode to produce a specific field for testing purposes between the active conductor and the enlarged ground plane. As enlarged ground plane the ground plane of the standard EMC test board according to IEC 61967-1 should be used. An optional shielding enclosure may be used for fixing the IC stripline configuration and for shielding purposes. This leads to a closed version of the IC stripline in opposite to the open version without shielding enclosure. For further information, see Annex A.

3.4 two-port TEM waveguide

TEM waveguide with input/output measurement ports at both ends

3.5 characteristic impedance

magnitude of the ratio of the voltage between the active conductor and the corresponding ground plane to the current on either conductor for any constant phase wave-front

Note 1 to entry: The characteristic impedance is independent of the voltage/current magnitudes and depends only on the cross-sectional geometry of the transmission line. TEM waveguides are typically designed to have a 50 Ω characteristic impedance. For further information and equation to stripline arrangements, see Annex A.

3.6 primary field component

electric field component aligned with the intended test polarization

Note 1 to entry: For example, in IC stripline, the active conductor is parallel to the horizontal floor, and the primary mode electric field vector is vertical at the transverse centre of the IC stripline.

4 General

This test method is based on the TEM wave guide measurement principle according to IEC 61000-4-20. A stripline set-up is used to measure the RF emission of ICs. The RF voltage at the stripline port is related to the electromagnetic radiation potential of the IC and will be measured using a spectrum analyzer or measuring receiver. The intent of this test method is to provide a quantitative measure of the RF emissions from ICs for comparison or other evaluation.

5 Test conditions

5.1 General

The test conditions shall meet the requirements as described in IEC 61967-1. In addition, the following test conditions described in 5.2 and 5.3 shall apply.

5.2 Supply voltage

The supply voltage shall be as specified by the IC manufacturer. If the users of this procedure agree to other values, they shall be documented in the test report.

5.3 Frequency range

The effective frequency range for the IC stripline is 150 kHz to 6 GHz in combination with the VSWR characteristic $\leq 1,25$ for $f \leq 3$ GHz and $\leq 1,4$ for $f > 3$ GHz. The upper frequency can be extended if the IC stripline does not exhibit significant higher order modes over the frequency range being measured.

NOTE The given VSWR value of 1,4 is based on evolving technical solutions for IC striplines. For accuracy reasons the VSWR value is targeted as low as possible (e.g. 1,3).

6 Test equipment

6.1 General

The test equipment shall meet the requirements as described in IEC 61967-1. In addition, the test equipment requirements described in 6.2 to 6.5 shall apply.

6.2 RF measuring instrument

A spectrum analyzer or EMI receiver shall be used. The resolution bandwidth shall be 9 kHz for EMI receivers or 10 kHz for spectrum analyzers in the frequency range from 150 kHz to 30 MHz and respectively 120 kHz or 100 kHz above 30 MHz according to IEC 61967-1. Measurements shall be made with a peak detector and presented in units of dB μ V [for 50 Ω system: (dBm readings) + 107 = dB μ V]. For spectrum analyzers, the frequency band of interest shall be swept in calibrated or coupled mode (auto sweep).

6.3 Pre-amplifier

Optionally, a 20 dB to 30 dB gain, low noise pre-amplifier can be used. If used, the pre-amplifier shall be connected directly to the measurement port of the IC stripline using the appropriate 50 Ω coaxial adapter.

6.4 IC stripline

Two-port TEM waveguide, consisting of an active conductor placed on a defined spacing over an enlarged ground plane, connected to a port structure on either end and an optional shielded enclosure. The spacing between active conductor and ground plane of the IC stripline has a default value of 6,7 mm. Other spacing can be used but shall be noted in the test report.

NOTE A conversion factor allows comparisons between IC stripline arrangements with different spacing between active conductor and ground plane (see Annex A).

This IC stripline arrangement guides wave propagation in the transverse electromagnetic mode to produce a specific field for testing purposes between the active conductor and the enlarged ground plane. As enlarged ground plane, the ground plane of the standard EMC test board according to IEC 61967-1 should be used. The EMC test board controls the geometry and orientation of the operating IC relative to the IC stripline and eliminates any connecting leads within the IC stripline (these are on the backside of the board, which is opposite to the IC stripline). An optional shielding enclosure may be used for fixing the IC stripline configuration and for shielding purposes. This leads to a closed version of the IC stripline as opposed to the open version without shielding enclosure.

For further information, see Annex A.

6.5 50 Ω termination

A 50 Ω termination with a VSWR less than 1,1 for $f \leq 3$ GHz and $\leq 1,2$ for $f > 3$ GHz up to the maximum used frequency shall be used for the IC stripline 50 Ω port not connected to the RF measuring instrument.

7 Test set-up

7.1 General

The test set-up shall meet the requirements as described in IEC 61967-1. In addition, the following test set-up requirements shall apply.

7.2 Test configuration

See Figure 1 for IC stripline test configuration. One of the 50 Ω ports is terminated with a 50 Ω load. The remaining 50 Ω port is connected to the spectrum analyzer through the optional preamplifier.

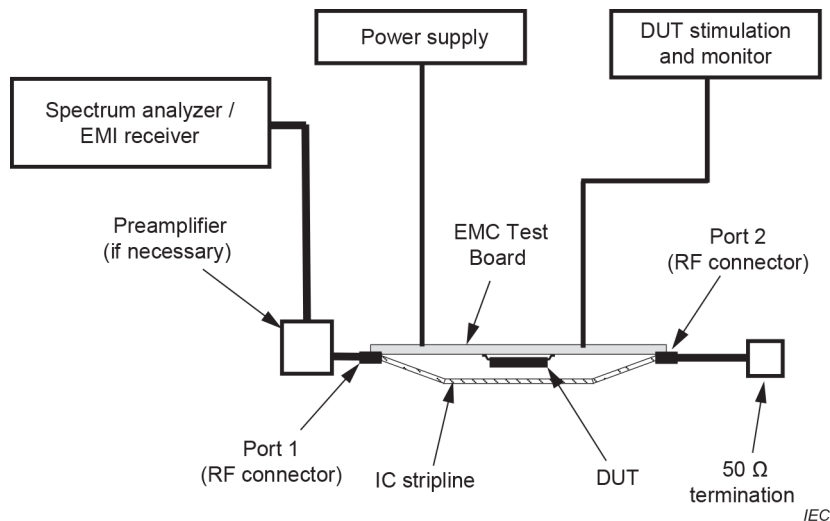


Figure 1 – IC stripline test set-up

For further information and cross-section view of the IC stripline arrangement, see Annex A.

7.3 EMC test board (PCB)

The DUT shall be mounted on a PCB according to IEC 61967-1 and in this document. In cases where IEC 61967-1 and this document are in conflict, the requirements of this document shall apply.

The EMC test board is provided with the appropriate measurement or monitoring points to ensure the correct DUT operation. It controls the geometry and orientation of the DUT relative to the active conductor and eliminates in case of a closed version of the IC stripline, any connecting leads within the housing (these are on the backside of the board, which is outside the housing).

8 Test procedure

8.1 General

These default test conditions are intended to assure a consistent test environment. If the users of this procedure agree to other conditions, they shall be documented in the test report.

8.2 Ambient conditions

The definitions for ambient temperature and general condition of IEC 61967-1 are valid.

The ambient RF noise level shall be verified to be at least 6 dB below the lowest emission level(s) to be measured. The DUT shall be installed in the test set-up, as used for testing. The DUT shall not be activated (e.g. power supply voltage disabled). A scan shall be made to measure the ambient noise. A description of the ambient conditions shall be a part of the test report.

If the measured noise floor is excessive, e.g. due to external ambient noises or the noise floor of the measurement system itself, shielded enclosure should be used. The noise floor measurement system can be improved by using a lower noise preamplifier.

8.3 Operational check

Energize the DUT and complete an operational check to assure proper function of the device (i.e. Run IC test code).

8.4 Verification of IC stripline RF characteristic

For verification of the IC stripline RF characteristic, the VSWR value of the empty IC stripline with 50 Ω load termination at the second port shall be measured and documented in the test report. The value shall be in accordance with 5.3.

In addition, it is recommended to check also the DUT loaded IC stripline. Before testing, the IC stripline resonances should be determined in accordance with IEC 61000-4-20 with the test set-up including DUT installed and powered off.

$$A_{\text{tloss}} = \left| 10 \times \log_{10} \left(\frac{P_{\text{output}}}{P_{\text{fwd}} - P_{\text{refl}}} \right) \right| \leq 1 \text{ dB} \quad (1)$$

where

A_{tloss} is the transmission loss of loaded IC stripline [dB];

P_{refl} is the reflected power at input port [W];

P_{fwd} is the forward power at input port [W];

P_{output} is the measured power at output port [W].

Measurements carried out at frequencies where the VSWR and transmission losses A_{tloss} exceed the maximum tolerated values shall be ignored.

8.5 Test technique

With the EMC test board energized and the DUT being operated in the intended test mode, measure the RF emissions over the desired frequency band.

When using a spectrum analyzer, enable the "Max Hold" function and allow the analyzer to perform a minimum of three sweeps while the IC code loop executes. The sweep time shall be greater than the IC code loop execution time.

NOTE The "Max Hold" setting on a spectrum analyzer maintains the maximum level of each trace data point and updates each point if a new maximum level is detected in successive sweeps.

When using a receiver, the dwell time for each test frequency shall be greater than or equal to two times the IC code loop execution time and record the maximum level detected.

Four separate emissions measurements are performed resulting in four sets of data. The first measurement is made with the EMC test board mounted in an arbitrary orientation in the test setup. The second measurement is made with the EMC test board rotated 90 degrees from the orientation in the first measurement. For each of the third and fourth measurements, the EMC test board is rotated again to ensure emissions are measured from all four possible orientations. The four sets of data shall be documented in the test report.