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**Thermal standardization on semiconductor packages –
Part 2-1: 3D thermal simulation models of semiconductor packages for steady-
state analysis – Discrete packages**

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

THERMAL STANDARDIZATION ON SEMICONDUCTOR PACKAGES –**Part 2-1: 3D thermal simulation models of semiconductor packages for steady-state analysis – Discrete packages**

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The text of this International Standard is based on the following documents:

Draft	Report on voting
47D/976/FDIS	47D/982/RVD

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this International Standard is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/publications.

A list of all parts in the IEC 63378 series, published under the general title *Thermal standardization on semiconductor packages*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under webstore.iec.ch in the data related to the specific document. At this date, the document will be

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- withdrawn, or
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THERMAL STANDARDIZATION ON SEMICONDUCTOR PACKAGES –

Part 2-1: 3D thermal simulation models of semiconductor packages for steady-state analysis – Discrete packages

1 Scope

This part of IEC 63378 specifies three-dimensional (3D) thermal models of discrete semiconductor packages (TO-243, TO-252 and TO-263), utilized in the steady-state thermal analysis of electronic devices to estimate junction temperatures accurately.

This model is assumed to be made by semiconductor suppliers and to be used by assembly makers of electronic devices.

2 Normative references

There are no normative references in this document.

3 Terms and definitions iTeh Standards

For the purposes of this document, the following terms and definitions apply.

ISO and IEC maintain terminology databases for use in standardization at the following addresses:

- IEC Electropedia: available at <https://www.electropedia.org/>
- ISO Online browsing platform: available at <https://www.iso.org/obp>

3.1

detailed model

semiconductor package model which has both the structures of each portion, such as die or moulding or terminal, and the material properties for thermal analysis

Note 1 to entry: This model is often simplified to some extent.

4 Attributes of 3D thermal models

4.1 General

Clause 4 shows the attributes such as dimensions and thermal properties of three different type of TO-packages. These attributes are used to estimate junction temperatures.

4.2 TO-243

Figure 1 and Table 1 show the attributes which TO-243 thermal simulation models shall have.

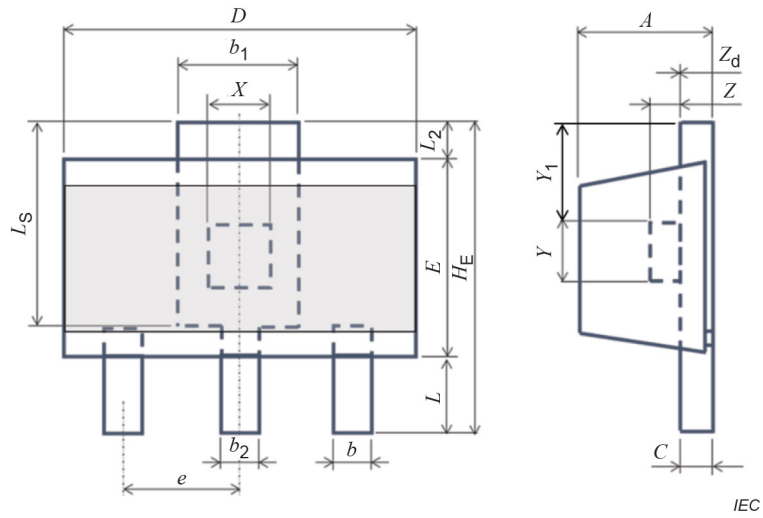


Figure 1 – Dimensions of TO-243

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Table 1 – Attributes for TO-243

	Attribute	Symbol	Value	Unit
Dimension	Package thickness	A		m
	Terminal width	b		m
	Heat spreader width	b_1		m
	Centre terminal width	b_2		m
	Terminal thickness	C		m
	Package width	D		m
	Package body length	E		m
	Terminal pitch	e		m
	Span	H_E		m
	Terminal length	L		m
	Exposed terminal length	L_2		m
	Heat spreader length	L_S		m
	Die width	X		m
	Die length	Y		m
	Die length offset	Y_1		m
	Die thickness	Z		m
Die attach thickness	Z_d		m	
Thermal conductivity	Thermal conductivity of die	k_d		W/(m × K)
	Thermal conductivity of die attach	k_{da}		W/(m × K)
	Thermal conductivity of molding	k_m		W/(m × K)
	Thermal conductivity of heat spreader	k_s		W/(m × K)
NOTE 1 The default thermal conductivity of molding is 0,6 W/(m·K) and that of heat spreader is 300 W/(m × K). The thermal conductivity of die and die attach varies a lot according to the material adopted.				
NOTE 2 The thickness of molding is the same as package thickness "A".				
NOTE 3 The length and width of the die attach are the same as those of die X and Y .				
NOTE 4 The columns for "Value" shall be filled in.				

4.3 TO-252

Figure 2 and Table 2 show the attributes which TO-252 thermal simulation models shall have. (A contribution analysis in the case of TO-252 is given in Annex A.)

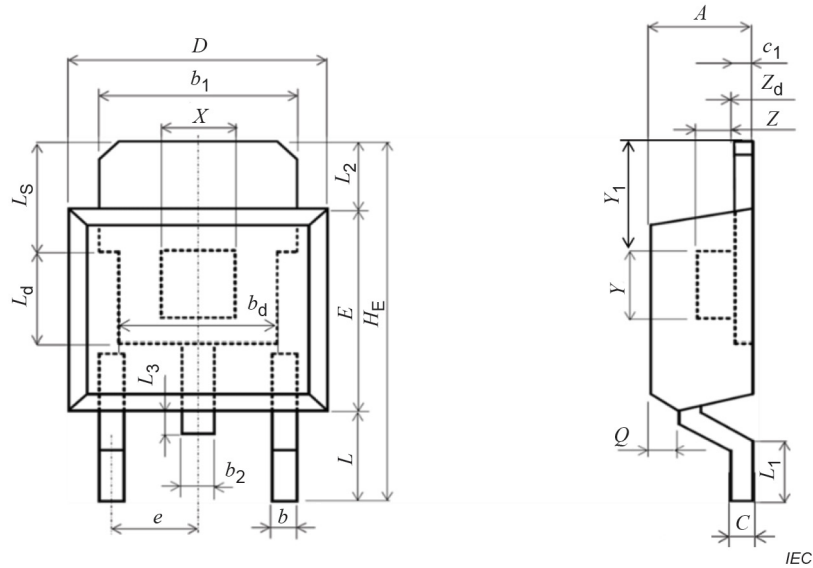


Figure 2 – Dimensions of TO-252

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