# IEC 61523-4

Edition 2.0 2023-10

# INTERNATIONAL STANDARD

IEEE Std 1801™

colour inside

## Delay and power calculation standards –
## Part 4: Design and Verification of Low-Power, Energy-Aware Electronic Systems



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# DELAY AND POWER CALCULATION STANDARDS –

# Part 4: Design and Verification of Low-Power, Energy-Aware Electronic Systems

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# IEEE Standard for Design and Verification of Low-Power, Energy-Aware Electronic Systems

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Approved 27 September 2018

**IEEE-SA Standards Board**

**Abstract:** A method is provided for specifying power intent for an electronic design, for use in verification of the structure and behavior of the design in the context of a given power-management architecture, and for driving implementation of that power-management architecture. The method supports incremental refinement of power intent specifications required for IP-based design flows.

**Keywords:** corruption semantics, IEEE 1801™, interface specification, IP reuse, isolation, level-shifting, power-aware design, power domains, power intent, power modes, power states, progressive design refinement, retention, retention strategies

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