



**SLOVENSKI STANDARD**  
**SIST EN 300 395-2 V1.3.1:2006**  
**01-april-2006**

---

**Prizemni snopovni radio (TETRA) – Govorni kodek za kanal s polno hitrostjo – 2.  
del: Kodek TETRA**

Terrestrial Trunked Radio (TETRA); Speech codec for full-rate traffic channel; Part 2:  
TETRA codec

**iTeh STANDARD PREVIEW**  
**(standards.iteh.ai)**

[SIST EN 300 395-2 V1.3.1:2006](#)

<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006>

**Ta slovenski standard je istoveten z:** **EN 300 395-2 Version 1.3.1**

---

**ICS:**

33.070.10	Prizemni snopovni radio (TETRA)	Terrestrial Trunked Radio (TETRA)
-----------	------------------------------------	--------------------------------------

**SIST EN 300 395-2 V1.3.1:2006** **en**

## iTeh STANDARD PREVIEW (standards.iteh.ai)

[SIST EN 300 395-2 V1.3.1:2006](#)

<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006>

# ETSI EN 300 395-2 V1.3.1 (2005-01)

European Standard (Telecommunications series)

## Terrestrial Trunked Radio (TETRA); Speech codec for full-rate traffic channel; Part 2: TETRA codec

iTeh STANDARD PREVIEW  
(standards.iteh.ai)

[SIST EN 300 395-2 V1.3.1:2006](#)

<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006>



## Reference

REN/TETRA-05059

## Keywords

TETRA, radio, codec

***ETSI***

650 Route des Lucioles  
F-06921 Sophia Antipolis Cedex - FRANCE

Tel.: +33 4 92 94 42 00 Fax: +33 4 93 65 47 16

Siret N° 348 623 562 00017 - NAF 742 C  
Association à but non lucratif enregistrée à la  
Sous-Préfecture de Grasse 06 N° 7303/88

## iTeh STANDARD PREVIEW (standards.iteh.ai)

[SIST EN 300 395-2 V1.3.1:2006](#)  
<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-bb9ca146b3>  
**Important notice**

Individual copies of the present document can be downloaded from:  
<http://www.etsi.org>

The present document may be made available in more than one electronic version or in print. In any case of existing or perceived difference in contents between such versions, the reference version is the Portable Document Format (PDF). In case of dispute, the reference shall be the printing on ETSI printers of the PDF version kept on a specific network drive within ETSI Secretariat.

Users of the present document should be aware that the document may be subject to revision or change of status.  
Information on the current status of this and other ETSI documents is available at  
<http://portal.etsi.org/tb/status/status.asp>

If you find errors in the present document, please send your comment to one of the following services:  
[http://portal.etsi.org/chaircor/ETSI\\_support.asp](http://portal.etsi.org/chaircor/ETSI_support.asp)

---

### ***Copyright Notification***

No part may be reproduced except as authorized by written permission.  
The copyright and the foregoing restriction extend to reproduction in all media.

© European Telecommunications Standards Institute 2005.  
All rights reserved.

**DECT™, PLUGTESTS™ and UMTS™** are Trade Marks of ETSI registered for the benefit of its Members.  
**TIPHON™** and the **TIPHON logo** are Trade Marks currently being registered by ETSI for the benefit of its Members.  
**3GPP™** is a Trade Mark of ETSI registered for the benefit of its Members and of the 3GPP Organizational Partners.

---

## Contents

Intellectual Property Rights .....	7
Foreword.....	7
1 Scope .....	9
2 References .....	9
3 Abbreviations .....	9
4 Full rate codec .....	10
4.1 Structure of the codec .....	10
4.2 Functional description of the codec.....	12
4.2.1 Pre- and post-processing .....	12
4.2.2 Encoder.....	13
4.2.2.1 Short-term prediction .....	14
4.2.2.2 LP to LSP and LSP to LP conversion .....	14
4.2.2.3 Quantization and interpolation of LP parameters.....	16
4.2.2.4 Long-term prediction analysis.....	17
4.2.2.5 Algebraic codebook: structure and search.....	19
4.2.2.6 Quantization of the gains .....	21
4.2.2.7 Detailed bit allocation .....	23
4.2.3 Decoder.....	23
4.2.3.1 Decoding process .....	24
4.2.3.1.1 Decoding of LP filter parameters.....	24
4.2.3.1.2 Decoding of the adaptive codebook vector.....	24
4.2.3.1.3 Decoding of the innovation vector .....	25
4.2.3.1.4 Decoding of the adaptive and innovative codebook gains.....	25
4.2.3.1.5 Computation of the reconstructed speech.....	25
4.2.3.2 Error concealment .....	25
5 Channel coding for speech .....	26
5.1 General .....	26
5.2 Interfaces in the error control structure .....	26
5.3 Notations .....	28
5.4 Definition of sensitivity classes and error control codes .....	28
5.4.1 Sensitivity classes .....	28
5.4.2 CRC codes .....	28
5.4.3 16-state RCPC codes .....	30
5.4.3.1 Encoding by the 16-state mother code of rate 1/3.....	30
5.4.3.2 Puncturing of the mother code .....	30
5.5 Error control scheme for normal speech traffic channel.....	31
5.5.1 CRC code.....	31
5.5.2 RCPC codes .....	31
5.5.2.1 Puncturing scheme of the RCPC code of rate 8/12 (equal to 2/3).....	31
5.5.2.2 Puncturing scheme of the RCPC code of rate 8/18 .....	31
5.5.3 Matrix Interleaving .....	32
5.6 Error control scheme for speech traffic channel with frame stealing activated.....	33
5.6.1 CRC code.....	33
5.6.2 RCPC codes .....	34
5.6.2.1 Puncturing scheme of the RCPC code of rate 8/17 .....	35
5.6.3 Interleaving .....	35
6 Channel decoding for speech .....	35
6.1 General .....	35
6.2 Error control structure .....	35
7 Codec performance.....	36
8 Bit exact description of the TETRA codec.....	36

9	AMR speech codec.....	38
10	Channel coding for AMR speech .....	38
10.1	General .....	38
10.2	Interfaces in the error control structure .....	38
10.3	Notations .....	38
10.4	Definition of sensitivity classes and error control codes .....	38
10.4.1	Sensitivity classes .....	38
10.4.2	CRC codes .....	39
10.4.3	16-state RCPC codes .....	40
10.4.3.1	Encoding by the 16-state mother code of rate 1/3.....	40
10.4.3.2	Puncturing of the mother code .....	41
10.5	Error control scheme for normal AMR speech traffic channel.....	41
10.5.1	CRC code.....	41
10.5.2	RCPC codes .....	41
10.5.2.1	Puncturing scheme of the RCPC code of rate 8/12 (equal to 2/3).....	42
10.5.2.2	Puncturing scheme of the RCPC code of rate 8/18 .....	42
10.5.3	Matrix Interleaving .....	42
10.6	Error control scheme for AMR speech traffic channel with frame stealing activated .....	43
10.6.1	Speech frames in stealing mode.....	43
10.6.2	CRC code.....	44
10.6.3	RCPC codes .....	45
10.6.3.1	Puncturing scheme of the RCPC code of rate 14/8 .....	45
10.6.4	Interleaving .....	45
11	Channel decoding for AMR speech .....	45
11.1	General .....	45
11.2	Error control structure .....	45
12	Bit exact description of the AMR codec FEC .....	46
<b>Annex A (informative):      Implementation of speech channel decoding.....</b>		<b>47</b>
A.1 Algorithmic description of speech channel decoding <small>SIST EN 300 395-2 V1.3.1:2006 <a href="https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-441a-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006">https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-441a-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006</a></small>		47
A.1.1	Definition of error control codes .....	47
A.1.1.1	16-state RCPC codes .....	47
A.1.1.1.1	Obtaining the mother code from punctured code .....	47
A.1.1.1.2	Viterbi decoding of the 16-state mother code of the rate 1/3 .....	47
A.1.1.2	CRC codes .....	48
A.1.1.3	Type-4 bits .....	48
A.1.2	Error control scheme for normal speech traffic channel.....	48
A.1.2.1	Matrix Interleaving .....	48
A.1.2.2	RCPC codes .....	48
A.1.2.2.1	Puncturing scheme of the RCPC code of rate 8/12 (equal to 2/3).....	49
A.1.2.2.2	Puncturing scheme of the RCPC code of rate 8/18 .....	49
A.1.2.3	CRC code.....	49
A.1.2.4	Speech parameters .....	49
A.1.3	Error control scheme for speech traffic channel with frame stealing activated .....	49
A.1.3.1	Interleaving .....	49
A.1.3.2	RCPC codes .....	49
A.1.3.2.1	Puncturing scheme of the RCPC code of rate 8/17 .....	50
A.1.3.3	CRC code.....	50
A.1.3.4	Speech parameters .....	50
A.2	C Code for speech channel decoding .....	50
<b>Annex B (informative):      Indexes .....</b>		<b>51</b>
B.1	Index of C code routines .....	51
B.2	Index of files.....	54
<b>Annex C (informative):      Codec performance.....</b>		<b>55</b>
C.1	General .....	55

C.2 Quality .....	55
C.2.1 Subjective speech quality .....	55
C.2.1.1 Description of characterization tests .....	55
C.2.1.2 Absolute speech quality .....	55
C.2.1.3 Effect of input level .....	55
C.2.1.4 Effect of input frequency characteristic .....	55
C.2.1.5 Effect of transmission errors .....	56
C.2.1.6 Effect of tandeming .....	56
C.2.1.7 Effect of acoustic background noise .....	56
C.2.1.8 Effect of vocal effort .....	56
C.2.1.9 Effect of frame stealing .....	56
C.2.1.10 Speaker and language dependency .....	56
C.2.2 Comparison with analogue FM .....	56
C.2.2.1 Analogue and digital systems results .....	56
C.2.2.2 All conditions .....	57
C.2.2.3 Input level .....	57
C.2.2.4 Error patterns .....	58
C.2.2.5 Background noise .....	58
C.2.3 Additional tests .....	58
C.2.3.1 Types of signals .....	58
C.2.3.2 Codec behaviour .....	58
C.3 Performance of the channel coding/decoding for speech .....	59
C.3.1 Classes of simulation environment conditions .....	59
C.3.2 Classes of equipment .....	59
C.3.3 Classes of bits .....	60
C.3.4 Channel conditions .....	60
C.3.5 Results for normal case .....	60
<b>iTeh STANDARD PREVIEW (standards.iteh.ai)</b>	
C.4 Complexity .....	61
C.4.1 Complexity analysis .....	61
C.4.1.1 Measurement methodology .....	61
C.4.1.2 TETRA basic operators .....	61
C.4.1.3 Worst case path for speech encoder .....	63
C.4.1.4 Worst case path for speech decoder .....	64
C.4.1.5 Condensed complexity values for encoder and decoder .....	65
C.4.2 DSP independence .....	66
C.4.2.1 Program control structure .....	66
C.4.2.2 Basic operator implementation .....	66
C.4.2.3 Additional operator implementation .....	66
C.5 Delay .....	66
<b>Annex D (informative):      Results of the TETRA codec characterization listening and complexity tests .....</b>	67
D.1 Characterization listening test .....	67
D.1.1 Experimental conditions .....	67
D.1.2 Tables of results .....	68
D.2 TETRA codec complexity study .....	76
D.2.1 Computational analysis results .....	76
D.2.1.1 TETRA speech encoder .....	76
D.2.1.2 TETRA speech decoder .....	84
D.2.1.3 TETRA channel encoder and decoder .....	87
D.2.2 Memory requirements analysis results .....	89
D.2.2.1 TETRA speech encoder .....	89
D.2.2.2 TETRA speech decoder .....	90
D.2.2.3 TETRA speech channel encoder .....	90
D.2.2.4 TETRA speech channel decoder .....	90
<b>Annex E (informative):      Description of attached computer files .....</b>	91
E.1 Directory C-WORD .....	91

E.2	Directory C-CODE.....	91
E.3	Directory AMR-Code.....	91
<b>Annex F (informative):</b>	<b>Bibliography .....</b>	<b>92</b>
History .....		93

## iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN 300 395-2 V1.3.1:2006

<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006>

# Intellectual Property Rights

IPRs essential or potentially essential to the present document may have been declared to ETSI. The information pertaining to these essential IPRs, if any, is publicly available for **ETSI members and non-members**, and can be found in ETSI SR 000 314: "Intellectual Property Rights (IPRs); Essential, or potentially Essential, IPRs notified to ETSI in respect of ETSI standards", which is available from the ETSI Secretariat. Latest updates are available on the ETSI Web server (<http://webapp.etsi.org/IPR/home.asp>).

Pursuant to the ETSI IPR Policy, no investigation, including IPR searches, has been carried out by ETSI. No guarantee can be given as to the existence of other IPRs not referenced in ETSI SR 000 314 (or the updates on the ETSI Web server) which are, or may be, or may become, essential to the present document.

## Foreword

This European Standard (Telecommunications series) has been produced by ETSI Project Terrestrial Trunked Radio (TETRA).

The present document is part 2 of a multi-part deliverable covering speech codec for full-rate traffic channel, as identified below:

Part 1: "General description of speech functions";

**Part 2: "TETRA codec";**

Part 3: "Specific operating features";

Part 4: "Codec conformance testing".

iTeh STANDARD PREVIEW

(standards.iteh.ai)

Clause 4 provides a complete description of the full rate speech source encoder and decoder, whilst clause 5 describes the speech channel encoder and clause 6 the speech channel decoder.

SIST EN 300 395-2 V1.3.1:2006  
<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-119ca146b3d2/sist-en-300-395-2-v1-3-1-2006>

Clause 7 describes the codec performance.

Clause 8 introduces the bit exact description of the codec. This description is given as an ANSI C code, fixed point, bit exact. The whole C code corresponding to the TETRA codec is given in computer files attached to the present document, and are an integral part of this multi-part deliverable.

Clause 9 describes the optional AMR codec.

Clause 10 describes the AMR speech channel encoder.

Clause 11 describes the AMR speech channel decoder.

Clause 12 introduces the AMR speech channel encoder and decoder. This description is given as an ANSI C code.

In addition to these clauses, five informative annexes are provided.

Annex A describes a possible implementation of the speech channel decoding function.

Annex B provides comprehensive indexes of all the routines and files included in the C code associated with the present document.

Annex C describes the actual quality, performance and complexity aspects of the codec.

Annex D reports detailed results from codec characterization listening and complexity tests.

Annex E contains instructions for the use of the attached electronic files.

Annex F lists informative references relevant to the speech codec.

<b>National transposition dates</b>	
Date of adoption of this EN:	21 January 2005
Date of latest announcement of this EN (doa):	30 April 2005
Date of latest publication of new National Standard or endorsement of this EN (dop/e):	31 October 2005
Date of withdrawal of any conflicting National Standard (dow):	31 October 2005

## iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN 300 395-2 V1.3.1:2006

<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65-bb9ca146b3d2/sist-en-300-395-2-v1-3-1-2006>

## 1 Scope

The present document contains the full specification of the speech codecs for use in the Terrestrial Trunked Radio (TETRA) system.

The TETRA codec specified in clauses 4 to 8 is mandatory for all TETRA mobiles and networks. The AMR codec specified in clauses 9 to 12 is optional. If the AMR codec is implemented, the equipment shall conform to the whole of clause 9 to 12.

## 2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication and/or edition number or version number) or non-specific.
- For a specific reference, subsequent revisions do not apply.
- For a non-specific reference, the latest version applies.

Referenced documents which are not found to be publicly available in the expected location might be found at <http://docbox.etsi.org/Reference>.

- [1] ETSI EN 300 392-2: "Terrestrial Trunked Radio (TETRA); Voice plus Data (V+D); Part 2: Air Interface (AI)". **iTeh STANDARD PREVIEW** ([standards.iteh.ai](https://standards.iteh.ai/catalog/standard/sist/en/300-392-2-v1.3.1-2006))
- [2] ETSI TS 126 073: "Universal Mobile Telecommunications System (UMTS); ANSI-C code for the Adaptive Multi Rate speech codec (3GPP TS 26.073 Release 4)".
- [3] ETSI TS 126 074: "Universal Mobile Telecommunications System (UMTS); Mandatory speech codec speech processing functions; AMR speech codec test sequences (3GPP TS 26.074 Release 4)". <https://standards.iteh.ai/catalog/standard/sist/en/300-395-2-v1.3.1-2006/bb9ca146a3d7/sist-en-300-395-2-v1.3.1-2006>
- [4] ETSI TS 126 090: "Universal Mobile Telecommunications System (UMTS); Mandatory Speech Codec speech processing functions AMR Speech Codec - Transcoding functions (3GPP TS 26.090 Release 4)".

## 3 Abbreviations

For the purposes of the present document, the following abbreviations apply:

ACELP	Algebraic CELP
AMR	Adaptive Multi-Rate
ANSI	American National Standards Institute
BER	Bit Error Ratio
BFI	Bad Frame Indicator
BS	Base Station
CELP	Code-Excited Linear Predictive
CRC	Cyclic Redundancy Code
DSP	Digital Signal Processor
DTMF	Dual Tone Multiple Frequency
EP	Error Pattern
EQ	Equalizer test
FIR	Finite Impulse Response
GSM	Global System for Mobile communications
HT	Hilly Terrain
IRS	Intermediate Reference System

LP	Linear Prediction
LPC	Linear Predictive Coding
LSF	Line Spectral Frequency
LSP	Line Spectral Pair
MER	Message Error Rate
MNRU	Multiplicative Noise Reference Unit
MOPS	Million of Operations per Second
MOS	Mean Opinion Score
MS	Mobile Station
MSE	Mean Square Error
PDF	Probability Density Function
PUEM	Probability of Undetected Erroneous Message
RAM	Random Access Memory
RCPC	Rate-Compatible Punctured Convolutional
RF	Radio Frequency
ROM	Read-Only Memory
SCR	Source Controlled Rate
STCH	STealing CHannel
TDM	Time Division Multiplex
TU	Typical Urban
VQ	Vector Quantization
V+D	Voice + Data

## 4 Full rate codec

### iTeh STANDARD PREVIEW

#### 4.1 Structure of the (standards.iteh.ai)

The TETRA speech codec is based on the Code-Excited Linear Predictive (CELP) coding model. In this model, a block of  $N$  speech samples is synthesized by filtering an appropriate innovation sequence from a codebook, scaled by a gain factor  $g_c$ , through two time varying filters. A simplified high level block diagram of this synthesis process, as implemented in the TETRA codec, is shown in figure 1.

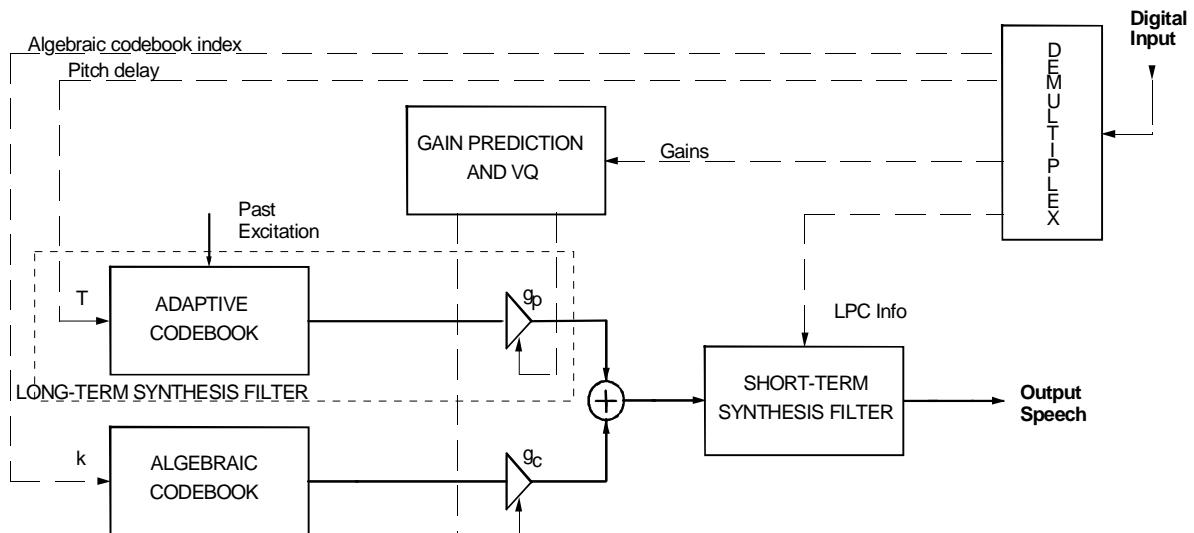


Figure 1: High level block diagram of the TETRA speech synthesizer

The first filter is a long-term prediction filter (pitch filter) aiming at modelling the pseudo-periodicity in the speech signal and the second is a short-term prediction filter modelling the speech spectral envelope.

The long-term or pitch, synthesis filter is given by:

$$\frac{1}{B(z)} = \frac{1}{1 - g_p z^{-T}} \quad (1)$$

where  $T$  is the pitch delay and  $g_p$  is the pitch gain. The pitch synthesis filter is implemented as an adaptive codebook, where for delays less than the sub-frame length the past excitation is repeated.

The short-term synthesis filter is given by:

$$H(z) = \frac{1}{A(z)} = \frac{1}{1 + \sum_{i=1}^p a_i z^{-i}} \quad (2)$$

where  $a_i, i = 1, \dots, p$ , are the Linear Prediction (LP) parameters and  $p$  is the predictor order. In the TETRA codec  $p$  shall be 10.

The TETRA encoder uses an analysis-by-synthesis technique to determine the pitch and excitation codebook parameters. The simplified block diagram of the TETRA encoder is shown in figure 2.

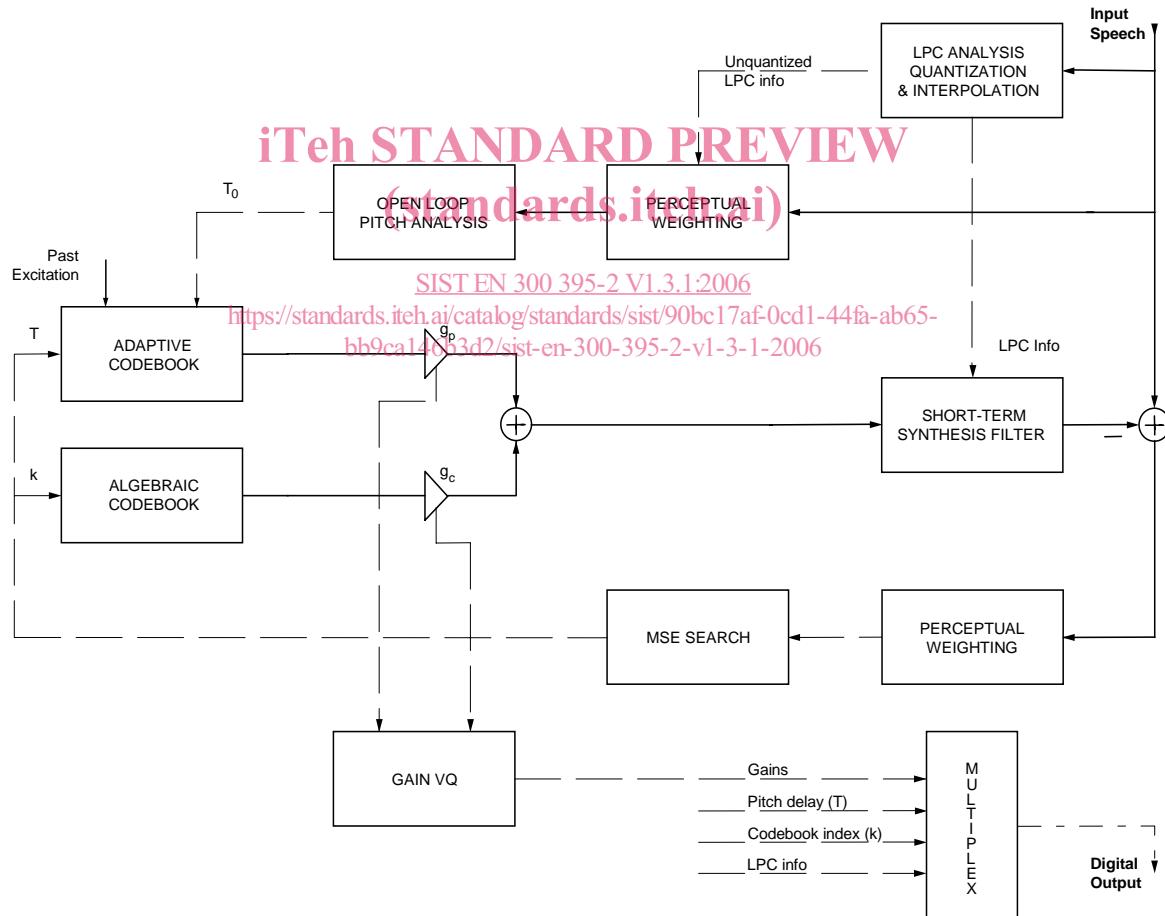


Figure 2: High level block diagram of the TETRA speech encoder

In this analysis-by-synthesis technique, the synthetic speech is computed for all candidate innovation sequences retaining the particular sequence that produces the output closer to the original signal according to a perceptually weighted distortion measure. The perceptual weighting filter de-emphasizes the error at the formant regions of the speech spectrum and is given by:

$$W(z) = \frac{A(z)}{A(z/\gamma)} \quad (3)$$

where  $A(z)$  is the LP inverse filter (as in Equation (2)) and  $0 < \gamma \leq 1$ . The value  $\gamma_1 = 0,85$  shall be used.

Both the weighting filter,  $W(z)$ , and formant synthesis filter,  $H(z)$ , shall use the quantized LP parameters.

In the Algebraic CELP (ACELP) technique, special innovation codebooks having an algebraic structure are used. This algebraic structure has several advantages in terms of storage, search complexity, and robustness. The TETRA codec shall use a specific dynamic algebraic excitation codebook whereby the fixed excitation vectors are shaped by a dynamic shaping matrix (see annex F). The shaping matrix is a function of the LP model  $A(z)$ , and its main role is to shape the excitation vectors in the frequency domain so that their energies are concentrated in the important frequency bands. The shaping matrix used is a Toeplitz lower triangular matrix constructed from the impulse response of the filter:

$$F(z) = \frac{A(z/\gamma_1)}{A(z/\gamma_2)} \quad (4)$$

where  $A(z)$  is the LP inverse filter. The values  $\gamma_1 = 0,75$  and  $\gamma_2 = 0,85$  shall be used.

In the TETRA codec, 30 ms speech frames shall be used. It is required that the short-term prediction parameters (or LP parameters) are computed and transmitted every speech frame. The speech frame shall be divided into 4 sub-frames of 7,5 ms (60 samples). The pitch and algebraic codebook parameters have also to be transmitted every sub-frame.

Table 1 gives the bit allocation for the TETRA codec. 137 bits shall be produced for each frame of 30 ms resulting in a bit rate of 4 567 bit/s.

<https://standards.iteh.ai/catalog/standards/sist/90bc17af-0cd1-44fa-ab65->  
**Table 1: Bit allocation for the TETRA codec**

Parameter	1 <sup>st</sup> subframe	2 <sup>nd</sup> subframe	3 <sup>rd</sup> subframe	4 <sup>th</sup> subframe	Total per frame
LP filter					26
Pitch delay	8	5	5	5	23
Algebraic code	16	16	16	16	64
VQ of 2 gains	6	6	6	6	24
Total					137

More details about the sequence of bits within the speech frame of 137 bits per 30 ms, with reference to the speech parameters, can be found in clause 4.2.2.7, table 3.

## 4.2 Functional description of the codec

### 4.2.1 Pre- and post-processing

Before starting the encoding process, the speech signal shall be pre-processed using the offset compensation filter:

$$H_p(z) = \frac{1}{2} \left( \frac{1 - z^{-1}}{1 - \alpha z^{-1}} \right) \quad (5)$$

where  $\alpha = 32\ 735/32\ 768$ . In the time domain, this filter corresponds to:

$$\overset{\circ}{s}(n) = s(n)/2 - s(n-1)/2 + \alpha \overset{\circ}{s}(n-1) \quad (6)$$

where  $s(n)$  is the input signal and  $s'(n)$  is the pre-processed signal. The purpose of this pre-processing is firstly to remove the dc from the signal (offset compensation), and secondly, to scale down the input signal in order to avoid saturation of the synthesis filtering.

At the decoder, the post-processing consists of scaling up the reconstructed signal (multiplication by 2 with saturation control).

#### 4.2.2 Encoder

Figure 3 presents a detailed block diagram of the TETRA encoder illustrating the major parts of the codec as well as signal flow. On this figure, names appearing at the bottom of the various building blocks correspond to the C code routines associated with the present document.

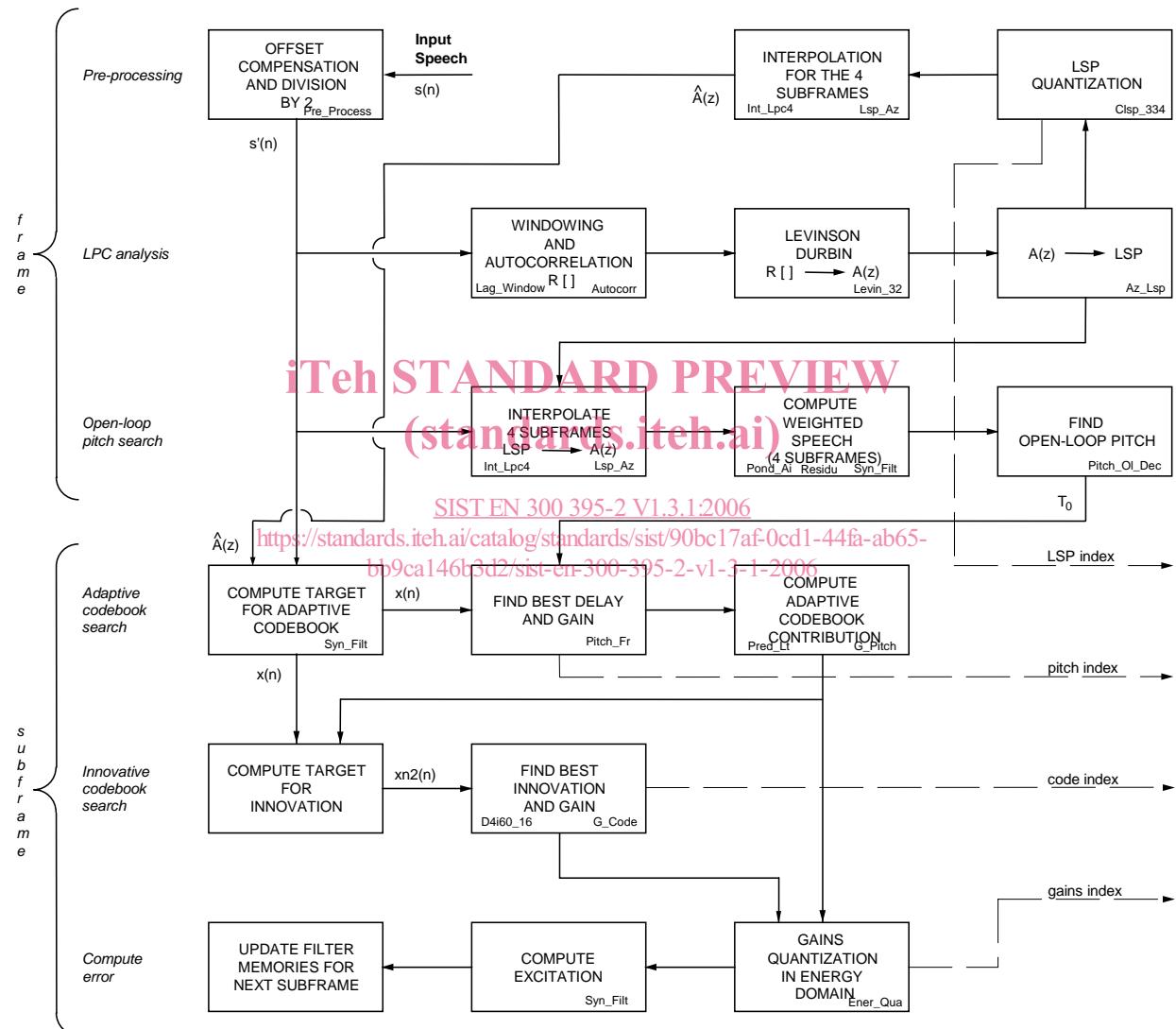


Figure 3: Signal flow at the encoder