

Edition 4.0 2006-05

INTERNATIONAL STANDARD





THIS PUBLICATION IS COPYRIGHT PROTECTED

Copyright © 2006 IEC, Geneva, Switzerland

All rights reserved. Unless otherwise specified, no part of this publication may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm, without permission in writing from either IEC or IEC's member National Committee in the country of the requester.

If you have any questions about IEC copyright or have an enquiry about obtaining additional rights to this publication, please contact the address below or your local IEC member National Committee for further information.

Droits de reproduction réservés. Sauf indication contraire, aucune partie de cette publication ne peut être reproduite ni utilisée sous quelque forme que ce soit et par aucun procédé, électronique ou mécanique, y compris la photocopie et les microfilms, sans l'accord écrit de la CEI ou du Comité national de la CEI du pays du demandeur.

Si vous avez des questions sur le copyright de la CEI ou si vous désirez obtenir des droits supplémentaires sur cette publication, utilisez les coordonnées ci-après ou contactez le Comité national de la CEI de votre pays de résidence.

IEC Central Office 3, rue de Varembé CH-1211 Geneva 20 Switzerland Email: inmail@iec.ch

Web: www.iec.ch

About the IEC

The International Electrotechnical Commission (IEC) is the leading global organization that prepares and publishes International Standards for all electrical, electronic and related technologies.

About IEC publications

The technical content of IEC publications is kept under constant review by the IEC. Please make sure that you have the latest edition, a corrigenda or an amendment might have been published.

Catalogue of IEC publications: www.iec.ch/searchpub

The IEC on-line Catalogue enables you to search by a variety of criteria (reference number, text, technical committee,...). It also gives information on projects, withdrawn and replaced publications.

IEC Just Published: www.iec.ch/online_news/justpub/

Stay up to date on all new IEC publications. Just Published details twice a month all new publications released. Available on-line and also by email.

■ Electropedia: <u>www.electropedia.org</u>

The world's leading online dictionary of electronic and electrical terms containing more than 20 000 terms and definitions in English and French, with equivalent terms in additional languages. Also known as the International Electrotechnical Vocabulary online.

Customer Service Centre: www.iec.ch/webstore/custserv

If you wish to give us your feedback on this publication or need further assistance, please visit the Customer Service Centre FAQ or contact us:

Email: csc@iec.ch Tel.: +41 22 919 02 11 Fax: +41 22 919 03 00

A propos de la CEI

La Commission Electrotechnique Internationale (CEI) est la première organisation mondiale qui élabore et publie des normes internationales pour tout ce qui a trait à l'électricité, à l'électronique et aux technologies apparentées.

A propos des publications CEI

Le contenu technique des publications de la CEI est constamment revu. Veuillez vous assurer que vous possédez l'édition la plus récente, un corrigendum ou amendement peut avoir été publié.

■ Catalogue des publications de la CEI: <u>www.iec.ch/searchpub/cur_fut-f.htm</u>

Le Catalogue en-ligne de la CEI vous permet d'effectuer des recherches en utilisant différents critères (numéro de référence, texte, comité d'études,...). Il donne aussi des informations sur les projets et les publications retirées ou remplacées.

Just Published CEI: www.iec.ch/online_news/justpub

Restez informé sur les nouvelles publications de la CEI. Just Published détaille deux fois par mois les nouvelles publications parues. Disponible en-ligne et aussi par email.

■ Electropedia: <u>www.electropedia.org</u>

Le premier dictionnaire en ligne au monde de termes électroniques et électriques. Il contient plus de 20 000 termes et définitions en anglais et en français, ainsi que les termes équivalents dans les langues additionnelles. Egalement appelé Vocabulaire Electrotechnique International en ligne.

Service Clients: www.iec.ch/webstore/custserv/custserv_entry-f.htm

Si vous désirez nous donner des commentaires sur cette publication ou si vous avez des questions, visitez le FAQ du Service clients ou contactez-nous:

Email: csc@iec.ch Tél.: +41 22 919 02 11 Fax: +41 22 919 03 00



Edition 4.0 2006-05

INTERNATIONAL STANDARD



Low-voltage switchgear and controlgear -

Part 2: Circuit-breakers

Appareillage à basse tension -

Partie 2: Disjoncteurs

<u>0947-2:2006</u>

eview

INTERNATIONAL ELECTROTECHNICAL COMMISSION

COMMISSION ELECTROTECHNIQUE INTERNATIONALE

PRICE CODE CODE PRIX

ICS 29.130.20 ISBN 2-8318-8649-X

CONTENTS

FO	REWORD	7
1	General	۵
•		
	1.1 Scope and object	
2	Definitions	
	Classification	
3		
4	Characteristics of circuit-breakers	16
	4.1 Summary of characteristics	16
	4.2 Type of circuit-breaker	· 17
	4.3 Rated and limiting values of the main circuit. 4.4 Utilization categories 4.5 Control circuits	1/
	4.4 Utilization categories	
	4.5 Control circuits	
	4.6 Auxiliary circuits	
	4.7 Releases	
5	Product information	∠ა
5	Product information	23
	5.1 Nature of the information	23
		24
6	5.3 Instructions for installation, operation and maintenance	25
6	Normal service, mounting and transport conditions	
7	Constructional and performance requirements	
	7.1 Constructional requirements	25
	7.2 Performance requirements	
_	7.3 Electromagnetic compatibility (EMC)	
8	Tests	
	8.1 Kind of tests	
	8.2 Compliance with constructional requirements	
	8.3 Type tests	
	8.4 Routine tests	61
Λnı	ex A (normative) Co-ordination under short-circuit conditions between a circuit-	
	aker and another short-circuit protective device associated in the same circuit	66
Anı	ex B (normative) Circuit-breakers incorporating residual current protection	75
Anı	ex C (normative) Individual pole short-circuit test sequence	107
Anı	ex D Vacant	108
Anı	ex E (informative) Items subject to agreement between manufacturer and user	109
	ex F (normative) Additional tests for circuit-breakers with electronic over-current	
	ection	110
Anı	ex G (normative) Power loss	140
Anı	ex H (normative) Test sequence for circuit-breakers for IT systems	143

	cuit-breakers	
•	native) Glossary of symbols related to products covered by this standard.	159
	ative) Circuit-breakers not fulfilling the requirements for overcurrent	161
	ative) Modular residual current devices (without integral current	166
	ative) Electromagnetic compatibility (EMC) – Additional requirements ds for devices not covered by Annexes B, F and M	211
Annex O Insta	ntaneous trip circuit-breakers (ICB)	215
Bibliography		218
0 1 3		
Figure 1 – Test	arrangement (connecting cables not shown) for short-circuit tests	65
Figure A.1 – Ov protection by a	ver-current co-ordination between a circuit-breaker and a fuse or back-up fuse: operating characteristics	71
Figure A.2	Figure A.3	72
Total discrimina	ation between two circuit-breakers	72
Figure A.4		73
Back-up protect	tion by a circuit-breaker – Operating characteristics	73
Figure A.6 – Ex showing cable of	ample of test circuit for conditional short-circuit breaking capacity tests connections for a 3-pole circuit-breaker (C1)	74
Figure B.1 – Te	st circuit for the verification of the operating characteristic (see B.8.2)	99
Figure B.2 – Te current under or	st circuit for the verification of the limiting value of the non-operating ver-current conditions (see B.8.5)	100
Figure B.3 – Te B.3.1.2.2 (see E	st circuit for the verification of the behaviour of CBRs classified under)94701 ² 101
Figure B.4 – Cu	ırrent ring wave 0,5 μs/100 kHz	102
Figure B.5 – Ex	ample of test circuit for the verification of resistance to unwanted	
tripping		
	rge current wave 8/20 μs	103
	st circuit for the verification of resistance to unwanted tripping in case of at follow-on current (B.8.6.2)	104
	st circuit for the verification of the correct operation of CBRs, in the case ating direct currents (see B.8.7.2.1, B.8.7.2.2 and B.8.7.2.3)	105
of a residual pu	st circuit for the verification of the correct operation of CBRs, in the case Isating direct current superimposed by a smooth direct residual current	106
	presentation of test current produced by back-to-back thyristors in produced by back-to-back thyristors in	119
	st circuit for immunity and emission tests in accordance with F.4.1.3, 4.6, F.4.7.1, F.5.4 and F.6.2 – Two phase poles in series	120
	st circuit for immunity and emission tests in accordance with F.4.1.3, 4.6, F.4.7.1, F.5.4 and F.6.2 – Three phase poles in series	121

F.4.2, F.4.3, F.4.6, F.4.7.1, F.5.4 and F.6.2 – Three-phase connection	122
Figure F.5 – Test current for the verification of the influence of the current dips and interruptions in accordance with F.4.7.1	123
Figure F.6 – Circuit for electrical fast transients/bursts (EFT/B) immunity test in accordance with F.4.4 – Two phase poles in series	124
Figure F.7 – Circuit for electrical fast transients/bursts (EFT/B) immunity test in accordance with F.4.4 – Three phase poles in series	125
Figure F.8 – Circuit for electrical fast transients/bursts (EFT/B) immunity test in accordance with F.4.4 – Three-phase connection	126
Figure F.9 – Test circuit for the verification of the influence of surges in the main circuit (line-to-earth) in accordance with F.4.5 – Two phase poles in series	127
Figure F.10 – Test circuit for the verification of the influence of surges in the main circuit (line-to-earth) in accordance with F.4.5 – Three phase poles in series	128
Figure F.11 – Test circuit for the verification of the influence of surges in the main circuit (line-to-earth) in accordance with F.4.5 – Three-phase connection	129
Figure F.12 – Test circuit for the verification of the influence of current surges in the main circuit in accordance with F.4.5 – Two phase poles in series	130
Figure F.13 – Test circuit for the verification of the influence of current surges in the main circuit in accordance with F.4.5 – Three phase poles in series	130
Figure F.14 – Test circuit for the verification of the influence of current surges in the main circuit in accordance with F.4.5 – Three-phase connection	131
Figure F.15 – Temperature variation cycles at a specified rate of change in accordance	
with F.9.1	131
with F.9.1	132
with F.9.1 Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f.	132
Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast	132 133
with F.9.1	132 133 134 135
with F.9.1 Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines	132 133 134 135 136
Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines Figure F.20 – General test set-up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode) Figure F.21 – Arrangement of connections for the verification of immunity to conducted	132 133 134 135 136
Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines Figure F.20 – General test set-up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode) Figure F.21 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Two phase poles in series configuration Figure F.22 – Arrangement of connections for the verification of immunity to conducted	132 133 134 135 136 137
Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines Figure F.20 – General test set-up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode) Figure F.21 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Two phase poles in series configuration Figure F.22 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration Figure F.23 – Arrangement of connections for the verification of immunity to conducted	132 133 134 135 136 137 138
Figure F.16 – General test set up for immunity tests Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines Figure F.20 – General test set up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode) Figure F.21 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Two phase poles in series configuration Figure F.22 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields – Three-phase configuration	132 133 134 135 136 137 138 139
Figure F.16 – General test set up for immunity tests. Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields. Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines. Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines. Figure F.20 – General test set-up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode). Figure F.21 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Two phase poles in series configuration. Figure F.22 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration. Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase configuration. Figure G.1 – Example of power loss measurement according to G.2.1	132133134135136137138139142142
Figure F.16 – General test set up for immunity tests. Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines Figure F.20 – General test set-up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode) Figure F.21 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Two phase poles in series configuration. Figure F.22 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration. Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration. Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields – Three-phase configuration. Figure G.1 – Example of power loss measurement according to G.2.2 and G.2.3.	132133134135136137138139142142153
Figure F.16 – General test set up for immunity tests. Figure F.17 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields Figure F.18 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines Figure F.19 – Test set up for verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines Figure F.20 – General test set up for the verification of immunity to conducted disturbances induced by r.f. fields (common mode) Figure F.21 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Two phase poles in series configuration. Figure F.22 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase poles in series configuration. Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three phase configuration. Figure F.23 – Arrangement of connections for the verification of immunity to conducted disturbances induced by r.f. fields - Three-phase configuration. Figure G.1 – Example of power loss measurement according to G.2.1 and G.2.2 and G.2.3. Figure G.2 – Example of power loss measurement according to G.2.2 and G.2.3.	132133134135136137138139142142153154

Figure J.4 – Test set up for the verification of immunity to radiated r.f. electromagnetic fields	156
Figure J.5 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on power lines	157
Figure J.6 – Test set up for the verification of immunity to electrical fast transients/bursts (EFT/B) on signal lines	158
Figure K.1 – Relationship between symbols and tripping characteristics	160
Figure M.1 – Test circuits for the verification of operation in the case of a steady increase of residual current	190
Figure M.2 – Test circuits for the verification of operation in the case of a sudden appearance of residual current (with breaking device)	191
Figure M.3 – Test circuits for the verification of operation in the case of a sudden appearance of residual current (without breaking device)	192
Figure M.4 – Test circuits for the verification of the limiting value of non-operating current under overcurrent conditions	193
Figure M.5 – Test circuits for the verification of the resistance to unwanted tripping in the case of loading of the network capacitance	194
Figure M.6 – Test circuit for the verification of the resistance to unwanted tripping in the case of flashover without follow-on current	195
Figure M.7 – Test circuits for the verification of operation in the case of a continuous rise of a residual pulsating direct current	196
Figure M.8 – Test circuits for the verification of operation in the case of a sudden appearance of residual pulsating direct current (without breaking device)	197
Figure M.9 – Test circuits for the verification of operation in the case of a sudden appearance of residual pulsating direct current (with breaking device)	198
Figure M.10 – Test circuits for the verification of operation in the case of a residual pulsating direct current superimposed by smooth direct current of 6 mA	199
Figure M.11 – Test circuits for the verification of operation in the case of a slowly rising residual smooth direct current	200
Figure M.12 – Test circuits for the verification of operation in the case of a sudden appearance of residual smooth direct current (without breaking device)	201
Figure M.13 – Test circuits for the verification of operation in the case of a sudden appearance of residual smooth direct current (with breaking device)	202
Figure M.14 Test circuits for the verification of operation in the case of a slowly rising residual current resulting from a fault in a circuit fed by a three-pulse star or a six-pulse bridge connection	203
Figure M.15 – Test circuits for the verification of operation in the case of a slowly rising residual current resulting from a fault in a circuit fed by a two-pulse bridge connection line-to-line	204
Figure M.16 – Test circuit for the verification of the behaviour of MRCDs with separate sensing means in the case of a failure of the sensor means connection	205
Figure M.17 – Test circuit for the verification of the behaviour of MRCD with separate sensing means under short-circuit conditions	
Figure M.18 – Test circuit for the verification of the behaviour of MRCD with integral sensing means under short-circuit conditions	207

Figure M.19 – Test circuit for the verification of the behaviour of terminal type MRCD under short-circuit conditions	208
Figure M.20 – Verification of immunity to radiated r.f. electromagnetic fields - Test set-up for MRCD with separate sensing means (additional to the test of Annex B)	209
Figure M.21 – Verification of immunity to electrical fast transients/bursts (EFT/B) on the sensing means connection of an MRCD with separate sensing means (additional to the test of Annex B)	210
Figure M.22 – Verification of immunity to conducted disturbances induced by r.f. fields - Test set up for MRCD with separate sensing means (additional to the test of Annex B)	210
Table 1 – Standard ratios between I _{CS} and I _{CU}	19
Table 2 – Ratio <i>n</i> between short-circuit making capacity and short-circuit beaking capacity and related power factor (for a.c. circuit-breakers)	20
Table 3 – Minimum values of rated short-time withstand current	20
Table 4 – Utilization categories	21
Table 5 – Preferred values of the rated control supply voltage, if different from that of the main circuit	21
Table 6 – Characteristics of the opening operation of inverse time-delay over-current opening releases at the reference temperature	29
Table 7 – Temperature-rise limits for terminals and accessible parts	31
Table 8 – Number of operating cycles	32
Table 9 – Overall schema of test sequences a	36
Table 9a – Applicability of test sequences according to the relationship between I_{CS} , I_{CU} and I_{CW}	37
Table 10 – Number of samples for test	40
Table 11 – Values of power factors and time constants corresponding to test currents	42
Table 12 – Test circuit characteristics for overload performance	
Table B.1 – Operating characteristic for non-time-delay type	947-20 ²⁰
Table B.2 – Operating characteristic for time-delay-type having a limiting non-actuating time of 0,06 s	
Table B.3 – Requirements for CBRs functionally dependent on line voltage	85
Table B 4 – Additional test sequences	88
Table B.5 – Tripping current range for CBRs in case of an earth fault comprising a d.c. component	93
Table F.1 – Test parameters for current dips and interruptions	115
Table J.1 – EMC – Immunity tests	147
Table J.2 – Reference data for immunity test specifications	148
Table J.3 – EMC – Emission tests	151
Table J.4 – Reference data for emission test specifications	151
Table M.1 – Product information	
Table M.2 – Requirements for MRCDs with voltage source	174
Table M.3 – Test sequences	176

INTERNATIONAL ELECTROTECHNICAL COMMISSION

LOW-VOLTAGE SWITCHGEAR AND CONTROLGEAR -

Part 2: Circuit-breakers

FOREWORD

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (Precediter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.
- 5) IEC provides no marking procedure to indicate its approval and cannot be rendered responsible for any equipment declared to be in conformity with an IEC Publication.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

International Standard IEC 60947-2 has been prepared by subcommittee 17B: Low-voltage switchgear and controlgear, of IEC technical committee 17: Switchgear and controlgear.

This fourth edition of IEC 60947-2 cancels and replaces the third edition published in 2003.

The main changes introduced in this new edition are an amendment to the verification of dielectric properties, the improvement of EMC clauses in Annexes B, F, J and M, and the addition of a new Annex O regarding instantaneous trip circuit-breakers.

The text of this standard is based on the third edition and the following documents:

FDIS	Report on voting
17B/1455/FDIS	17B/1463/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

The IEC 60947 series comprises the following parts under the general title *Low-voltage* switchgear and controlgear:

Part 1: General rules

Part 2: Circuit-breakers

Part 3: Switches, disconnectors, switch-disconnectors and fuse-combination units

Part 4: Contactors and motor-starters

Part 5: Control circuit devices and switching elements

Part 6: Multiple function equipment

Part 7: Ancillary equipment

Part 8: Control units for built-in thermal protection (PTC)/for rotating electrical machines

The committee has decided that the contents of this publication will remain unchanged until the maintenance result date indicated on the EC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

reconfirmed;

· withdrawn:

replaced by a revised edition, or

amended.

LOW-VOLTAGE SWITCHGEAR AND CONTROLGEAR -

Part 2: Circuit-breakers

1 General

The provisions of the general rules dealt with in IEC 60947-1 are applicable to this standard, where specifically called for. Clauses and subclauses, tables, figures and annexes of the general rules thus applicable are identified by reference to IEC 60947-1, for example, 1.2.3 of IEC 60947-1, Table 4 of IEC 60947-1, or Annex A of IEC 60947-1.

1.1 Scope and object

This standard applies to circuit-breakers, the main contacts of which are intended to be connected to circuits, the rated voltage of which does not exceed 1 000 V a.c. or 1 500 V d.c.; it also contains additional requirements for integrally fused circuit-breakers.

It applies whatever the rated currents, the method of construction or the proposed applications of the circuit-breakers may be.

The requirements for circuit-breakers which are also intended to provide earth-leakage protection are contained in Annex B.

The additional requirements for circuit-breakers with electronic over-current protection are contained in Annex F.

The additional requirements for circuit-breakers for IT systems are contained in Annex H.

The requirements and test methods for electromagnetic compatibility of circuit-breakers are 2006 contained in Annex J.

The requirements for circuit-breakers not fulfilling the requirements for over-current protection are contained in Annex L.

The requirements for modular residual current devices (without integral current breaking device) are contained in Annex M.

The requirements and test methods for electromagnetic compatibility of circuit-breaker auxiliaries are contained in Annex N.

Supplementary requirements for circuit-breakers used as direct-on-line starters are given in IEC 60947-4-1, applicable to low-voltage contactors and starters.

The requirements for circuit-breakers for the protection of wiring installations in buildings and similar applications, and designed for use by uninstructed persons, are contained in IEC 60898.

The requirements for circuit-breakers for equipment (for example electrical appliances) are contained in IEC 60934.

For certain specific applications (for example traction, rolling mills, marine service) particular or additional requirements may be necessary.

NOTE Circuit-breakers which are dealt with in this standard may be provided with devices for automatic opening under predetermined conditions other than those of over-current and undervoltage as, for example, reversal of power or current. This standard does not deal with the verification of operation under such pre-determined conditions.

The object of this standard is to state:

- a) the characteristics of circuit-breakers;
- b) the conditions with which circuit-breakers shall comply with reference to:
 - 1) operation and behaviour in normal service;
 - 2) operation and behaviour in case of overload and operation and behaviour in case of short-circuit, including co-ordination in service (discrimination and back-up protection);
 - 3) dielectric properties;
- c) tests intended for confirming that these conditions have been met and the methods to be adopted for these tests;
- d) information to be marked on or given with the apparatus

1.2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60050(441):1984, International Electrotechnical Vocabulary (IEV) – Chapter 441: Switchgear, controlgear and fuses
Amendment 1 (2000)

IEC 60051 (all parts) Direct acting indicating analogue electrical measuring instruments and their accessories

IEC 60068-2-14:1984, Environmental testing – Part 2: Tests. Test N: Change of temperature Amendment 1 (1986)

IEC 60068-2-30:2005, Environmental testing – Part 2-30: Tests – Test Db: Damp heat, cyclic (12 h + 12 h cycle)

IEC 60364 (all parts), Electric installations of buildings

IEC 60364-4-41:2001, Electrical installations of buildings – Part 4-41: Protection for safety – Protection against shock

IEC 60695-2-10:2000, Fire hazard testing – Part 2-10: Glowing/hot-wire based test methods – Glow-wire apparatus and common test procedure

IEC 60695-2-11:2000, Fire hazard testing – Part 2-11: Glowing/hot-wire based test methods – Glow-wire flammability test method for end-products

IEC 60695-2-12:2000, Fire hazard testing – Part 2-12: Glowing/hot-wire based test methods – Glow-wire flammability test method for materials

IEC 60695-2-13:2000, Fire hazard testing – Part 2-13: Glowing/hot-wire based test methods – Glow-wire ignitability test method for materials

IEC 60755:1983, General requirements for residual current operated protective devices Amendment 1 (1988)
Amendment 2 (1992)

IEC 60898, Circuit-breakers for over-current protection for household and similar installations

IEC 60934, Circuit-breakers for equipment (CBE)

IEC 60947-1:2004, Low-voltage switchgear and controlgear – Part 1: General rules

IEC 60947-4-1:2000, Low-voltage switchgear and controlgear – Part 4-1: Contactors and motor-starters – Electromechanical contactors and motor-starters
Amendment 1 (2002)

IEC 61000-3-2:2000, Electromagnetic compatibility (EMC) – Part 3-2: Limits – Limits for harmonic current emissions (equipment input current \leq 16 A per phase)

Amendment 1 (2001) Amendment 2 (2004)

Amendment 2 (2004)

IEC 61000-3-3:1994, Electromagnetic compatibility (EMC) — Part 3: Limits — Section 3: Limitation of voltage fluctuations and flicker in low-voltage supply systems for equipment with rated current \leq 16 A

Amendment 1 (2001)

IEC 61000-4-2:1995, Electromagnetic compatibility(EMC) - Part 4: Testing and measurement techniques – Section 2: Electrostatic discharge immunity test

Amendment 1 (1998)

Amendment 2 (2000)

IEC 61000-4-3:2002, Electromagnetic compatibility (EMC) – Part 4-3: Testing and measurement techniques – Radiated radio-frequency electromagnetic field immunity test Amendment 1 (2002)

IEC 61000-4-4:1995, Electromagnetic compatibility (EMC) – Part 4: Testing and measurement techniques – Section 4: Electrical fast transient/burst immunity test

Amendment 1 (2000)

Amendment 2 (2001)

IEC 61000-4-5:1995, Electromagnetic compatibility(EMC) – Part 4: Testing and measurement techniques – Section 5: Surge immunity test

Amendment 1 (2000)

IEC 61000-4-6:2003, Electromagnetic compatibility (EMC) – Part 4-6: Testing and measurement techniques – Immunity to conducted disturbances, induced by radiofrequency fields

Amendment 1 (2004)

IEC 61000-4-11:2004. Electromagnetic compatibility (EMC) – Part 4-11: Testing and measurement techniques – Voltage dips, short interruptions and voltage variations immunity tests

IEC 61000-4-13:2002, Electromagnetic compatibility (EMC) – Part 4-13: Testing and measurement techniques – Harmonics and interharmonics including mains signalling at a.c. power port, low frequency immunity tests

IEC 61000-5-2:1997, Electromagnetic compatibility (EMC) – Part 5: Installation and mitigation guidelines – Section 2: Earthing and cabling

IEC 61008-1:1996, Residual current operated circuit-breakers without integral over-current protection for household and similar uses (RCCBs) – Part 1: General rules Amendment 1 (2002)

IEC 61009-1:1996, Residual current operated circuit-breakers with integral over-current protection for household and similar uses (RCBOs) – Part 1: General rules Amendment 1 (2002)

CISPR 11:2003, Industrial, scientific and medical (ISM) radio-frequency equipment – Electromagnetic disturbance characteristics – Limits and methods of measurement Amendment 1 (2004)

CISPR 22:2005, Information technology equipment – Radio disturbance characteristics – Limits and methods of measurement Amendment 1 (2005)

2 Definitions

For the majority of the definitions required in connection with this standard, see Clause 2 of IEC 60947-1.

For the purpose of this standard, the following additional definitions shall apply:

NOTE Where these definitions are taken unchanged from the *International Electrotechnical Vocabulary (IEV)*, IEC 60050(441), the IEV reference is given in brackets.

2.1

circuit-breaker

a mechanical switching device, capable of making, carrying and breaking currents under normal circuit conditions and also making, carrying for a specified time and breaking currents under specified abnormal circuit conditions such as those of short-circuit

[IEV 441-14-20]

2.1.1

frame size

a term designating a group of circuit-breakers, the external physical dimensions of which are common to a range of current ratings. Frame size is expressed in amperes corresponding to the highest current rating of the group. Within a frame size, the width may vary according to the number of poles

NOTE This definition does not imply dimensional standardization.

2.1.2

construction break

a significant difference in construction between circuit-breakers of a given frame size, requiring additional type testing (see 7.1.5)

2.2

integrally fused circuit-breaker

a combination, in a single device, of a circuit-breaker and fuses, one fuse being placed in series with each pole of the circuit-breaker intended to be connected to a phase conductor

[IEV 441-14-22]

2.3

current-limiting circuit-breaker

a circuit-breaker with a break-time short enough to prevent the short-circuit current reaching its otherwise attainable peak value

[IEV 441-14-21]