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INTERNATIONAL STANDARD

NORME INTERNATIONALE

Integrated circuits – Measurement of electromagnetic immunity –
Part 2: Measurement of radiated immunity – TEM cell and wideband TEM cell method

Circuits intégrés – Mesure de l'immunité electromagnétique –
Partie 2: Mesure de l'immunité rayonnée – Méthode de cellule TEM et cellule TEM à large bande

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INTEGRATED CIRCUITS – MEASUREMENT OF ELECTROMAGNETIC IMMUNITY –

Part 2: Measurement of radiated immunity – TEM cell and wideband TEM cell method

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The text of this standard is based on the following documents:

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47A/838/FDIS	47A/843/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

This part of IEC 62132 is to be read in conjunction with IEC 62132-1.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

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INTEGRATED CIRCUITS – MEASUREMENT OF ELECTROMAGNETIC IMMUNITY –

Part 2: Measurement of radiated immunity – TEM cell and wideband TEM cell method

1 Scope

This International Standard specifies a method for measuring the immunity of an integrated circuit (IC) to radio frequency (RF) radiated electromagnetic disturbances. The frequency range of this method is from 150 kHz to 1 GHz, or as limited by the characteristics of the TEM cell.

2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60050-131:2002, *International Electrotechnical Vocabulary (IEV) – Part 131: Circuit theory*

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IEC 60050-161:1990, *International Electrotechnical Vocabulary (IEV) – Chapter 161: Electromagnetic compatibility*

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IEC 61967-2, *Integrated circuits – Measurement of electromagnetic emissions, 150 kHz to 1 GHz – Part 2: Measurement of radiated emissions – TEM cell and wideband TEM cell method*

IEC 62132-1:2006, *Integrated circuits – Measurement of electromagnetic immunity, 150 kHz to 1 GHz – Part 1: General conditions and definitions*

3 Terms and definitions

For the purpose of this document, the definitions in IEC 62132-1, IEC 60050-131 and IEC 60050-161, as well as the following, apply.

3.1
transverse electromagnetic mode (TEM)
waveguide mode in which the components of the electric and magnetic fields in the propagation direction are much less than the primary field components across any transverse cross-section

3.2
TEM waveguide
open or closed transmission line system, in which a wave is propagating in the transverse electromagnetic mode to produce a specified field for testing purposes.

3.3
TEM cell
enclosed TEM waveguide, often a rectangular coaxial line, in which a wave is propagated in the transverse electromagnetic mode to produce a specified field for testing purposes. The outer conductor completely encloses the inner conductor

3.4
two-port TEM waveguide
TEM waveguide with input/output measurement ports at both ends

3.5
one-port TEM waveguide
TEM waveguide with a single input/output measurement port

NOTE Such TEM waveguides typically feature a broadband line termination at the non-measurement-port end.

3.6
characteristic impedance
for any constant phase wave-front, the magnitude of the ratio of the voltage between the inner conductor and the outer conductor to the current on either conductor

NOTE The characteristic impedance is independent of the voltage/current magnitudes and depends only on the cross-sectional geometry of the transmission line. TEM waveguides are typically designed to have a 50 Ω characteristic impedance. TEM waveguides with a 100 Ω characteristic impedance are often used for transient testing.

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3.7
anechoic material
material that exhibits the property of absorbing, or otherwise reducing, the level of electromagnetic energy reflected from that material

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3.8
broadband line termination
termination which combines a low-frequency discrete-component load, to match the characteristic impedance of the TEM waveguides (typically 50 Ω), and a high-frequency anechoic-material volume

3.9
primary (field) component
electric field component aligned with the intended test polarization

NOTE For example, in conventional two-port TEM cells, the septum is parallel to the horizontal floor, and the primary mode electric field vector is vertical at the transverse centre of the TEM cell.

3.10
secondary (field) component
in a Cartesian coordinate system, either of the two electric field components orthogonal to the primary field component and orthogonal to each other

4 General

The IC to be evaluated for EMC performance is referred to as the device under test (DUT). The DUT shall be mounted on a printed circuit board (PCB), referred to as the EMC test board. The EMC test board is provided with the appropriate measurement or monitoring points at which the DUT response parameters can be measured.

The EMC test board is clamped to a mating port (referred to as a wall port) cut in the top or bottom of a transverse electromagnetic mode (TEM) cell. Either a two-port TEM cell or a one-port TEM cell may be used. Within this standard, a two-port TEM cell is referred to as a TEM cell while a one-port TEM cell is referred to as a wideband (Gigahertz) TEM, or GTEM, cell.

The test board is not positioned inside the cell, as in the conventional usage, but becomes a part of the cell wall. This method is applicable to any TEM or GTEM cell modified to incorporate the wall port; however, the measured response of the DUT will be affected by many factors. The primary factor affecting the DUT's response is the septum to EMC test board (cell wall) spacing.

NOTE 1 This procedure was developed using a 1 GHz TEM cell with a septum to housing spacing of 45 mm and a GTEM cell with a septum to housing spacing of 45 mm at the centre of the wall port.

The EMC test board controls the geometry and orientation of the DUT relative to the cell and eliminates any connecting leads within the cell (these are on the backside of the board, which is outside the cell). For the TEM cell, one of the 50 Ω ports is terminated with a 50 Ω load. The other 50 Ω port for a TEM cell, or the single 50 Ω port for a GTEM cell, is connected to the output of an RF disturbance generator. The injected CW disturbance signal exposes the DUT to a plane wave electromagnetic field where the electric field component is determined by the injected voltage and the distance between the DUT and the septum of the cell. The relationship is given by

$$E = V/h$$

where

E is the field strength (V/m) within the cell;

V is the applied voltage (V) across the 50 Ω load; and

h is the height (m) between the septum and the centre of the IC package.

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Rotating the EMC test board in the four possible orientations in the wall port of the TEM or GTEM cell is required to determine the sensitivity of the DUT to induced magnetic fields. Dependent upon the DUT, the response parameters of the DUT may vary (e.g. a change of current consumption, deterioration in function performance, waveform jitter, etc.) The intent of this test method is to provide a quantitative measure of the RF immunity of ICs for comparison or other purposes.

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NOTE 2 Additional information on the use and characterization of TEM cells for radiated immunity testing can be found in IEC 61000-4-20.

5 Test conditions

The test conditions shall meet the requirements as described in IEC 62132-1.

6 Test equipment

6.1 General

The test equipment shall meet the requirements as described in IEC 62132-1. In addition, the following test equipment requirements shall apply.

6.2 Cables

Double shielded or semi-rigid coaxial cable may be required depending on the local RF ambient conditions.

6.3 RF disturbance source

The RF disturbance source may comprise an RF signal generator with a modulation function, an RF power amplifier, and an optional variable attenuator. The gain (or attenuation) of the RF disturbance generating equipment, without the TEM or GTEM cell, shall be known with a tolerance of ±0,5 dB.

6.4 TEM cell

The TEM cell used for this test procedure is a two-port TEM waveguide and shall be fitted with a wall port sized to mate with the EMC test board. The TEM cell shall not exhibit higher order modes over the frequency range being measured. For this procedure, the recommended TEM cell frequency range is 150 kHz to the frequency of the first resonance of the lowest higher order mode (typically <2 GHz). The frequency range being evaluated shall be covered using only a single cell.

The VSWR of the TEM cell over the frequency range being measured shall be less than 1,5. However, due to the potential for error when calculating the applied E-field, a TEM cell with a VSWR of less than 1,2 is preferred. A TEM cell with a VSWR less than 1,2 does not require field strength characterization. A TEM cell with a VSWR larger than or equal to 1,2 but less than 1,5 shall be characterized in accordance with the procedure in Annex A. The raw TEM cell VSWR data (over the frequency range of the measurement) shall be included in the test report. Measurement results obtained from a TEM cell with a VSWR of less than 1,2 will prevail over data taken from a TEM cell with a higher VSWR.

6.5 Gigahertz TEM cell

The Gigahertz, or wideband, TEM (GTEM) cell used for this test procedure is a one-port TEM waveguide and shall be fitted with a wall port sized to mate with the EMC test board. The GTEM cell shall not exhibit higher order modes over the frequency range being measured. For this procedure, the recommended GTEM cell frequency range is from 150 kHz to the frequency of the first resonance of the lowest higher order mode (typically >2 GHz). The frequency range being evaluated shall be covered using a single cell.

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The VSWR of the GTEM cell over the frequency range being measured shall be less than 1,5. However, due to the potential for error when calculating the applied E-field, a GTEM cell with a VSWR of less than 1,2 is preferred. A GTEM cell with a VSWR less than 1,2 does not require field strength characterization. A GTEM cell with a VSWR larger than or equal to 1,2 but less than 1,5 shall be characterized in accordance with the procedure in Annex A. The raw GTEM cell VSWR data (over the frequency range of the measurement) shall be included in the test report. Measurement results obtained from a GTEM cell with a VSWR of less than 1,2 will prevail over data taken from a GTEM cell with a higher VSWR.

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6.6 50 Ω termination

A 50 Ω termination with a VSWR less than 1,1 and sufficient power handling capabilities over the frequency range of measurement is required for the TEM cell measurement port not connected to the RF disturbance generator.

6.7 DUT monitor

The performance of the DUT shall be monitored for indications of performance degradation. The monitoring equipment shall not be adversely affected by the injected RF disturbance signal.

7 Test set-up

7.1 General

The test set-up shall meet the requirements as described in IEC 62132-1. In addition, the following test set-up requirements shall apply.

7.2 Test set-up details

The EMC test board shall be mounted in the wall port of the TEM cell or GTEM cell with the DUT facing the septum as shown in Figure 1.

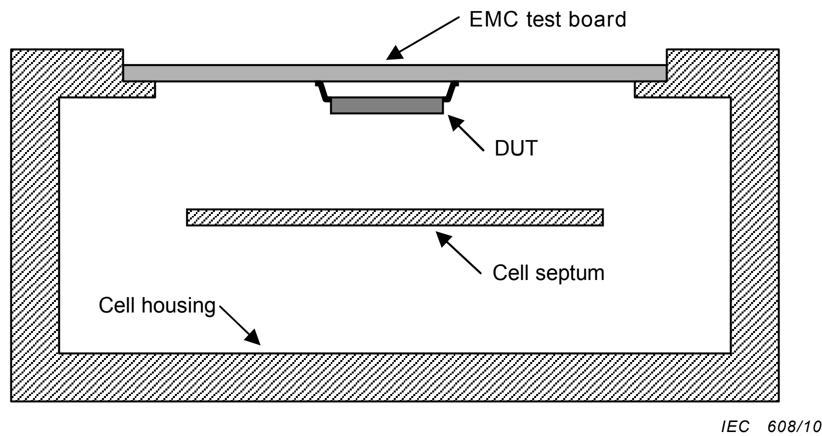

Figure 1 – TEM and GTEM cell cross-section

The test setup shall be as described in Figure 2 and Figure 3 for TEM cell and GTEM cell test configurations, respectively. One of the TEM cell measurement ports shall be terminated with a 50 Ω load. The remaining TEM cell measurement port, or the single GTEM measurement port, shall be connected to the output port of the power amplifier.

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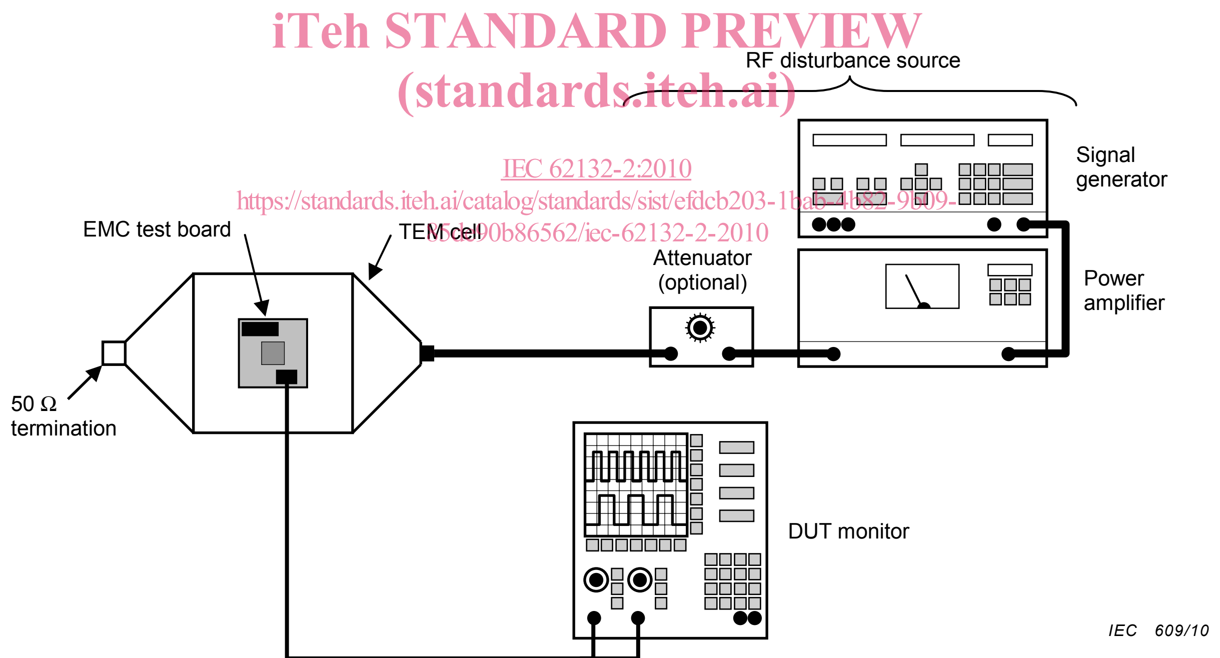

Figure 2 – TEM cell test set-up

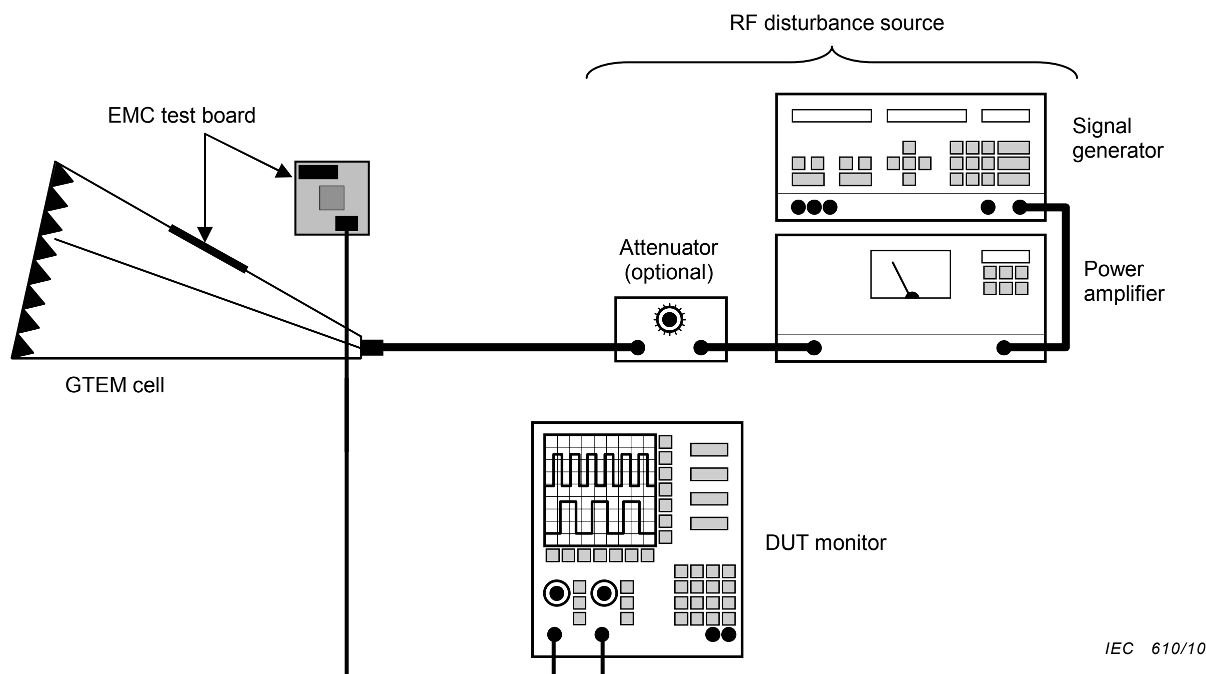

Figure 3 – GTEM cell test set-up

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7.3 EMC test board

The EMC test board shall be designed in accordance with the requirements in IEC 61967-2.

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8 Test procedure

8.1 General

The test procedure shall be in accordance with IEC 62132-1 except as modified herein. These default test conditions are intended to assure a consistent test environment. The following steps shall be performed:

a) field strength characterization (see Annex A);

b) immunity measurement (see 8.2).

If the users of this procedure agree to other conditions, these conditions shall be documented in the test report.

8.2 Immunity measurement

8.2.1 General

With the EMC test board energized and the DUT being operated in the intended test mode, measure the immunity to the injected RF disturbance signal over the desired frequency range.

8.2.2 RF disturbance signals

The RF disturbance signals shall be

- CW (continuous wave) and
- AM (amplitude modulated CW) at 80 % depth by a 1 kHz sine wave or (optionally) pulse modulated at 100 % depth with 50 % duty cycle and 1 kHz pulse repetition rate.

NOTE The optional pulse modulation requirement is typically about 6 dB more severe than the stated amplitude modulation requirement.

8.2.3 Test frequencies

The RF immunity of the DUT shall be evaluated at a number of discrete test frequencies from 150 kHz to 1 GHz, or as limited by the characteristics of the TEM cell. The frequencies to be tested shall be generated from the requirements specified in Table 2 of IEC 62132-1.

In addition, the RF immunity of the DUT shall be evaluated at critical frequencies. Critical frequencies are frequencies that are generated by, received by, or operated on by the DUT. Critical frequencies include but are not limited to crystal frequencies, oscillator frequencies, clock frequencies, data frequencies, etc.

8.2.4 Test levels and dwell time

The applied test level shall be increased in steps until a malfunction is observed or the maximum signal generator setting is reached. The step size shall be documented in the test report.

At each test level and frequency, the RF disturbance signal shall be applied for a minimum of 1 s (or at least the time necessary for the DUT to respond and the monitoring system to detect any performance degradation).

8.2.5 DUT monitoring

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The DUT shall be monitored for indications of susceptibility using the appropriate test equipment and as required in IEC 62132-1.

8.2.6 Detail procedure

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8.2.6.1 Field strength characterization

At each frequency to be tested, the signal generator setting to achieve the desired electric field level or levels shall be determined as described in Annex A.

8.2.6.2 Immunity measurement

The test flow, including major steps, is described in Figure 4. One of two strategies may be employed in performing this measurement as follows:

a) the output of the RF disturbance generator shall be set at a low value (e.g. 20 dB below the desired limit) and slowly increased up to the desired limit while monitoring the DUT for performance degradation. Any performance degradation at or below the desired limit shall be recorded;
b) the output of the RF disturbance generator shall be set at the desired performance limit while monitoring the DUT for performance degradation. Any performance degradation at the desired limit shall be recorded. The output of the RF disturbance generator shall then be reduced until normal function returns. The output of the RF disturbance generator shall then be increased until the performance degradation occurs again. This level shall also be recorded.

NOTE The DUT may respond differently to each of the above methods. In such a case, a method in which the interference signal is ramped up as well as down may be required.

The RF immunity measurement shall be performed in each of the four possible orientations resulting in four separate sets of data. The first measurement is made with the IC test board mounted in an arbitrary orientation of the IC in the cell wall port. The second measurement is made with the IC test board rotated 90 degrees from the orientation in the first measurement. For each of the third and fourth measurements, the test board is rotated again to ensure

immunity is measured in all four possible orientations. The four sets of data shall be documented in the test report.

9 Test report

The test report shall be in accordance with the requirements of IEC 62132-1.

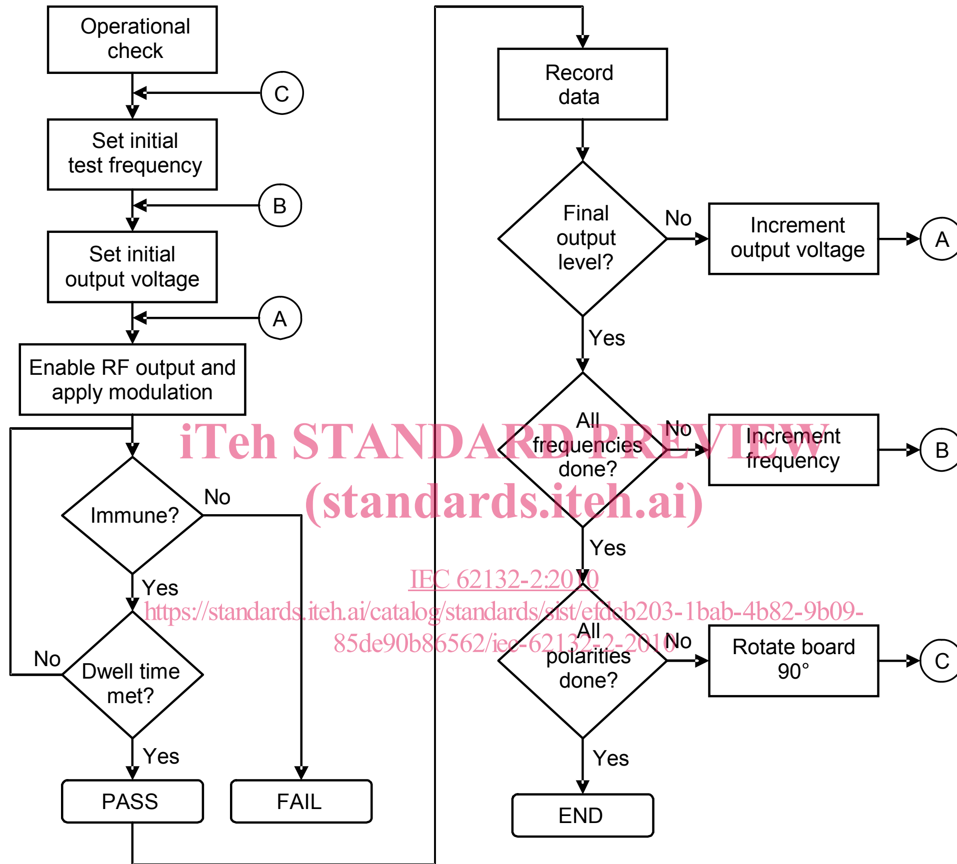

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Figure 4 – Immunity measurement procedure flowchart

Annex A
(normative)

Field strength characterization procedure

A.1 General

The signal level setting of the RF disturbance generator required to achieve the desired electric field level within the TEM or GTEM cell shall be determined in accordance with this procedure. This measurement shall be performed at each standard frequency (either linear or logarithmic as used in the actual test) as determined in accordance with 8.2.3. The RF disturbance signal used for characterization shall be a CW signal (e.g. no modulation shall be applied).

A.2 Electric (*E*) field strength characterization

A.2.1 Electric field characterization test fixture

The electric field can be measured by using a small monopole antenna at the centre location of the characterization board as shown in Figure A.1. It is recommended that the diameter of the top plate capacitive load shall be small (e.g. an area of approximately 0,001 m^2 or 10 cm^2) and either circular or square. The antenna top plate shall be kept parallel to the top metallic surface of the characterization board, which may be either a printed circuit board or metal plate, at a height of 3,0 mm ±0,1 mm. This top plate will yield a capacitance of about 3 pF. The centre of this plate shall be fed to a surface-mount, coaxial bulkhead connector that in turn shall be connected to the 50 Ω input impedance of an RF voltmeter or spectrum analyser. The resulting high-pass circuit results in an incremental slope of 20 dB/decade over the full frequency range up to 1 GHz.

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IEC 62132-2:2010
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The characterization board shall be identical in size to the EMC test board to be used during the actual radiated immunity measurements as specified in IEC 62132-1. The bulkhead connector shall be a 50 Ω type, either SMA or SMB, and placed in the exact centre of the characterization board.

The PCB shall be constructed with at least one conductive layer. The conductive layer should cover the entire board forming a solid ground plane. The SMA or SMB connector should be mounted on the side of the PCB opposite the ground plane, with its outer conductor connected to the ground plane and the centre conductor passing through an unplated, through-hole penetration to the other side of the board. Additional conductive PCB layers should be assigned to ground and connected using multiple vias as shown for the EMC test board in IEC 62132-1.

NOTE Tolerances for top plate area, capacitance and board location are under development.

A.2.2 Capacitance measurement

The top plate capacitance of the monopole shall be measured separately to assure a capacitance of 3 pF. The capacitance shall be measured with the characterization test fixture inserted into the TEM cell. With the impedance reference plane set at the bulkhead coaxial connector mounted at the location where the device/IC is to be positioned, the monopole is mounted to this bulkhead connector and the impedance (i.e. capacitance) is measured at a reference frequency of 10 MHz. This measurement is made to ensure that the physical length of the wire (i.e. the inductance) does not affect the characterization.