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INTERNATIONAL STANDARD

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Low-voltage surge protective devices—RD PREVIEW

Part 11: Surge protective devices connected to low-voltage power systems –

Requirements and test methods

Parafoudres basse tension itch ai/catalog/standards/sist/d54e9fce-f80f-406a-a69e-Partie 11: Parafoudres connectés aux systèmes basse tension – Exigences et méthodes d'essai





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Part 11: Surge protective devices connected to low-voltage power systems —

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IEC 61643-11:2011

Parafoudres basse tension ten ai/catalog/standards/sist/d54e9fce-f80f-406a-a69e-

Partie 11: Parafoudres connectés aux systèmes basse tension – Exigences et méthodes d'essai

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CONTENTS

FOI	REWC	PRD	7	
INT	RODU	JCTION	9	
1	Scop	e	10	
2	Norm	ative references	10	
3	Term	s, definitions and abbreviations	10	
	3.1	Terms and definitions		
	3.2	Abbreviations		
4	Servi	ce conditions	_	
	4.1	Frequency		
	4.2	Voltage		
	4.3 Air pressure and altitude			
	4.4	Temperatures		
	4.5	Humidity		
5	Class	ification	18	
	5.1	Number of ports	18	
		5.1.1 One	18	
		5.1.2 TwojTeh.STANDARD.PREVIEW	18	
	5.2	SPD design	18	
		5.2.1 Voltage switching and ards.iteh.ai)	18	
		5.2.2 Voltage limiting	18	
		5.2.3 Combination <u>IEC 61643-11:2011</u>	18	
	5.3	5.2.3 Combination IEC 61643-11:2011 Class I, II and III tests Uarb36e402ec/iec-61643-11-2011	18	
	5.4	Location	19	
		5.4.1 Indoor		
		5.4.2 Outdoor		
	5.5	Accessibility		
		5.5.1 Accessible		
	- 0	5.5.2 Inaccessible		
	5.6	Mounting method		
		5.6.2 Portable		
	5.7	Disconnectors (including overcurrent protection)		
	5.7	5.7.1 Location		
		5.7.2 Protection functions		
	5.8	Degree of protection provided by enclosures		
	5.9	Temperature and humidity range		
		5.9.1 Normal		
		5.9.2 Extended	20	
	5.10	Power system	20	
		5.10.1 AC between 47 Hz and 63 Hz	20	
		5.10.2 AC other than the range of 47 Hz to 63 Hz	20	
	5.11	11 Multipole SPD		
	5.12	SPD failure behaviour	20	
		5.12.1 open circuit (standard type SPD)	20	
		5.12.2 short-circuit (short-circuiting type SPD)	20	

6	Prefe	erred va	alues for SPD	20	
	6.1	Prefer	red values of impulse discharge current I _{imp} for class I tests	20	
	6.2		red values of nominal discharge current for class II tests <i>I</i> _n		
	6.3		red values of open-circuit voltage for class III tests $U_{ m oc}$		
	6.4		red values of voltage protection level $U_{\rm p}$		
	6.5		red values of r.m.s. maximum continuous operating voltage U_{c}		
7	Requ	uiremen	ıts	21	
	7.1	General requirements			
		7.1.1	Identification		
		7.1.2	Marking		
	7.2	Electri	ical requirements		
		7.2.1	Protection against direct contact	22	
		7.2.2	Residual current I _{PF}		
		7.2.3	Voltage protection level $U_{\rm p}$		
		7.2.4	Operating duty		
		7.2.5	Disconnectors and status indicators		
		7.2.6	Insulation resistance	24	
		7.2.7	Dielectric withstand	24	
		7.2.8	Behaviour under Temporary Overvoltages	24	
	7.3	· · · · · · · · · · · · · · · · · · ·			
		7.3.1	anical requirements. Mounting h STANDARD PREVIEW	25	
		7.3.2			
		7.3.3	Screws, current carrying parts and connections	25	
		7.3.4	Air clearances and creepage distances	27	
		7.3.5	Mechanical strength atalog/standards/sist/d54e9fce-f80f-406a-a69e-		
	7.4	Environmental and material requirements 43-11-2011			
		7.4.1	Protection provided by enclosure (IP code)	27	
		7.4.2	Heat resistance	27	
		7.4.3	Fire resistance	27	
		7.4.4	Tracking resistance	27	
		7.4.5	Electromagnetic compatibility	28	
	7.5	Additional requirements for specific SPD designs			
		7.5.1	Two port SPDs and one port SPDs with separate input/output terminals	28	
		7.5.2	Environmental tests for outdoor SPDs	28	
		7.5.3	SPDs with separate isolated circuits	28	
		7.5.4	Short-circuiting type SPDs	29	
	7.6	Additional requirements as may be declared by the manufacturer			
		7.6.1	One-port and two-port SPDs	29	
		7.6.2	Two port SPDs only	29	
8	Туре	tests		29	
	8.1	General testing procedures			
		8.1.1	Impulse discharge current used for class I additional duty test	36	
		8.1.2	Current impulse used for class I and class II residual voltage and operating duty tests	36	
		8.1.3	Voltage impulse used for class I and II sparkover tests		
		8.1.4	Combination wave used for class III tests		
	8.2				
	83		ical tests		

		0.3.1	Protection against direct contact	40
		8.3.2	Residual current IPE	40
		8.3.3	Measured limiting voltage	41
		8.3.4	Operating duty test	44
		8.3.5	Disconnectors and safety performance of overstressed SPDs	48
		8.3.6	Insulation resistance	54
		8.3.7	Dielectric withstand	55
		8.3.8	Behaviour under Temporary Overvoltages (TOVs)	56
8	3.4		ical tests	
		8.4.1	Reliability of screws, current-carrying parts and connections	60
			Terminals for external conductors	
			Verification of air clearances and creepage distances	
			Mechanical strength	
8	3.5		mental and material tests	
			Resistance to ingress of solid objects and to harmful ingress of water	
			Heat resistance	
			Ball pressure test	
			Resistance to abnormal heat and fire	
			Tracking resistance	
8	3.6	Addition	nal tests for specific SPD designs	75
		8.6.1	Test for two-port SPDs and one-port SPDs with separate input/output	7.
		0.6.0	terminals Environmental tests for outdoor SPDsh.ai)	/ 5
			SPDs with separate isolated circuits	
		0.0.3	Short circuiting type depaid43-11:2011	70
ç	3.7	0.0.4 Addition	Short-circuiting type SPDs1643-11:2011 https://standards.itch.ai/catalog/standards/sst/d54e9fce-180f-406a-a69e- nal tests for specific performance if declared by the manufacturer	70
C). <i>1</i>		Total discharge current test for multipole SPDs	
			Test to determine the voltage drop	
			Load-side surge withstand capability	
			Measurement of voltage rate of rise du/dt	
9 F	Routi		acceptance tests	
			tests	
_	9.1		ance tests	
		•	ve) Reference test voltages for SPDs U _{REF}	
			ve) TOV Ratings	80
			ve) Tests to determine the presence of a switching component and the follow current	90
	•			
		•	ve) Reduced test procedures	91
			tive) Alternative circuits for testing SPDs under TOVs caused by (medium) voltage system	93
Anne	x F ((informat	tive) Environmental tests for outdoor SPDs	94
		•	ve) Temperature rise limits	
ווטוט	Jyraf	J11 y		91
Figur	re 1 -	– Metalli	c screen test set-up	31
_			le of a decoupling network for single-phase power	
-		•	le of a decoupling network for three-phase power	
_		•	, -	
rigur	e 4 -	- Aiterna	te test for the measured limiting voltage	39

Figure 5 – Flow chart of testing to check the voltage protection level $U_{\rm p}$	42
Figure 6 – Flow chart of the operating duty test	45
Figure 7 – Test set-up for operating duty test	46
Figure 8 – Operating duty test timing diagram for test classes I and II	47
Figure 9 – Additional duty test timing diagram for test class I	48
Figure 10 – Operating duty test timing diagram for test class III	48
Figure 11 – Test circuit for SPD with I_{fi} lower than the declared short-circuit rating	52
Figure 12 – Test circuit for SPD's failure mode simulation	53
Figure 13 – Timing diagram for SPD's failure mode simulation	53
Figure 14 – Example of a test circuit to perform the test under TOVs caused by faults in the low voltage system	57
Figure 15 – Timing diagram for the test under TOVs caused by faults in the low voltage system	57
Figure 16 – Example of circuit for testing SPDs for use in TT systems under TOVs caused by faults in high (medium) voltage system	59
Figure 17 – Timing diagram for use in testing SPDs under TOVs caused by faults in the high (medium) voltage system using circuit of Figure 16	60
Figure 18 – Test apparatus for impact test	69
Figure 19 – Striking element of the pendulum hammer	70
Figure 20 – Ball thrust testen STANDARD PREVIEW	73
Figure 21 – Loading rod for ball thrust tester rds.iteh.ai)	73
Figure 22 – Examples for appropriate test circuits of the load side short-circuit test(s)	
Figure E.1 – Examples of a three-phase and single-phase circuit for use in testing SPDs under TOVs daused by faults in the high (medium) voltage system	93
Table 1 – List of Abbreviations	17
Table 2 – Class I, II and III tests	19
Table 3 – Type test requirements for SPDs	32
Table 4 – Common pass criteria for type tests	34
Table 5 – Cross reference for pass criteria versus type tests	35
Table 6 – Preferred parameters for class I test	36
Table 7 – Tests to be performed to determine the measured limiting voltage	43
Table 8 – Prospective short-circuit current and power factor	50
Table 9 – Dielectric withstand	55
Table 10 – Screw thread diameters and applied torques	61
Table 11 – Cross-sections of copper conductors for screw-type or screwless terminals	62
Table 12 – Pulling forces (screw terminals)	63
Table 13 – Conductor dimensions	63
Table 14 – Pulling forces (screwless terminals)	64
Table 15 – Air clearances for SPDs	66
Table 16 – Creepage distances for SPDs	67
Table 17 – Relationship between material groups and classifications	68
Table 18 – Fall distances for impact requirements	71
Table 19 – Test conductors for rated load current test	75

Table 20 – Current factor <i>k</i> for overload behaviour	76
Table 21 – Tolerances for proportional surge currents	79
Table A.1 – Reference test voltage values	82
Table B.1 – TOV test values for systems complying with IEC 60364 series	86
Table B.2 – TOV test parameters for North American systems	87
Table B.3 – TOV test parameters for Japanese systems	88
Table D.1 – Reduced test procedure for SPDs complying with IEC 61643-1:2005	92
Table G.1 – Temperature-rise limits	96

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

LOW-VOLTAGE SURGE PROTECTIVE DEVICES -

Part 11: Surge protective devices connected to low-voltage power systems – Requirements and test methods

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International Standard IEC 61643-11 has been prepared by subcommittee 37A: Low-voltage surge protective devices, of IEC technical committee 37: Surge arresters.

This first edition of IEC 61643-11 cancels and replaces the second edition of IEC 61643-1 published in 2005. This edition constitutes a technical revision.

The main changes with respect of the second edition of IEC 61643-1 are the complete restructuring and improvement of the test procedures and test sequences.

The text of this standard is based on the following documents:

FDIS	Report on voting
37A/229/FDIS	37A/232/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts of the IEC 61643 series can be found, under the general title *Low-voltage* surge protective devices, on the IEC website.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

- · reconfirmed.
- withdrawn,
- replaced by a revised edition, or
- · amended.

NOTE The attention of National Committees is drawn to the fact that equipment manufacturers and testing organizations may need a transitional period following publication of a new, amended or revised IEC publication in which to make products in accordance with the new requirements and to equip themselves for conducting new or revised tests.

It is the recommendation of the committee that the content of this publication be adopted for national implementation not earlier than 12 months from the date of publication.

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IMPORTANT – The "colour inside" logo on the cover page of this publication indicates that it contains colours which are considered to be useful for the correct understanding of its contents. Users should therefore print this publication using a colour printer.

INTRODUCTION

This part of IEC 61643 addresses safety and performance tests for surge protective devices (SPDs).

There are three classes of tests:

The Class I test is intended to simulate partial conducted lightning current impulses. SPDs subjected to Class I test methods are generally recommended for locations at points of high exposure, e.g., line entrances to buildings protected by lightning protection systems.

SPDs tested to Class II or III test methods are subjected to impulses of shorter duration.

SPDs are tested on a "black box" basis as far as possible.

IEC 61643-12 addresses the selection and application principles of SPDs in practical situations.

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LOW-VOLTAGE SURGE PROTECTIVE DEVICES -

Part 11: Surge protective devices connected to low-voltage power systems – Requirements and test methods

1 Scope

This part of IEC 61643 is applicable to devices for surge protection against indirect and direct effects of lightning or other transient overvoltages. These devices are packaged to be connected to 50/60 Hz a.c. power circuits, and equipment rated up to 1 000 V r.m.s. Performance characteristics, standard methods for testing and ratings are established. These devices contain at least one nonlinear component and are intended to limit surge voltages and divert surge currents.

2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60060-1:1989, High-voltageStest Itechniques Le Part 1) General definitions and test requirements

IEC 61643-11:2011

IEC 60112, Method for the determination of the proof and the comparative tracking indices of solid insulating materials

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IEC 60529, Degrees of protection provided by enclosures (IP Code)

IEC 60664-1:2007, Insulation coordination for equipment within low-voltage systems – Part 1: Principles, requirements and tests

IEC 60695-2-11:2000, Fire hazard testing – Part 2-11: Glowing/hot-wire based test methods – Glow-wire flammability test method for end-products

IEC 61000 (all parts), Electromagnetic compatibility (EMC)

IEC 61180-1, High-voltage test techniques for low voltage equipment – Part 1: Definitions, test and procedure requirements

3 Terms, definitions and abbreviations

For the purposes of this document, the following terms, definitions and abbreviations apply.

3.1 Terms and definitions

3.1.1

surge protective device

device that contains at least one nonlinear component that is intended to limit surge voltages and divert surge currents

NOTE An SPD is a complete assembly, having appropriate connecting means.

3.1.2

one-port SPD

SPD having no intended series impedance

NOTE A one port SPD may have separate input and output connections.

3.1.3

two-port SPD

SPD having a specific series impedance connected between separate input and output connections

3.1.4

voltage switching type SPD

SPD that has a high impedance when no surge is present, but can have a sudden change in impedance to a low value in response to a voltage surge

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NOTE Common examples of components used in voltage switching type SPDs are spark gaps, gas tubes and thyristors. These are sometimes called "crowbar type" components:

3.1.5

voltage limiting type SPD

IEC 61643-11:2011

SPD that has a high impedance when no surge is present, but will reduce it continuously with increased surge current and voltage b36e402ec/iec-61643-11-2011

NOTE Common examples of components used in voltage limiting type SPDs are varistors and avalanche breakdown diodes. These are sometimes called "clamping type" components.

3.1.6

combination type SPD

SPD that incorporates both, voltage switching components and voltage limiting components. The SPD may exhibit voltage switching, limiting or both

3.1.7

short-circuiting type SPD

SPD tested according to Class II tests which changes its characteristic to an intentional internal short-circuit due to a surge current exceeding its nominal discharge current I_n

3.1.8

mode of protection of an SPD

an intended current path, between terminals that contains protective components, e.g. line-to-line, line-to-earth, line-to-neutral, neutral-to-earth.

3.1.9

nominal discharge current for class II test

I_n

crest value of the current through the SPD having a current waveshape of 8/20

3.1.10

impulse discharge current for class I test

crest value of a discharge current through the SPD with specified charge transfer Q and specified energy W/R in the specified time

3.1.11

maximum continuous operating voltage

 $U_{\rm C}$

maximum r.m.s. voltage, which may be continuously applied to the SPD's mode of protection

NOTE The $U_{\rm C}$ value covered by this standard may exceed 1 000 V.

3.1.12

follow current

peak current supplied by the electrical power system and flowing through the SPD after a discharge current impulse

3.1.13

rated load current

maximum continuous rated r.m.s. current that can be supplied to a resistive load connected to the protected output of an SPD

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3.1.14 voltage protection level

(standards.iteh.ai)

maximum voltage to be expected at the SPD terminals due to an impulse stress with defined voltage steepness and an impulse stress with a discharge current with given amplitude and https://standards.iteh.ai/catalog/standards/sist/d54e9fce-f80f-406a-a69e waveshape

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NOTE The voltage protection level is given by the manufacturer and may not be exceeded by:

- the measured limiting voltage, determined for front-of-wave sparkover (if applicable) and the measured limiting voltage, determined from the residual voltage measurements at amplitudes corresponding to I_n and/or I_{imp} respectively for test classes II and/or I;
- the measured limiting voltage at $U_{\rm OC}$, determined for the combination wave for test class III.

3.1.15

measured limiting voltage

highest value of voltage that is measured across the terminals of the SPD during the application of impulses of specified waveshape and amplitude

3.1.16

residual voltage

crest value of voltage that appears between the terminals of an SPD due to the passage of discharge current

3.1.17

temporary overvoltage test value

test voltage applied to the SPD for a specific duration t_T , to simulate the stress under TOV conditions

3.1.18

load-side surge withstand capability for a two-port SPD

ability of a two-port SPD to withstand surges on the output terminals originating in circuitry downstream of the SPD

3.1.19

voltage rate-of-rise of a two-port SPD

rate of change of voltage with time measured at the output terminals of a two port SPD under specified test conditions

3.1.20

1,2/50 voltage impulse

voltage impulse with a nominal virtual front time of 1,2 μs and a nominal time to half-value of 50 μs

NOTE The Clause 6 of IEC 60060-1 (1989) defines the voltage impulse definitions of front time, time to half-value and waveshape tolerance.

3.1.21

8/20 current impulse

current impulse with a nominal virtual front time of 8 μs and a nominal time to half-value of 20 μs

NOTE The Clause 8 of IEC 60060-1 (1989) defines the current impulse definitions of front time, time to half-value and waveshape tolerance.

3.1.22

combination wave

a wave characterized by defined voltage amplitude ($U_{\rm OC}$) and waveshape under open-circuit conditions and a defined current amplitude ($I_{\rm CW}$) and waveshape under short-circuit conditions iTeh STANDARD PREVIEW

NOTE The voltage amplitude, current amplitude and waveform that is delivered to the SPD are determined by the combination wave generator (CWG) impedance Z_f and the impedance of the DUT.

3.1.23 <u>IEC 61643-11:2011</u>

open circuit voltagletps://standards.iteh.ai/catalog/standards/sist/d54e9fce-f80f-406a-a69e-

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open circuit voltage of the combination wave generator at the point of connection of the device under test

3.1.24

combination wave generator short-circuit current

¹CW

prospective short-circuit current of the combination wave generator, at the point of connection of the device under test

NOTE When the SPD is connected to the combination wave generator, the current that flows through the device is generally less than $I_{\rm CW}$.

3.1.25

thermal stability

SPD is thermally stable if, after heating up during the operating duty test, its temperature decreases with time while energized at specified maximum continuous operating voltage and at specified ambient temperature conditions

3.1.26

degradation (of performance)

undesired permanent departure in the operational performance of equipment or a system from its intended performance

3.1.27

short-circuit current rating

ISCCR

maximum prospective short-circuit current from the power system for which the SPD, in conjunction with the disconnector specified, is rated