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МЕЖДУНАРОДНАЯ ОРГАНИЗАЦИЯ ПО СТАНДАРТИЗАЦИИ

Information processing — Data interchange on 130 mm (5.25 in) flexible disk cartridges using modified frequency modulation recording at 13 262 ftprad, on 80 tracks on each side —

Part 3:
Track format B for 80 tracks

Reference number
ISO 8630-3 : 1987 (E)

Foreword

ISO (the International Organization for Standardization) is a worldwide federation of national standards bodies (ISO member bodies). The work of preparing International Standards is normally carried out through ISO technical committees. Each member body interested in a subject for which a technical committee has been established has the right to be represented on that committee. International organizations, governmental and non-governmental, in liaison with ISO, also take part in the work.

Draft International Standards adopted by the technical committees are circulated to the member bodies for approval before their acceptance as International Standards by the ISO Council. They are approved in accordance with ISO procedures requiring at least 75 % approval by the member bodies voting.

International Standard ISO 8630-3 was prepared by Technical Committee ISO/TC 97, *Information processing systems.*

Users should note that all International Standards undergo revision from time to time and that any reference made herein to any other International Standard implies its latest edition, unless otherwise stated.

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0 Introduction

ISO 8630 specifies the characteristics of 130 mm (5.25 in) flexible disk cartridges recorded at 13 262 ftprad, using modified frequency modulation (MFM) recording, on 80 tracks on each side.

ISO 8630-1 specifies the dimensional, physical and magnetic characteristics of the cartridge, so as to provide physical interchangeability between data processing systems.

ISO 8630-2 specifies an alternative track format for data interchange.

ISO 8630-1 and ISO 8630-3, together with the labelling scheme specified in ISO 9293, provide for full data interchange between data processing systems.

1 Scope and field of application

This part of ISO 8630 specifies the quality of recorded signals, the track layout, and a track format to be used on 130 mm (5.25 in), 13 262 ftprad flexible disk cartridges intended for data interchange between data processing systems.

NOTE — Numeric values in the SI and/or Imperial measurement system in this part of ISO 8630 may have been rounded off and therefore are consistent with, but not exactly equal to, each other. Either system may be used, but the two should be neither intermixed nor re-converted. The original design was made using Imperial units and further developments were made using SI units.

2 Conformance

A flexible disk cartridge shall be in conformance with ISO 8630 when it meets all the requirements of parts 1 and 3 of ISO 8630.

NOTE — ISO 9293 specifies a field in the volume label.

3 References

ISO 646, *Information processing — ISO 7-bit coded character set for information interchange.*

ISO 2022, *Information processing — ISO 7-bit and 8-bit coded character sets — Code extension techniques.*

ISO 4873, *Information processing — ISO 8-bit code for information interchange — Structure and rules for implementation.*

ISO 6429, *Information processing — ISO 7-bit and 8-bit character sets — Additional control functions for character-imaging devices.*

ISO 9293, *Information processing — Volume and file structure of flexible disk cartridges for information interchange.*

4 General requirements

4.1 Mode of recording

The mode of recording shall be Modified Frequency Modulation (MFM) for which the conditions are

a) a flux transition shall be written at the centre of each bit cell containing a ONE;

b) a flux transition shall be written at each cell boundary between consecutive bit cells containing ZEROs.

Exceptions to this are defined in 4.12.

4.2 Track location tolerance of the recorded flexible disk cartridge

The centrelines of the recorded tracks shall be within ± 0,042 5 mm (± 0.001 67 in) of the nominal positions, over the range of operating environment specified in ISO 8630-1.

4.3 Recording offset angle

At the instant of writing or reading a magnetic transition, the transition shall have an angle of 0° ± 18′ with the radius.

NOTE — As tracks may be written and overwritten at extremes of the tolerances given in 4.2 and 4.3, a band of old information may be left at one edge of the newly written data and would constitute unwanted noise when reading. It is therefore necessary to trim the edges of the tracks by erasure after writing.

4.4 Density of recording

4.4.1 The nominal density of recording shall be 13 262 ftprad*. The resulting nominal bit cell length is 75,5 µrad.

4.4.2 The long-term average bit cell length shall be the average bit cell length measured over a sector. It shall be within ± 3,0 % of the nominal bit cell length.

4.4.3 The short-term average bit cell length, referred to a particular bit cell, shall be the average of the lengths of the preceding eight bit cells. It shall be within ± 8 % of the long-term average bit cell length.

4.5 Flux transition spacing (see figure 1)

The instantaneous spacing between flux transitions may be influenced by the reading and writing process, the bit sequence recorded (pulse crowding effects) and other factors.

The locations of the transitions are defined as the locations of the peaks in the signal when reading. Tests should be carried out using a peak-sensing read amplifier (see annexes B and C).

4.5.1 The spacing between the flux transitions in a sequence of ONEs shall be between 80 % and 120 % of the short-term average bit cell length.

4.5.2 The spacing between the flux transition for a ONE and that between two ZEROs preceding or following it shall be between 130 % and 165 % of the short-term average bit cell length.

4.5.3 The spacing between the flux transitions of two ONEs surrounding a ZERO shall lie between 185 % and 225 % of the short-term average bit cell length.

4.6 Average Signal Amplitude

For each side the Average Signal Amplitude on any track (see ISO 8630-1) of the interchanged flexible disk cartridge shall be less than 160 % of SRA_{1f} and more than 40 % of SRA_{2f}.

4.7 Byte

A byte is a group of eight bit-positions, identified B1 to B8. The bit in each position is a ZERO or a ONE.

4.8 Sector

All tracks shall be divided into 15 sectors of 512 bytes.

4.9 Cylinder

A pair of tracks, one on each side of the disk, having the same track number.

4.10 Cylinder Number

The Cylinder Number shall be a two-digit number identical with the track number of the tracks of the cylinder.

4.11 Data capacity of a track

The data capacity of a track shall be 7 680 bytes.

4.12 Hexadecimal notation

Hexadecimal notation shall be used hereafter to denote the following bytes:

(00) for (B8 to B1) = 00000000
(01) for (B8 to B1) = 00000001
(02) for (B8 to B1) = 00000010
(4E) for (B8 to B1) = 01001110
(FE) for (B8 to B1) = 11111110
(FB) for (B8 to B1) = 11111011
(A1)* for (B8 to B1) = 10100001

In byte (A1)* the boundary transition between B3 and B4 is missing.

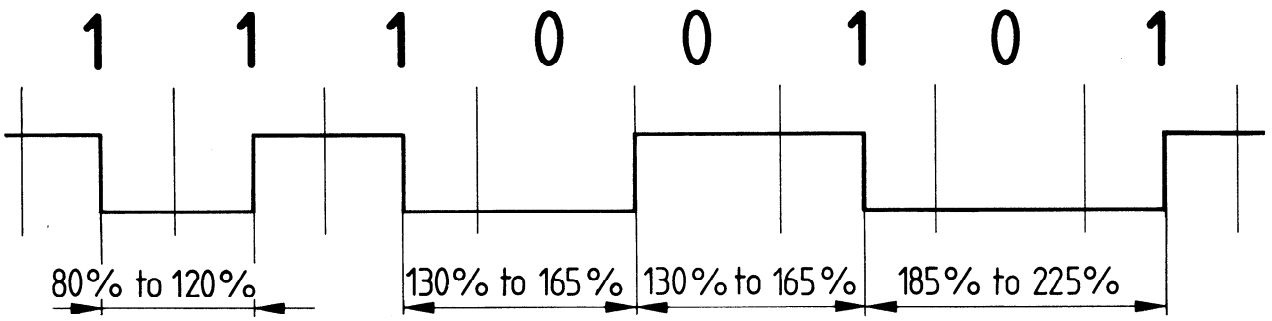

Figure 1

* Flux transitions per radian

4.13 Error Detection Characters (EDC)

The two EDC bytes are hardware-generated by shifting serially the relevant bits, specified later for each part of the track, through a 16-bit shift register described by the generator polynomial:

$$X^{16} + X^{12} + X^5 + 1$$

(See also annex A.)

5 Track layout

After formatting, there shall be 15 usable sectors on each track.

The layout of each track shall be as shown in figure 2.

5.1 Index Gap

At nominal density, this field shall comprise not less than 32 bytes and not more than 146 bytes, the content of which is not specified except that it shall not contain an (A1)*-byte.

Writing the Index Gap is started when the index hole is detected. Any of the first 16 bytes may become ill-defined due to subsequent overwriting.

5.2 Sector Identifier

The layout of this field shall be as shown in table 1.

5.2.1 Identifier Mark

This field shall comprise 16 bytes:

12 (00)-bytes

3 (A1)*-bytes

1 (FE)-byte

5.2.2 Address Identifier

This field shall comprise 6 bytes.

5.2.2.1 Track Address

This field shall comprise 2 bytes:

a) Cylinder Number (C)

This field shall specify in binary notation the Cylinder Number from 00 for the outermost cylinder to 79 for the innermost cylinder.

b) Side Number (Side)

This field shall specify the side of the disk. On side 0 it shall be (00) on all tracks. On side 1 it shall be (01) on all tracks.

5.2.2.2 Sector Number (S)

The 3rd byte shall specify in binary notation the Sector Number from 01 for the first sector to 15 for the last sector.

The sectors may be recorded in any order of their Sector Numbers.

Table 1

Sector Identifier							
Identifier Mark			Address Identifier				
			Track Address		S		EDC
12 bytes (00)	3 bytes (A1)*	1 byte (FE)	C 1 byte	Side 1 byte (00) or (01)	1 byte	1 byte (02)	2 bytes

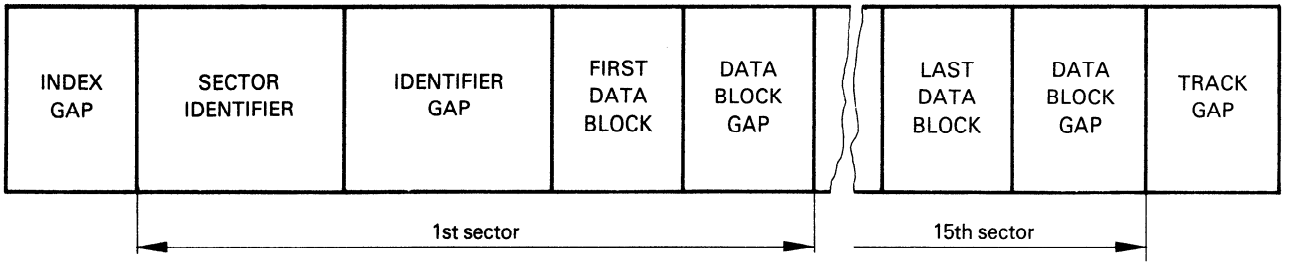

Figure 2

5.2.2.3 4th byte

The 4th byte shall always be a (02)-byte.

5.2.2.4 EDC

These two bytes shall be generated as defined in 4.13 using the bytes of the Sector Identifier starting with the first (A1)*-byte (see 5.2.1) of the Identifier Mark and ending with the 4th byte (see 5.2.2.3) of the Address Identifier.

If the EDC is incorrect, then the sector is defective. ISO 9293 specifies the handling of defective sectors.

5.3 Identifier Gap

This field shall comprise 22 initially recorded (4E)-bytes.

These bytes may have become ill-defined due to overwriting.

5.4 Data Block

The layout of this field shall be as given in table 2.

Table 2

Data Block				
Data Mark			Data Field	EDC
12 bytes (00)	3 bytes (A1)*	1 byte (FB)	512 bytes	2 bytes

5.4.1 Data Mark

This field shall comprise 16 bytes:

12 (00)-bytes

3 (A1)*-bytes

1 (FB)-byte.

5.4.2 Data Field

This field shall comprise 512 bytes.

If it comprises less than the requisite number of data bytes, the remaining positions shall be filled with (00)-bytes.

5.4.3 EDC

These two bytes shall be generated as defined in 4.13 using the bytes of the Data Block starting with the first (A1)*-byte of the Data Mark (see 5.4.1) and ending with the last byte of the Data Field (see 5.4.2).

If the EDC is incorrect, then the sector is defective. ISO 9293 specifies the handling of defective sectors.

5.5 Data Block Gap

This field shall comprise 84 initially recorded (4E)-bytes. These bytes may have become ill-defined due to overwriting.

The Data Block Gap is recorded after each Data Block and it precedes the following Sector Identifier. After the last Data Block, it precedes the Track Gap.

5.6 Track Gap

This field shall follow the Data Block Gap of the last sector. (4E)-bytes are written until the Index hole is detected, unless it has been detected during writing of the last Data Block Gap, in which case there shall be no Track Gap.

6 Coded representation of data

6.1 Standards

The contents of the Data Field shall be recorded and interpreted according to the relevant International Standards for the coding of information, for example ISO 646, ISO 2022 or ISO 4873.

6.2 Coding methods

6.2.1 When the coding method requires it, the Data Field shall be regarded as an ordered sequence of 8-bit bytes.

Within each byte the bit positions shall be identified by B8 to B1. The high-order bit shall be recorded in position B8 and the low-order bit in position B1. The sequence of recording shall be high-order bit first.

When the data is encoded according to an 8-bit code, the binary weights of the bit positions shall be as shown in figure 3.

Bit Position	B8	B7	B6	B5	B4	B3	B2	B1
Binary Weights	128	64	32	16	8	4	2	1

Figure 3

When the data is encoded according to a 7-bit code, bit position B8 shall contain bit ZERO, and the data shall be encoded in bit positions B7 to B1, using the same binary weights as shown in figure 3.

6.2.2 When the coding method requires it, the Data Field shall be regarded as an ordered sequence of bit positions, each containing a bit.

Annex A

EDC implementation

(This annex does not form part of the standard.)

Figure 4 shows the feedback connections of a shift register which may be used to generate the EDC bytes.

Prior to operation, all positions of the shift register are set to ONE. Input data are added (exclusive OR) to the contents of position C_{15} of the register to form a feedback. This feedback is in its turn added (exclusive OR) to the contents of position C_4 and position C_{11}.

On shifting, the outputs of the exclusive OR gates are entered respectively into positions C_0, C_5 and C_{12}. After the last data bit has been added, the register is shifted once more as specified above.

The register then contains the EDC bytes.

If further shifting is to take place during the writing of the EDC bytes, the control signal inhibits exclusive OR operations.

To check for errors when reading, the data bits are added into the shift register in exactly the same manner as they were during writing. After the data, the EDC bytes are also entered into the shift register as if they were data. After the final shift, the register contents will be all ZERO if the record does not contain errors.

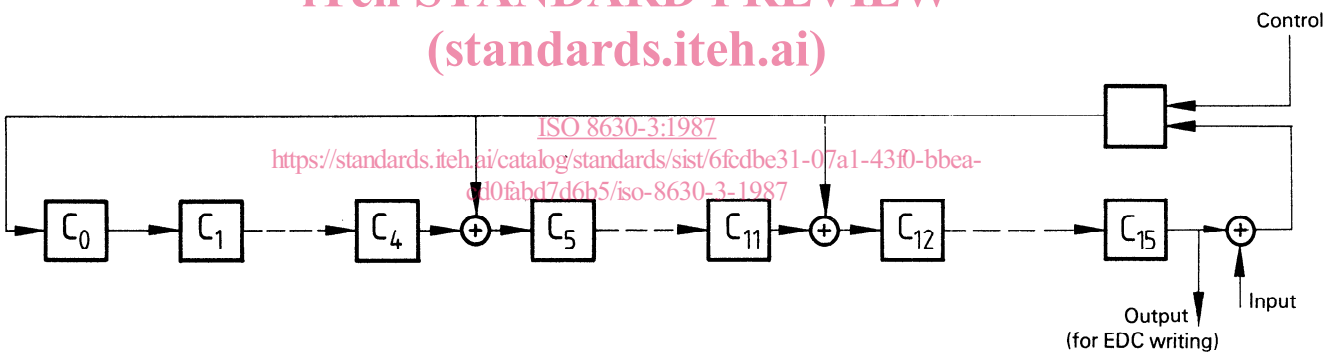

Figure 4