

PUBLICLY AVAILABLE SPECIFICATION PRE-STANDARD

**Mechanical standardization of semiconductor devices –
Part 6-19: Measurement methods of package warpage at elevated temperature
and the maximum permissible warpage**

IEC/PAS 60191-6-19:2008

<https://standards.iteh.ai/catalog/standards/iec/3d0a2009-a6b5-45e2-aa56-85e6848aa295/iec-pas-60191-6-19-2008>

WITHDRAWN



THIS PUBLICATION IS COPYRIGHT PROTECTED

Copyright © 2008 IEC, Geneva, Switzerland

All rights reserved. Unless otherwise specified, no part of this publication may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm, without permission in writing from either IEC or IEC's member National Committee in the country of the requester.

If you have any questions about IEC copyright or have an enquiry about obtaining additional rights to this publication, please contact the address below or your local IEC member National Committee for further information.

IEC Central Office
3, rue de Varembe
CH-1211 Geneva 20
Switzerland
Email: inmail@iec.ch
Web: www.iec.ch

About the IEC

The International Electrotechnical Commission (IEC) is the leading global organization that prepares and publishes International Standards for all electrical, electronic and related technologies.

About IEC publications

The technical content of IEC publications is kept under constant review by the IEC. Please make sure that you have the latest edition, a corrigenda or an amendment might have been published.

- Catalogue of IEC publications: www.iec.ch/searchpub

The IEC on-line Catalogue enables you to search by a variety of criteria (reference number, text, technical committee,...). It also gives information on projects, withdrawn and replaced publications.

- IEC Just Published: www.iec.ch/online_news/justpub

Stay up to date on all new IEC publications. Just Published details twice a month all new publications released. Available on-line and also by email.

- Electropedia: www.electropedia.org

The world's leading online dictionary of electronic and electrical terms containing more than 20 000 terms and definitions in English and French, with equivalent terms in additional languages. Also known as the International Electrotechnical Vocabulary online.

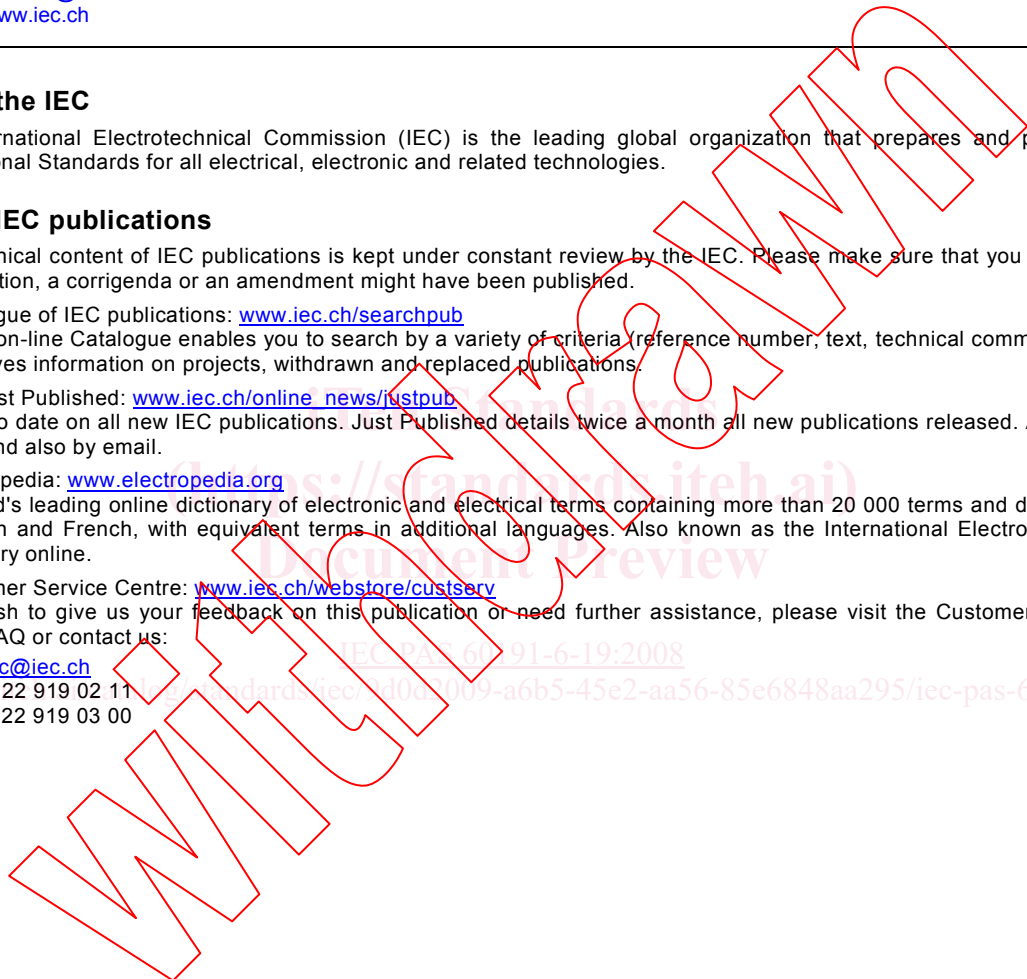
- Customer Service Centre: www.iec.ch/webstore/custserv

If you wish to give us your feedback on this publication or need further assistance, please visit the Customer Service Centre FAQ or contact us:

Email: csc@iec.ch

Tel.: +41 22 919 02 11

Fax: +41 22 919 03 00





PUBLICLY AVAILABLE SPECIFICATION PRE-STANDARD

**Mechanical standardization of semiconductor devices –
Part 6-19: Measurement methods of package warpage at elevated temperature
and the maximum permissible warpage**

<https://standards.iteh.ai/catalog/standards/iec/2d0e2009-a6b5-45e2-aa56-85e6848aa295/iec-pas-60191-6-19-2008>

INTERNATIONAL
ELECTROTECHNICAL
COMMISSION

PRICE CODE

S

CONTENTS

FOREWORD.....	3
1 Scope.....	5
2 Normative references	5
3 Terms and definitions	5
4 Sample.....	9
4.1 Sample size	9
4.2 Solder ball removal	9
4.3 Pre-treatment conditions	9
4.4 Maximum time after pretreatment until measurement.....	9
4.5 Repetition of the reflow cycles for the sample.....	9
5 Measurement	9
5.1 General description	9
5.2 Temperature profile and the temperatures for measurements	9
5.3 Measurement method	10
6 Maximum permissible package warpage at elevated temperature	11
7 Recommended datasheet for the package warpage.....	11
7.1 Measurement temperatures for data sheet.....	11
7.2 Data sheet.....	11
7.3 Example of data sheets	12
Annex A (informative) Explanatory notes	14
Bibliography.....	22
Figure 1 – Measuring zone of BGA and FBGA in full grid layout.....	6
Figure 2 – Measuring zone of BGA and FBGA perimeter layout with 4 rows and 4 columns	6
Figure 3 – Measuring zone of FLGA perimeter layout with 4 rows and 4 columns.....	7
Figure 4 – Calculation of the sign of package warpage	8
Figure 5 – Package warpage	8
Figure 6 – Thermocouple placement	10
Figure 7 – Temperature dependency of the package warpage	12
Figure 8 – Recommended data sheet.....	13
Figure A.1 – Calculation of maximum relative displacement immune from open solder joints.....	16
Figure A.2 – Calculation of maximum relative displacement immune from solder ball bridges	17
Figure A.3 – Package warpage of FLGA at elevated temperature.....	19
Table 1 – Maximum permissible package warpages for BGA and FBGA.....	11
Table 2 – Maximum permissible package warpages for FLGA.....	11
Table A.1 – Maximum permissible package warpage for BGA and FBGA – Explanatory table	18
Table A.2 – Maximum permissible package warpage for FLGA – Explanatory table	19
Table A.3 – Comparison between JESD22B112 and IEC/JEITA PAS 60191-6-19	20

INTERNATIONAL ELECTROTECHNICAL COMMISSION

MECHANICAL STANDARDIZATION OF SEMICONDUCTOR DEVICES –**Part 6-19: Measurement methods of package warpage at elevated temperature and the maximum permissible warpage****FOREWORD**

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.
- 5) IEC provides no marking procedure to indicate its approval and cannot be rendered responsible for any equipment declared to be in conformity with an IEC Publication.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

A PAS is a technical specification not fulfilling the requirements for a standard but made available to the public.

IEC-PAS 60191-6-19 was submitted by the JEITA (Japan Electronics and Information Technology Industries Association) and has been processed by IEC subcommittee SC47D: Mechanical standardization for semiconductor devices.

The text of this PAS is based on the following documents

This PAS was approved for publication by the P-members of the committee concerned as indicated in the following document:

Draft PAS	Report on voting
47D/691/NP	47D/707/RVN

Following publication of this PAS, which is a pre-standard publication, the technical committee or subcommittee concerned will transform it into an International Standard.

This PAS shall remain valid for an initial maximum period of three years starting from the publication date. The validity may be extended for a single three-year period, following which it shall be revised to become another type of normative document or shall be withdrawn.

Withdawn

iTeh Standards
(<https://standards.iteh.ai>)
Document Preview

IEC PAS 60191-6-19:2008

<https://standards.iteh.ai/catalog/standards/iec/3d0a2009-a6b5-45e2-aa56-85e6848aa295/iec-pas-60191-6-19-2008>

MECHANICAL STANDARDIZATION OF SEMICONDUCTOR DEVICES –

Part 6-19: Measurement methods of package warpage at elevated temperature and the maximum permissible warpage

1 Scope

This PAS stipulates the package warpage criteria and the package warpage measurement methods at elevated temperature for BGA, FBGA, and FLGA

2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document applies.

JEITA EDR-4701/301, *Resistance to soldering heat for surface mounting devices (SMD)*

3 Terms and definitions

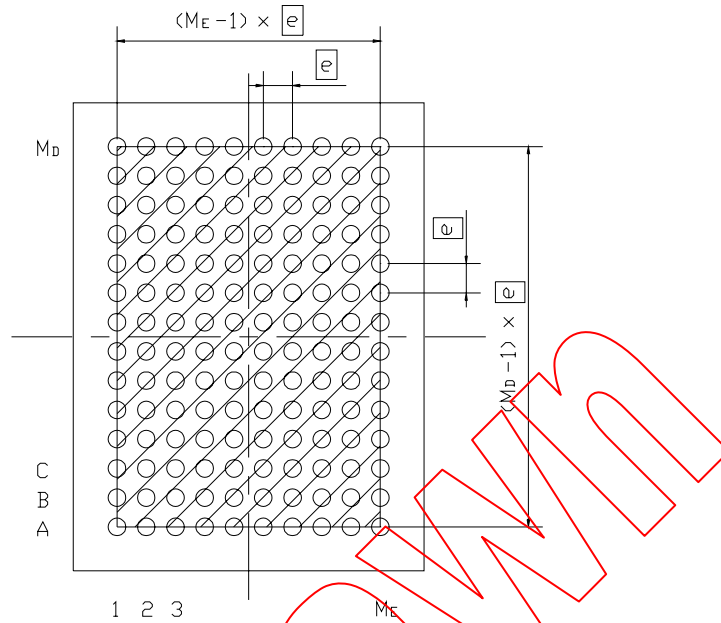
For the purposes of this document, the following terms and definitions apply.

3.1 measuring zone

area to be measured to determine the package warpage

NOTE 1 For the packages whose stand-off height is more than 0,1 mm, such as BGA and FBGA, the measuring zone is the area where terminals are located. This area is bordered by the lines connecting the centers of the outermost neighboring solder balls (see Figure 1 and Figure 2). If there are thermal balls at the package centre, their area is also considered as a part of the measuring zone

NOTE 2 For the packages whose stand-off height is 0,1 mm or less, such as FLGA, the measuring area is the substrate surface except certain edge margin (see Figure 3, dimension L). The width of this margin L depends on the capability of each measuring instrument (0,2 mm recommended).



NOTE The hatched area indicates the measuring zone.

Figure 1 – Measuring zone of BGA and FBGA in full grid layout

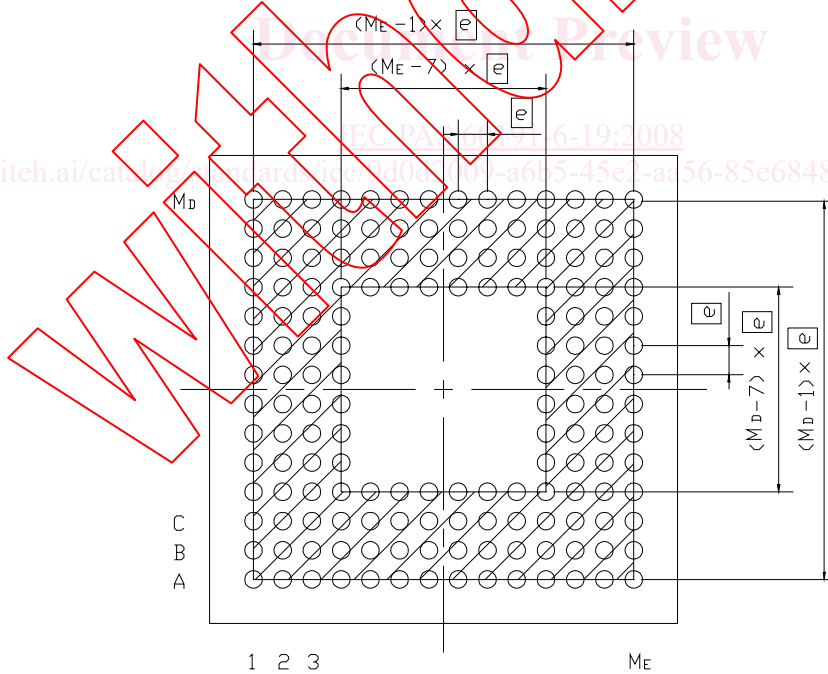
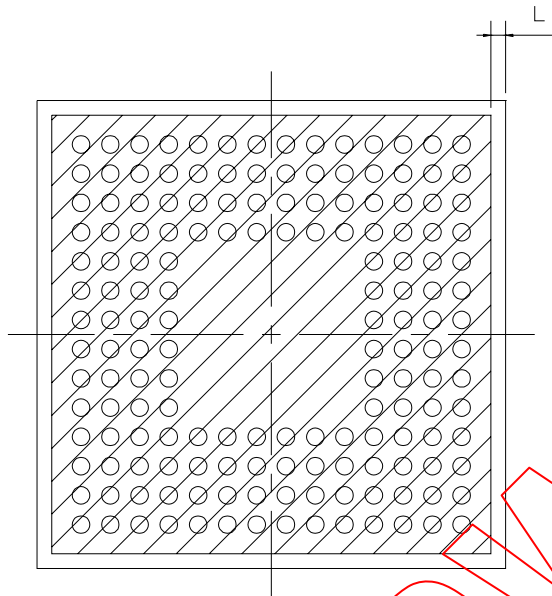


Figure 2 – Measuring zone of BGA and FBGA perimeter layout with 4 rows and 4 columns



NOTE The edge margin L indicates the exempt area from measurement to avoid measurement noise depending on the instrument capability. Recommended edge margin $L = 0.2$ mm.

Figure 3 – Measuring zone of FLGA perimeter layout with 4 rows and 4 columns

3.2

convex warpage

arched top surface (not interconnect side) of package being mounted on PWB

NOTE The sign of the convex warpage is defined as plus.

3.3

concave warpage

inward-curving top surface (not interconnect side) of package being mounted on PWB

NOTE The sign of the concave warpage is defined as minus.

3.4

package warpage sign

plus or minus sign of package warpage determined by the sign of the sum of the largest positive displacement and the largest negative displacement of the package profile on both measurement zone diagonals

NOTE These diagonals are regarded as base lines connecting the outermost opposite corners of the measuring zone. The sign of the package warpage is defined as the sign of:

$$(AB_{MAX} + AB_{MIN} + CD_{MAX} + CD_{MIN})$$

AB_{MAX} is the largest positive displacement and AB_{MIN} is the largest negative displacement of the package profile on the diagonal AB; (The sign of AB_{MAX} is plus and AB_{MIN} is zero in Figure 4.)

CD_{MAX} is the largest positive displacement and CD_{MIN} is the largest negative displacement of the package profile on the diagonal CD; (The sign of CD_{MAX} is plus and that of CD_{MIN} is minus in Figure 4.)

The concave or convex impression of the package warpage can differ from the above defined sign, in critical cases.

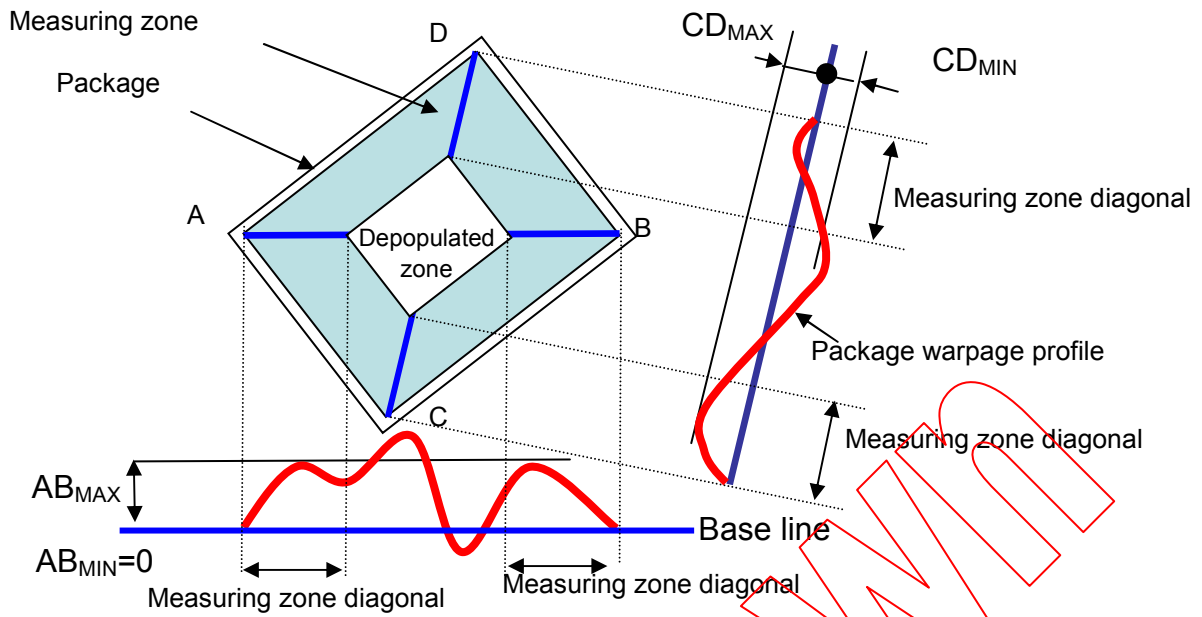


Figure 4 – Calculation of the sign of package warpage

3.5 package warpage

difference of the largest positive and the largest negative displacements of the package warpage in the measuring zone with respect to the reference plane, preceded by package warpage sign

NOTE This reference plane is derived using the least square method with the measuring zone data. For example, the absolute value of the package warpage $|C|$ is obtained by the sum of the absolute value of the largest positive displacement $|A|$ and that of the largest negative displacement $|B|$. This is in respect to the reference plane which is derived by using the least square method, as shown in Figure 5. Package warpage sign precedes $|C|$.

$$|C| = |A| + |B|$$

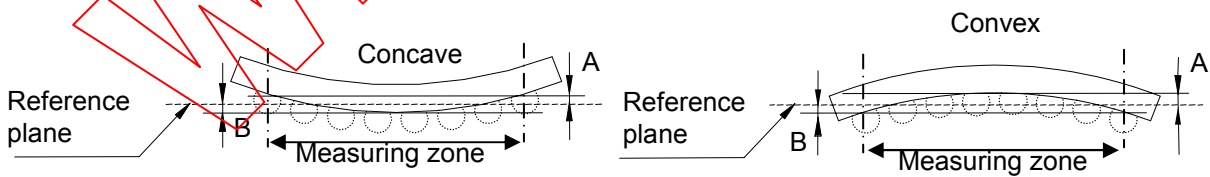


Figure 5 – Package warpage