



SLOVENSKI STANDARD
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Sectional Specification: Flexible multilayer printed boards with through connections

Sectional Specification: Flexible multilayer printed boards with through connections

Rahmenspezifikation: Flexible Mehrlagen-Leiterplatten mit Durchverbindungen

Spécification intermédiaire: Cartes imprimées multicouches souples avec connexions transversales

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EUROPEAN STANDARD
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Descriptors: Multilayer printed boards, with through connections, capability test, quality conformance inspection, test patterns

English version

Sectional Specification:
Flexible multilayer printed boards with through connections

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Cartes imprimées multicouches souples
avec connexions transversales

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mit Durchverbindungen

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CENELEC

European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B - 1050 Brussels

Foreword

This European Standard was prepared by CLC/TC CECC/SC 52, Printed boards (former WG 23).

It is based, wherever possible, on the Publications of the International Electrotechnical Commission, and in particular on IEC 326-9, Printed boards, Part 9: Specification for flexible multilayer printed boards with through connections.

The text of the draft based on document CECC(Secretariat)2865 was submitted to the formal vote; together with the voting report, circulated as document CECC(Secretariat)3092, it was approved as EN 123800 on 1992-05-22.

The following dates were fixed:

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1 Introduction

IEC 326-9 is the IEC Standard for flexible multilayer printed boards with through connections. The following document comprises this IEC Standard and in accordance with the Generic Specification EN 123000, the information additionally necessary for printed boards intended to be handled within the CENELEC system for electronic components of assessed quality.

1.1 Scope and object

This document is a Sectional Specification (SS) relating to flexible multilayer printed boards with through connections irrespective of their method of manufacture, when they are ready for the mounting of the components. It defines the characteristics to be assessed and the test methods to be used for capability approval testing and for quality conformance inspection (lot-by-lot and periodic inspection).

1.2 Related documents

IEC 68	Basic environmental testing procedures
IEC 97	Grid system for printed circuits
IEC 194	Terms and definitions for printed circuits
IEC 249	Metal-clad base materials for printed circuits
IEC 326-1	Printed boards, instructions for the specification writer
IEC 326-2	Test methods for printed boards
IEC 326-3	Recommendations for the design and the use of printed boards
IEC 326-9	Specification for flexible multilayer printed boards with through-connections

2 General

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This Sectional Specification (SS) applies to flexible printed boards with through-connections and is intended as a basis for the preparation of a

- Capability Detail Specification (CapDS) applying to specific materials e.g. according to IEC 249-2, and to be used for Capability Approval Procedures. It may be necessary to have a CapDS for each type of material. A CapDS may be prepared by an international or national body or by a manufacturer (see also CECC 00 111 / IV).
- Customer Detail Specification (CDS) for the custom built printed boards, according to clause 5 of EN 123000. The CDS will normally be written by the customer and allocated a number within his own system. Further details are also given in EN 123000 and CECC 00 114 / III.

Table I contains the basic characteristics that will normally be important for flexible multilayer printed boards with through connections and makes reference to the appropriate tests to verify these characteristics.

Table II contains the additional characteristics that may be important for certain flexible multilayer printed boards with through connections and / or certain applications and makes reference to the appropriate tests to verify these characteristics. Where necessary, the relevant specification may quote characteristics and tests from this Table II.

Where additional details for a test have to be specified in the relevant specification, this shall be indicated by "*" in the relevant column. These details shall then be specified in accordance with CECC 00 010 (IEC 326-2).

Table III contains the capability test programme. A specified composite test pattern (CTP) is used as a capability qualifying component.

Table IV contains the information for the quality conformance inspection.

The tables are not intended to prescribe a test sequence. The tests may be carried out in any sequence, unless otherwise specified.

3 Test specimen

A test specimen, which may be specifically designed for this purpose (composite test pattern), or taken from production (production board), which is used for verifying capability in accordance with the relevant Generic Specification. A suitable composite test pattern is shown in Figures 3a, 3b, 3c, 3d, 3e, 3f, 3g, 3h, 4a, and 4b to this document.

3.1 Capability Approval

3.1.1 Basic capability

The test shall be carried out on the composite test pattern given in clause 8.

3.1.2 Additional capability

Clause 3.5.3 of EN 123000 shall apply. For multiple arrangements; see also clause 8.

3.1.3 Maintenance of capability approval

Clause 3.8 of EN 123000 shall apply.

3.2 Quality conformance inspection

Unless otherwise specified, production boards and / or specially designed test patterns may be used for carrying out tests for the lot-by-lot and the periodic inspection.

Where specifically designed test patterns shall be used they may be included in the panel. They may be based on the appropriate pattern of the composite test pattern; see clause 8. Consultation between manufacturer and customer will usually be necessary.

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4 Relevant specification

The term "relevant specification" means a product specification for an actual printed board., i.e. a CDS as well as a CapDS applied to a specific material and technique, as applicable.

The relevant specification shall contain all information necessary to define the printed board clearly and completely. The recommendations given in IEC 326-3 shall preferably be followed.

Care should be taken to avoid unnecessary prescription. Permissible deviations should be stated where necessary, nominal values without tolerances or simple maxima or minima shall be given where sufficient. Where tolerances are necessary for certain areas or parts of the printed board only, they shall be applied and restricted to those areas or parts.

If there are several possibilities of presentation of tolerance classes etc., the selections given in IEC 326-3 shall preferably be applied.

In cases of discrepancy between the CDS and other pertinent specifications (e.g. BS, GS, or SS), the CDS shall prevail.

5 Characteristics of the printed boardBasic characteristics, see table I
Additional Characteristics, see table II**Table I - Basic characteristics (mandatory assessments)**

Characteristics	Test No. IEC 326-2	Additional Tests	Specimen of Composite Test Pattern	Requirements	Remarks
<u>General Examination</u>					
<u>Visual Examination</u>					
Conformity and Identification	1		Complete printed board or composite test pattern	Pattern, marking, identification, material and finishes shall comply with the relevant specification. There shall be no apparent defects.	
Appearance and Workmanship	1a		Complete printed board or composite test pattern	The boards shall appear to have been processed in a careful and workmanlike manner, in accordance with good current practice.	
Plated-through holes			Complete printed board or composite test pattern	Plated-through holes shall be clean and free from inclusions of any sort that could affect component insertion and solderability. Total area of the voids shall not exceed 10% of the total wall area. The largest dimension shall not exceed 25% of the hole circumference in the horizontal plane and 25% of the thickness of the board in the vertical plane. Plated-through holes shall have no plating voids at the interface of the hole wall and the conductive pattern or internal layer ring.	

* see clause 2

Characteristics	Test No. IEC 326-2	Additional Tests	Specimen of Composite Test Pattern	Requirements	Remarks
Board Edges	1c			<p>The interface shall be considered to extend into the hole below the surface of the board a distance of 1,5 times the total copper thickness on the surface or to be two times the inner layer thickness at the level of contact ring.</p> <p>Resin smear at the edge of the clad copper and the continuous plated copper is permitted provided the smear does not interrupt electrical continuity.</p>	
Eyelets	1a		Complete printed board or composite test pattern	<p>There shall be no circumferential cracks of the copper, or circumferential separation of the copper from the wall of the plated-through hole</p> <p>Holes with plating voids shall not exceed 5% of the total number of plated-through holes</p> <p>The edges of the board and internal cut-outs shall be clean cut without tears or nicks</p>	

Characteristics	Test No. IEC 326-2	Additional Tests	Specimen of Composite Test Pattern	Requirements	Remarks
<p>Bonding Conductor to Substrate</p> <p>Bonding Coverlayer to substrate and pattern</p>			<p>Complete printed board or composite test pattern</p> <p>Complete printed board</p>	<p>There shall be no separation of the conductors from the substrate by apparent blisters or wrinkles other than those permitted in the material specification.</p> <p>The bonding shall appear to be complete and uniform, Minor delaminations are permitted in the following positions:</p> <p>a) At random locations away from the conductors. Such delaminations shall have an area not exceeding 5 mm² each and shall be more than 0,5 mm from the edges.</p> <p>b) Along conductor edges. Such delaminations shall not infringe upon the design spacing between the conductors by more than 20% of the design width by visual estimation. (see figure 5)</p> <p>There shall be a minimum continuous bonding width of 0,5 mm between adjacent conductors. There shall be no delamination with conductor spacings less than 0,5 mm.</p>	
Conductor defects	1b		Complete printed board or composite test pattern	There shall be no cracks or breaks. Imperfections such as voids or edge defects are permissible, provided that the conductor width or leakage path between	Where necessary, this shall be verified

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Characteristics	Test No. IEC 326-2	Additional Tests	Specimen of Composite Test Pattern	Requirements	Remarks
Particles between conductors	1b or 1c		Complete printed board or composite test pattern	conductors is not reduced by more than that specified in the relevant specification, e.g. 20% or 35%. (see figure 1) Residual metallic particles are permissible provided that the leakage path is not reduced by more than 20% or to less than the distance required for circuit voltage.	by dimensional examination using test 2a. Where necessary, this shall be verified by dimensional examination using test 2a.
<u>Dimensional Examination</u>					
Board Dimensions	2		Complete printed board or composite test pattern	Dimensions and tolerances shall comply with the relevant specification. The nominal board thickness shall comply with the relevant specification.	
Board Thickness in the zone of edge board contacts.	2		K	The total board thickness shall comply with the relevant specification.	Total board thickness and tolerances shall be specified in accordance with IEC 321
Holes	2		Complete printed board or composite test pattern	Nominal diameters and tolerances of mounting holes and of component holes shall comply with the relevant specification. The nominal diameter of plated-through holes used for through connections only shall comply with the relevant specifications.	A recommended range of holes and tolerances is given in IEC 326-3. Accurate measurement is not necessary since deviations are not important.

Characteristics	Test No. IEC 326-2	Additional Tests	Specimen of Composite Test Pattern	Requirements	Remarks
Access holes	2		Complete printed board or composite test pattern	Registration of an access hole including any influence of the adhesive flow in the coverlayer with relation to the relevant land on the base material shall be such that any overlapping will not reduce the effective land dimensions to less than the minimum stated in the relevant specification. (see figure 6b)	Recommended min. effective land at any point around the hole: Plain hole: 0,15 mm Plated-through hole; 0,10 mm
Slots, notches	2		Complete printed board or composite test pattern	The dimensions shall comply with the relevant specification.	
Conductor width	2		Complete printed board or composite test pattern	The width shall comply with any specific dimensions given in the relevant specification.	If no tolerances are stated, the coarse deviation given in IEC 326-3 shall apply.
	2a			Imperfections such as voids or edge defects are permissible, provided the conductor width is not reduced by more than specified in the relevant specification, e.g. 20% or 35%. The length L of a defect shall not be greater than the conductor width S or 5 mm, whichever is the smaller (see figure 1)	
Spacing between conductors	2		Complete printed board or composite test pattern	The spacing between conductors shall comply with any specific dimensions given in the relevant specification.	
Misalignment of hole and land	1a 2a		Complete printed board or composite test pattern	There shall be no interruption of the land. There shall be no breakout at the	

Characteristics	Test No. IEC 326-2	Additional Tests	Specimen of Composite Test Pattern	Requirements	Remarks
Positional tolerance of hole centres			Complete printed board or composite test pattern	junction of the land and the conductor. The hole centres shall be within any deviation specified in the relevant specification.	
<u>Electrical Tests</u>					
Resistance					
Change in resistance of plated-through holes, thermal cycling	3c of IEC326-2a		D	The requirements of the relevant specification shall be met.	Not applicable to polyester materials.
Eyelets				Under consideration.	
Short circuit	4a	*	Complete printed board or composite test pattern		
<u>Insulation Resistance</u>					
Preconditioning	6 18a	*		The insulation resistance shall comply with the relevant specification.	Insulation resistance shall be measured before and after environmental conditioning and at elevated temperature
Measurement at standard atmospheric conditions	6	*			
Conditioning IEC 68-2-3		*			Applicable conditioning to be specified in the relevant specification.
Test Ca damp heat steady state, or IEC 68-2-38, Test Z/AD, composite temperature / humidity cyclic test					
Measurement at elevated temperature	6	*			Not applicable to polyester materials.
Surface Layers	6a		E or J		
Internal Layers	6b		E or J		
Between layers	6c		M		

* see clause 2