

TECHNICAL REPORT



IEC nanoelectronics standardization roadmap

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

IEC NANOELECTRONICS STANDARDIZATION ROADMAP

FOREWORD

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IEC 62834, which is a technical report, has been prepared by IEC technical committee 113: Nanotechnology standardization for electrical and electronic products and systems.

The text of this technical report is based on the following documents:

Enquiry draft	Report on voting
113/161/DTR	113/197/RVC

Full information on the voting for the approval of this technical report can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

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INTRODUCTION

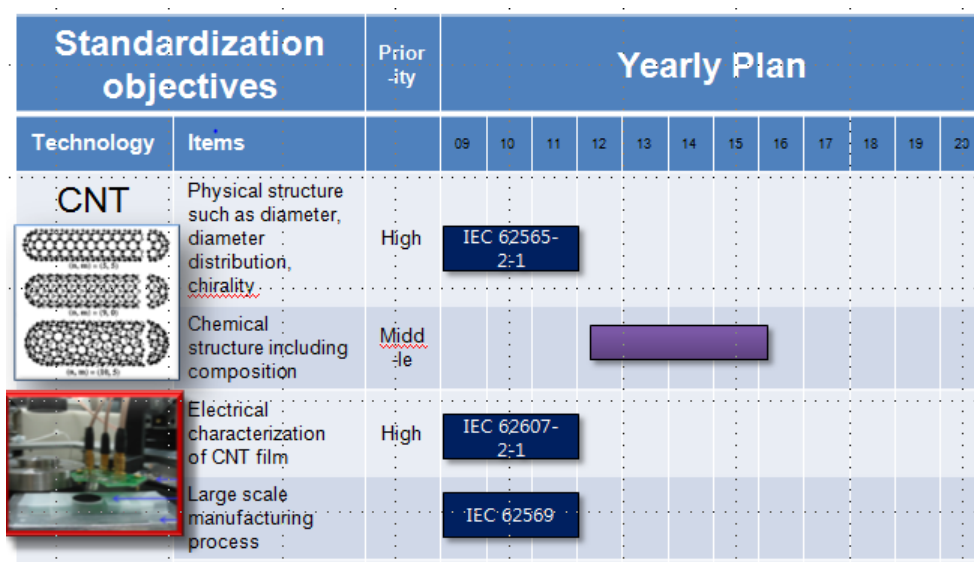
In IEC TC113 a survey on nano-electrotechnical standardization needs was initiated by the National Institute of Standards and Technology (NIST) in the USA to establish a strategy of standardization priorities regarding the nanoelectronics area. A TC 113 Project Team was then organized to build a “Nanoelectronics standards roadmap”. This document covers nanoscale devices and nanomaterials which will be in the market or are already commercialized for nanoelectronic applications. When selecting the devices and materials to be included in the roadmap, the Project Team considered their market size and the period of time needed for their technology development. Because most of the experts in TC 113 are from an electronics background, the first version (Part A) of this roadmap covers electronics and ICT (information and communication technology) rather than energy or convergence technologies.

Regarding nanomaterials, roadmaps for carbon nanotubes (CNT), graphene, nanofibres, nanoparticles and quantum dots were established. For each material there are several detailed items that need to be standardized, including physical properties and characterization methods. Some of such standards are already under development in TC 113, such as IEC 62565-2-1 and IEC 62569.

In the nanoelectronics device roadmap, nanoscale contacts, CNT interconnects, three-dimensional nanotransistors, nanoscale memory devices, and molecular devices were selected. Though the priority was on memory devices and new types of transistors, molecular devices were included in this version considering the impact of this technology.

The time span of the roadmap is important in order to cover the technology which may be realized in a certain period of time. However, with regard to nanoelectronics development, little information on the average technology development period is available at this stage. Thus TC 113 set the span of the roadmap up until the year 2020 to show the starting point of standardization tasks and the end of activity.

As the format should give insights and detailed information to the user of the roadmap, the Gantt chart format was used, including photos (see Figure 1). When a new version of the roadmap is prepared, TC 113 will develop a new format in parallel, which can give more accurate information to users.



IEC 2281/13

Figure 1 – Roadmap format

IEC NANO ELECTRONICS STANDARDIZATION ROADMAP

1 Scope

This Technical Report covers nanomaterials and nanoscale devices. To achieve consensus more quickly when building the roadmap, an ICT “More Moore” area has been adopted for the priority standardization items of this first version, as shown in Table 1.

Table 1 – Categories and detail potential products

Categories		Detail potential products	Version 1
Nanomaterials	Zero-dimensional nanomaterial	Nanoparticles/Nanopowders Quantum dot	√ √
	One-dimensional nanomaterial	Carbon nanotube Nanowire (III-V, II-VI, ZnO)	√
	Two-dimensional nanomaterial	Nanofunctional thin film Nanostructural film Graphene	√
	Three-dimensional nanomaterial	Nanopore materials Nanocomposites	
Nanoscale devices	Nanoelectronic devices	Nanoscale non-volatile memory devices	√
		1- and 3-dimensional nanoscale transistors	√
		Single electron transistor	√
		Nanoscale logic devices	√
		Nanoscale interconnection Post-CMOS signal processing	√
Nanoscale optical devices	Silicon optical devices Photonic crystal optical devices All-optical logic devices Quantum dot optical devices		
Nanoscale magnetic devices	Highly integrated memory devices High-speed magnetic logic devices	√ √	
Molecular devices	Molecular logic device Molecular memory device Molecular sensors Molecular mechanics devices Molecular optical devices	√ √	
Nanomaterials-based flexible devices	Nanomaterials-based flexible devices Nanomaterials-based displays		
Nanofabrication processes, equipment measurement	Nanofabrication process	Nano lithography Self-assembly	
	Nanoscale metrology and simulation	SPM	

2 Background

2.1 General

The development of an “IEC nanoelectronics standardization roadmap” is necessary to establish a common standardization strategy in the area of nano-electrotechnology. The first step for determining standardization needs was carried out through a survey conducted by the National Institute of Standards and Technology (NIST) in the USA. The goal of this survey was to begin building a consensus among members of the nano-electrotechnology community on a framework leading to inputs for consideration in standards development. The results from the survey were reported in 2009. [1]¹

Nevertheless, a standardization roadmap requires more than a framework of priority needs for standards established by the foregoing survey results. It requires a vision from technical experts as to which products will be developed in the future and which technologies will be available to realize them. That means a vision of market needs and technology availability.

Figure 2 shows technologies and their related markets. It will be possible to make the roadmap for all technology areas, such as electronics, information and communications technology (ICT), bio, energy etc.. In this version, we focus on the existing information available to develop a roadmap, for example the area of “More Moore”, including nanotechnologies and nanomaterials, which makes it possible to achieve technology innovation in terms of integrity and high performance. The areas not covered by this document will be provided separately after considering demands for standardization.

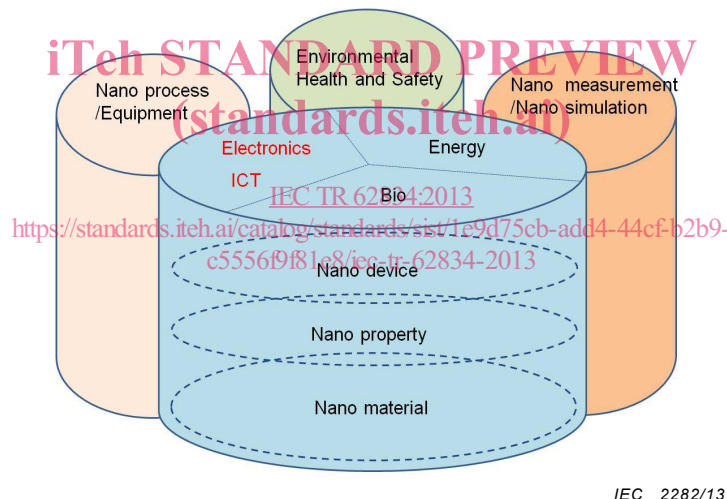


Figure 2 – Technologies and related products

The interaction between technology, product and the standardization roadmap is illustrated in Figure 3. Most of the stakeholders in nanotechnology have their own roadmap, and from time to time, publicly available roadmaps are under development. The problem here is that company-owned roadmaps are not available to the IEC and there is no guarantee that publicly available roadmaps they will be actualized on a regular basis. Therefore, IEC TC 113 decided to develop its own integrated roadmap based on its view of technologies, products and standards.

From an IEC strategic point of view, such a project could have some very important advantages. Assuming that the roadmap would be structured in line with the IEC technical committee structure, it would provide an effective planning tool for the IEC as a whole. It would support the work of IEC TC 113 by providing the relevant market information to establish and review its programme of work. If the IEC owns the product/technology/standards roadmap and the roadmap update process, it can be used in areas of interest other than the

¹ Numbers in square brackets refer to the Bibliography.

production of standards. Last but not least, the roadmap would demonstrate that the IEC has a position in nanotechnology which is agreed among relevant industry stakeholders.

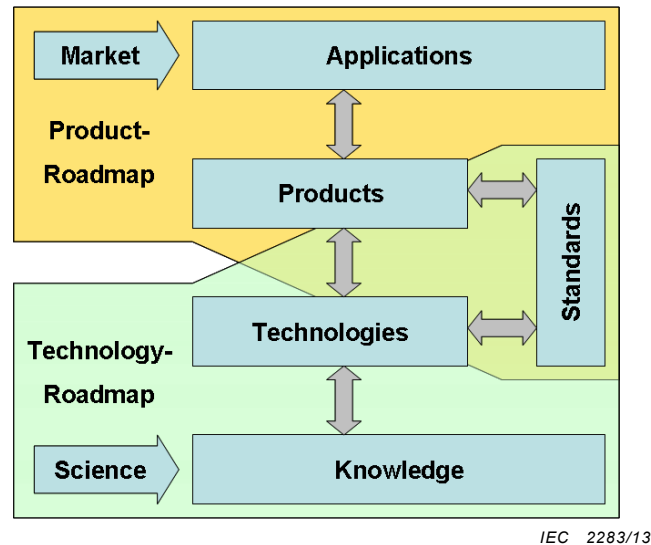


Figure 3 – Interaction of product, technology and standardization roadmaps

2.2 Classification of nanotechnology

2.2.1 General

The nanotechnology industry can be largely divided into nanomaterials, nanoscale devices, nano-biotechnology, the nanofabrication process, equipment and measurement areas.

2.2.2 Nanomaterials

These are materials that control, combine and mix materials at the nanoscale to remarkably improve physical properties and to create new physical properties and functions. They apply to mechanics, energy, environment and IT-related systems.

2.2.3 Nanoscale devices

These are devices that can perform special functions using unique characteristics of nanoscale materials.

2.2.4 Nano-biotechnology

This is an area of science and technology operating, analysing and controlling a system combining biosystems with nanomaterials and/or nanoscale devices.

2.2.5 Nanofabrication process – Equipment – Measurement

This is manufacturing technology of nanoscale processes (under 100 nm) that form nanoscale parts and devices, as well as equipment technology and performance measuring of nanomaterials, devices, subsystems and systems.

3 Current status and prospects

3.1 Related markets

Currently, zero-dimensional nanopowders, TiO₂ nanopowders for photocatalyst and anti-bacterial silver nanoparticles are widely commercially available materials. The market size of TiO₂ nanopowders for photocatalyst is 10 million US dollars (2005) worldwide.

The tool market is the largest area of application and the market of nanostructure thin film materials internationally. Although it is difficult to forecast the status of world tool markets, the world market of diamond cutting tools is estimated to be about 13 billion US dollars based on information from 1999.

The largest part of the market of mass production equipment is in nanostructure thin film materials, as well as tools, moulds and various mechanic parts. At present, equipment companies operating worldwide (e.g. Kobe, Cemecon, Balzers, Huazer²) have developed their equipment and handled materials, processes and patents collectively.

The market of nanocomposite materials is led by polymer matrix nanocomposites, with a world market size of about 5 billion – 7 billion US dollars in 2009. The market of ceramic nanocomposite materials was about 2,5 billion US dollars after 2010.

The market has shown a consistent growth trend due to continuous growth in several ten gigabyte (GB) high capacity flash memory and DRAM components. Given the vague overlapping area of existing semiconductor and nanotechnology markets, it is important to analyse characteristics, to standardize the modelling and design methodology and to obtain circuit IP based on nanoelectronic devices to address the nanotechnology market.

III-V compound semiconductors including nitride-based nanostructures will be used for light-emitting diodes (LEDs), and their scope of application continues to expand, including portable appliances, LCD (liquid crystal display) backlight, automobile lighting.

The most active area is the LED market, and many players are striving to launch into the general lighting market. The LED lighting industry is expected to replace almost all lighting areas such as traffic signals, construction and automobiles as well as LCD backlighting and general lighting. In addition, development and commercialization of quantum dot light receiving devices and infrared devices are expected to bring about a revolution in the area of image sensors. Solar energy is an area that has seen double-digit growth rates due to worldwide energy issues.

The flexible electronic device market is expected to grow rapidly from 16 million US dollars seen in 2008 to 1,314 million US dollars in 2013. The market is expected to be led by small and medium applications focusing on new mobile phones.

Since the world market of ITO (indium tin oxide) transparent electrode thin films for 2006 was 592 million US dollars, and that of touch panel, EL (electroluminescent) backlight and transparent conductivity films for 2006 was 90 million US dollars, the percentage share of transparent conductive electrode films was about 0,96% of flat panel display industry revenues in 2006.

The market for flexible transparent electrodes is expected to grow up to 1,929 million US dollars in 2015 for the display market, and it is expected that it may be applied to electrode materials for RFID (radio-frequency identification) and for solar cells.

3.2 Technology development directions for nanomaterials

3.2.1 General

Improving efficiency is important when developing a photocatalyst by doping of various metal oxides, high density coating films and by controlling particle size in the case of zero-dimensional nanomaterials.

For silver nanopowder, the control of silver nanoparticle size in the matrices is important when used for sterilization and anti-bacterial purposes.

Nanowires are used for transistors, sensors and other applications such as field emission display and NEMS (nanoelectromechanical systems).

The industrialization of nanomaterials is expected in optoelectronic devices and biotechnology areas.

² When companies are referenced specifically in this technical report, this information is given for the convenience of users of this document and does not constitute an endorsement by IEC.