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# TECHNICAL SPECIFICATION



Nanomanufacturing ekey control characteristics EVIEW Part 5-1: Thin-film organic/nano electronic devices – Carrier transport measurements

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# Nanomanufacturing - Key control characteristics EVIEW Part 5-1: Thin-film organic/nano electronic devices - Carrier transport measurements

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# INTERNATIONAL ELECTROTECHNICAL COMMISSION

# NANOMANUFACTURING – KEY CONTROL CHARACTERISTICS –

# Part 5-1: Thin-film organic/nano electronic devices – Carrier transport measurements

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IEC TS 62607-5-1, which is a technical specification, has been prepared by IEC technical committee 113: Nanotechnology standardization for electrical and electronic products and systems.

The text of this technical specification is based on the following documents:

Enquiry draft	Report on voting
113/212/DTS	113/221/RVC

Full information on the voting for the approval of this technical specification can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 62607 series, published under the general title Nanomanufacturing key control characteristics, can be found on the IEC website.

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#### INTRODUCTION

Organic/nano thin-film devices have many attractive features such as being light-weight and flexible, and having a low-cost, low-temperature fabrication process. Organic/nano electronic devices have been widely researched by academic institutions, research institutes, and materials and device industries. One of their possible applications is therefore expected to be in flexible and rollable devices. Many thin-film transistors based on organic semiconductor materials, called organic thin-film transistors (OTFTs), are expected to be mounted on organic electroluminescence display to drive each organic light-emitting diode pixel circuit. These OTFTs are also promising candidates for molecular nanoelectronics.

OTFTs show a relatively smaller carrier mobility (thin-film mobility: at most 10 cm<sup>2</sup>/Vs, but usually less than 1 cm<sup>2</sup>/Vs) compared with other thin-film transistors based on inorganic semiconductors (silicon, III-V compounds, metal oxides). Carrier transport properties such as thin-film mobility and thin-film carrier concentration in OTFTs are usually measured by simply applying the device physics of silicon metal-oxide-semiconductor transistors to OTFTs. Both the intrinsic bulk mobility of organic semiconductors and extrinsic effects such as contact resistance, carrier trap, interface, and surface state can limit thin-film mobility in OTFTs. Therefore, reliable methods of evaluating carrier transport properties for nanometer-scale thin-film materials have not yet been established and urgently need to be developed.

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# NANOMANUFACTURING – KEY CONTROL CHARACTERISTICS –

# Part 5-1: Thin-film organic/nano electronic devices – Carrier transport measurements

# 1 Scope

This part of IEC 62607, which is a Technical Specification, provides a standardized sample structure for characterizing charge transport properties in thin-film organic/nano electronic devices and a format to report details of the structure which shall be provided with the measurement results. The standardized OTFT testing structure with a contact-area-limited doping can mitigate contact resistance and enable reliable measurement of the charge carrier mobility. The purpose of this Technical Specification is to provide test sample structures for determining the intrinsic charge transport properties of organic thin-film devices. The intention is to provide reliable materials information for OTFTs and to set guidelines for making test sample structures so that materials information is clear and consistent throughout the research community and industry.

# 2 Normative references STANDARD PREVIEW

The following documents, in whole or in part, are normatively referenced in this document and are indispensable for its application. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

https://standards.iteh.ai/catalog/standards/sist/8bad0fce-01f8-47dd-aeee-

IEC 60050 (all parts), *International Electrotechnical Vocabulary* (available at http://www.electropedia.org/)

IEC 62860, Test methods for the characterization of organic transistors and materials

# 3 Terms, definitions and abbreviations

For the purposes of this document, the terms and definitions given in IEC 60050-521 as well as the following apply.

### 3.1 Terms and definitions

3.1.1 organic thin-film transistor OTFT

field-effect transistor that has a conduction channel made of thin films consisting of organic compounds

## 3.1.2

### thin-film mobility

charge carrier mobility of the conduction channel (the semiconductor layer) in an OTFT

# 3.1.3

# contact-area-limited doping

doping at around interface regions between the source and drain electrodes and the conduction channel in an OTFT

### 3.1.4

#### channel resistance

electrical resistance which comes from the conduction channel induced by applying gate voltages in a field-effect transistor

#### 3.1.5

#### contact resistance

electrical resistance obtained by subtracting the channel resistance from the total electrical resistance between the source and drain electrodes in a field-effect transistor

Note 1 to entry: Main components of the contact resistance are electrical leads and carrier injection barriers at the interface between the source electrode and the semiconductor layer.

#### 3.1.6

#### bottom-gate, bottom-contact device

field-effect transistor with the following structures:

- the gate electrode is located between the gate dielectric and the substrate;
- the source and drain electrodes are located directly on top of the substrate, and adjacent to the conduction channel-gate dielectric interface

#### 3.1.7

#### bottom-gate, top-contact device

field-effect transistor with the following structures:

- the gate electrode is located between the gate dielectric and the substrate;
- the source and drain electrodes are located on top of the semiconductor layer

#### 3.1.8

## top-gate, bottom-contact device IEC TS 62607-5-1:2014

field-effect transistor, with the following structures: /sist/8bad0fce-01f8-47dd-acce-

- the gate electrode is located farthest away from the substrate;
- the gate dielectric is located between the gate electrode and the semiconductor layer;
- the source and drain electrodes are located directly on top of the substrate, and adjacent to the conduction channel-gate dielectric interface

#### 3.1.9

#### top-gate, top-contact device

field-effect transistor with the following structures:

- the gate electrode is located farthest away from the substrate;
- the gate dielectric is located between the gate electrode and the semiconductor layer;
- the source and drain electrodes are located on top of the semiconductor layer

#### 3.2 Symbols and abbreviated terms

- OTFT organic thin-film transistor
- BGBC bottom-gate, bottom-contact
- BGTC bottom-gate, top-contact
- TGBC top-gate, bottom-contact
- TGTC top-gate, top-contact

F4TCNQ 2,3,5,6-tetrafluoro-7,7,8,8-tetracyanoquinodimethane

# 4 Sample structures of OTFTs

### 4.1 Typical device structures of OTFTs

Several different device structures on OTFTs are possible, depending on the position of the source-drain and gate electrodes. Figure 1 illustrates two typical device structures: a bottom-gate, top-contact (BGTC) structure and a bottom-gate, bottom-contact (BGBC) structure. BGTC devices usually show better performance in comparison with BGBC devices. In comparison, the BGBC structure is more suitable for high-density device integration. However, high contact resistance is a common and serious problem in OTFTs regardless of the device structure, because the high contact resistance leads to the underestimation of the intrinsic field-effect channel mobility in OTFTs [1],[2]<sup>1</sup>.

- 8 -

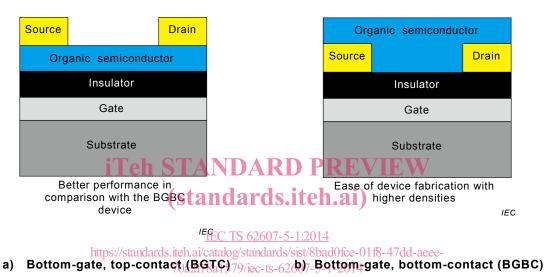
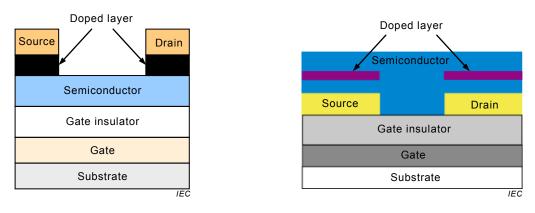


Figure 1 – Typical device structures of OTFTs

### 4.2 Contact-area-limited doping in OTFTs

Contact-area-limited doping is effective for increasing the drain current in OTFTs [2], [3], [4], [5], [6], [7]. In this type of doping, as shown in Figure 2, acceptor (or donor) doped layers are formed at the interface regions between the active semiconductor layer and the contact electrode. These doped layers cause a decrease in the contact resistance, resulting in an increase in the drain current.

<sup>&</sup>lt;sup>1</sup> Figures in square brackets refer to the Bibliography.



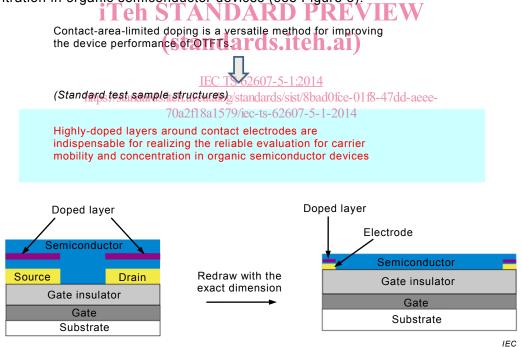
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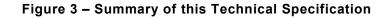






Contact-area-limited doping is a versatile method for improving the device performance of OTFTs. In other words, the effective thin-film mobility in the channel regions of OTFTs greatly depends on extrinsic effects such as structure and the electronic properties of the contact electrode area. Therefore, materials information on organic semiconductor films is not consistent throughout the research community and industry at present. This fact has led to this technical specification proposal for standard test sample structures. Namely, highly doped layers around contact electrodes are indispensable for reliably evaluating carrier mobility and concentration in organic semiconductor devices (see Figure 3).





### 5 Appropriate data format

A blank detail specification for OTFT test samples is an appropriate form for this Technical Specification (see Table 1). Items such as contact structure and contact electrode materials should be included in this Technical Specification.