

INTERNATIONAL STANDARD

NORME INTERNATIONALE

**Ferrite cores – Guidelines on the limits of surface irregularities –
Part 1: General specification**

**Noyaux ferrites – Lignes directrices relatives aux limites des irrégularités de
surface –
Partie 1: Spécification générale**

IEC 60424-1:2015

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CONTENTS

FOREWORD.....	3
INTRODUCTION.....	5
1 Scope.....	6
2 Normative reference.....	6
3 Terms and definitions.....	6
4 Examples of irregularities.....	7
5 Locations and functions of core parts and surfaces.....	7
5.1 General.....	7
5.2 Mating surfaces.....	7
5.3 Centre post.....	8
5.4 Outer walls or legs.....	8
5.5 Back wall, bottom and back surfaces.....	8
5.6 Wire-slot area.....	8
5.7 Wire-way area.....	8
5.8 Clamping recess area.....	8
6 Area and length reference for visual inspection.....	8
7 Limits of surface irregularities.....	11
7.1 General.....	11
7.2 Chips and ragged edges.....	11
7.3 Cracks.....	11
7.4 Flash.....	11
7.5 Pull-outs.....	11
7.6 Pores.....	11
7.7 Crystallites.....	11
8 Sectional specifications.....	11
Bibliography.....	13
Figure 1 – Examples of surface irregularities.....	7
Figure 2 – Location of main core parts and surfaces – Example of RM-core type.....	7
Table 1 – Area and length reference for visual inspection.....	10
Table 2 – IEC 60424 sectional specifications.....	12

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**FERRITE CORES –
GUIDELINES ON THE LIMITS OF SURFACE IRREGULARITIES –****Part 1: General specification**

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International Standard IEC 60424-1 has been prepared IEC technical committee 51: Magnetic components and ferrite materials.

This second edition cancels and replaces the first edition published in 1999. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) addition of pores in 3.5 and crystallites in 3.6.

The text of this standard is based on the following documents:

FDIS	Report on voting
51/1107/FDIS	51/1123/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 60424 series, published under the general title *Ferrite cores – Guidelines on the limits of surface irregularities*, can be found on the IEC website.

Future standards in this series will carry the new general title as cited above. Titles of existing standards in this series will be updated at the time of the next edition.

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- reconfirmed,
- withdrawn,
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- amended.

INTRODUCTION

Due to the method of manufacture and the physical nature of the products, ferrite cores can be expected to exhibit some degree of physical irregularities such as chips, ragged edges, cracks, flashing, and pull-out.

The permissible extent of these surface irregularities will depend on the type, position and size of the defect and on the function of the core. Thus, in order to establish limits of surface irregularities for a given series of ferrite cores, for example RM-cores, pot-cores, E-cores, U-cores and ring-cores, it is necessary to prepare a particular specification for each, setting out in detail the permissible extent of the various types of irregularities.

All surfaces of the core should be clean and free from loose ferrite particles or any other foreign matter. This is more critical for mating surfaces that should make good contact with one another. Stains, discolorations, surface crazing or crystallization are acceptable if they do not affect the normal performance of the core. The irregularities described below are considered as being detectable without the use of any magnifying equipment.

Surface irregularities limits are set for control of cosmetic appearance, and not for control of magnetic performance. Surface irregularities do not substantially affect core magnetic function, nor do they affect reliability. Reliability should be assessed for wound magnetics, rather than for cores alone. See IEC 60401-3 for more details concerning the reliability of ferrite cores and devices built with them.

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WITHDRAWN

FERRITE CORES – GUIDELINES ON THE LIMITS OF SURFACE IRREGULARITIES –

Part 1: General specification

1 Scope

This part of IEC 60424 gives guidelines on the allowable limits of surface irregularities of ferrite cores.

This standard should be considered as a general specification useful in the dialogue between ferrite core manufacturers and customers about surface irregularities.

2 Normative reference

The following documents, in whole or in part, are normatively referenced in this document and are indispensable for its application. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

Void.

3 Terms and definitions

For the purposes of this document, the following terms and definitions apply.

3.1 chips and ragged edges

areas with missing surface material that are generally caused by mechanical impact during handling

3.2 crack

surface irregularity which has a width much smaller than its length, and penetrates into the core

3.3 flash

sharp feather-edge wall extending beyond the intended contour surface of the core

3.4 pull-out

consequence of the removal of a surface layer of the core due to die “sticking”

3.5 pores

holes left on the surface of cores after sintering and surface finishing

3.6 crystallites

grains of abnormal size distinguishable on the surface, often with sparkling facets

4 Examples of irregularities

Figure 1 shows different examples of surface irregularities on an RM-core.

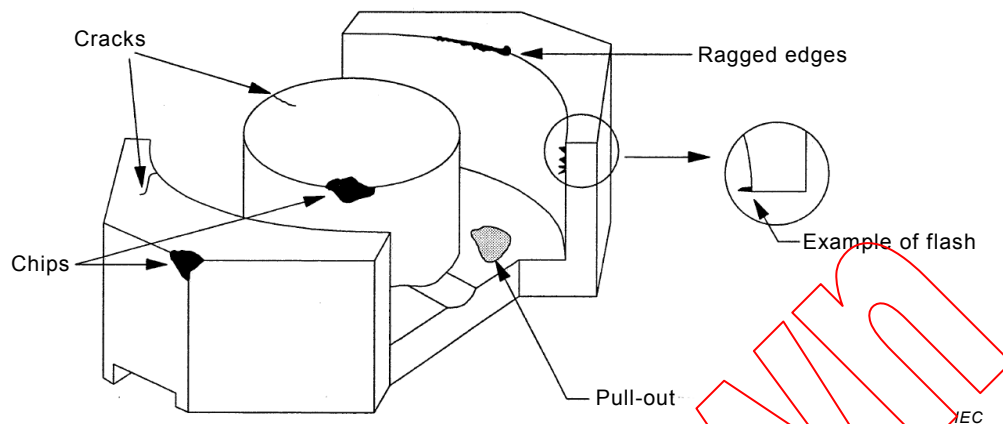


Figure 1 – Examples of surface irregularities

5 Locations and functions of core parts and surfaces

5.1 General

Figure 2 shows the location of main core parts and surfaces on an RM-core.

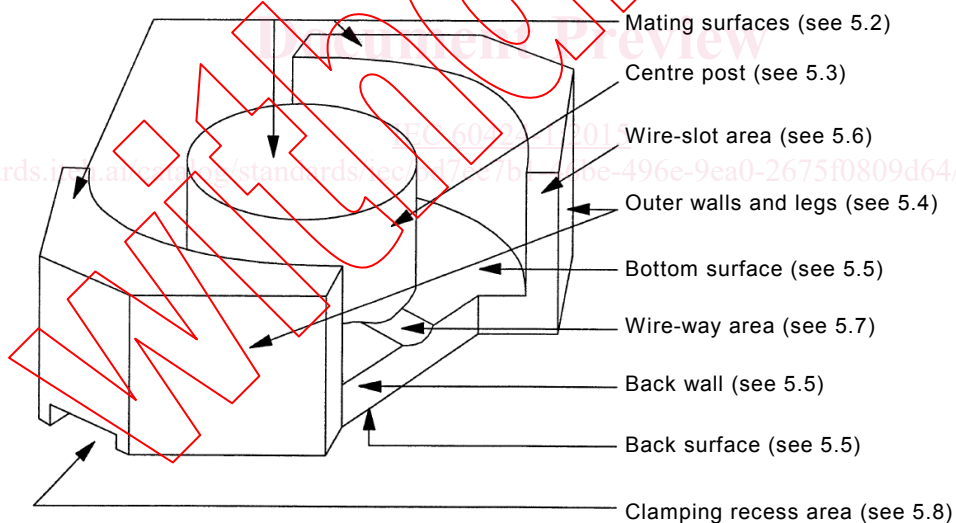


Figure 2 – Location of main core parts and surfaces – Example of RM-core type

5.2 Mating surfaces

These surfaces are generally ground in order to limit the residual air gap between the two core halves. Consequently, irregularities on these surfaces have to be considered as major ones, and carefully evaluated with regard to their influence on the magnetic properties of the complete circuit.

5.3 Centre post

This should be considered as the most important part of the core due to its function of carrying the total flux generated by the winding. The centre post of ferrite cores is generally circular (with or without a hole) or rectangular.

5.4 Outer walls or legs

The main function of the outer walls (for example pot-cores) or the outer legs (for example E-cores) is to guide the magnetic flux in a closed magnetic circuit.

5.5 Back wall, bottom and back surfaces

The back wall has the same magnetic function as the outer walls or legs; it may include wire-slots and wire-way areas (for example on RM-cores), the shapes and dimensions of which are dictated by the winding and isolation requirements.

Besides accommodating clamping, the back surface (ground or not) serves as a reference plane for grinding the mating surface to achieve its required parallelism, flatness and smoothness.

5.6 Wire-slot area

The wire-slot area is the lateral area of the outer walls, interfacing with the cut-out portion.

5.7 Wire-way area

The wire-way area is located on the bottom (inside) surface of the back wall, also called the floor. The wire-way runs radially from the centre post to the edge of the back wall, centred with respect to the wire-slot areas. The purpose of the wire-way is to provide a path for leads to reach from the inside of the coil to terminals on the outside.

5.8 Clamping recess area

The clamping recess areas on the back walls accommodate clamping clips, affording secure mounting, and preventing mechanical interference from the clip above the plane of the back wall.

6 Area and length reference for visual inspection

Irregularities such as chips, cracks and pull-out should be compared to five simple geometries which are shown in Table 1.

Such a table should be used as a reference for evaluation by operators either at the outgoing stage (core manufacturers) or at the incoming inspection (customers).

Other methods than visual inspection can be used, such as computerized optical detection.

NOTE The minimum area and length considered are respectively 0,5 mm² and 1 mm.

Recommended resolutions are as follows:

a) for area

- 0,5 mm² up to 5 mm²
- 1 mm² from 5 mm² to 10 mm²
- 2,5 mm² from 10 mm² to 20 mm²
- 5 mm² from 20 mm² to 50 mm²

b) for length

- 1 mm from 1 mm to 5 mm
- 2,5 mm from 5 mm to 10 mm

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