

SLOVENSKI STANDARD SIST EN 60749-21:2011

01-oktober-2011

Polprevodniški elementi - Mehanske in klimatske preskusne metode - 21. del: Spajkljivost

Semiconductor devices - Mechanical and climatic test methods - Part 21: Solderability

Halbleiterbauelemente - Mechanische und klimatische Prüfverfahren - Teil 21: Lötbarkeit

Dispositifs à semiconducteurs - Méthodes d'essais mécaniques et climatiques - Partie 21: Brasabilité (standards.iteh.ai)

Ta slovenski standard je istoveten Z: EN 60749-21:2011 https://standards.tich.a/catalog/standards/sist/e8639e90-zd40-4317-b7ae-944d47f27d93/sist-en-60749-21-2011

ICS:

31.080.01 Polprevodniški elementi Semiconductor devices in

(naprave) na splošno general

SIST EN 60749-21:2011 en

SIST EN 60749-21:2011

iTeh STANDARD PREVIEW (standards.iteh.ai)

EUROPEAN STANDARD

EN 60749-21

NORME EUROPÉENNE EUROPÄISCHE NORM

August 2011

ICS 31.080.01

Supersedes EN 60749-21:2005

English version

Semiconductor devices Mechanical and climatic test methods Part 21: Solderability

(IEC 60749-21:2011)

Dispositifs à semiconducteur -Méthodes d'essai mécaniques et climatiques -Partie 21: Brasabilité (CEI 60749-21:2011)

Halbleiterbauelemente -Mechanische und klimatische Prüfverfahren -Teil 21: Lötbarkeit (IEC 60749-21:2011)

iTeh STANDARD PREVIEW (standards.iteh.ai)

This European Standard was approved by CENELEC on 2011-05-12. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration sist/e8639e90-2d40-4317-b7ae-944d47f27d93/sist-en-60749-21-2011

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the Central Secretariat or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the Central Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Bulgaria, Croatia, Cyprus, the Czech Republic, Denmark, Estonia, Finland, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, the Netherlands, Norway, Poland, Portugal, Romania, Slovakia, Slovenia, Spain, Sweden, Switzerland and the United Kingdom.

CENELEC

European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

Management Centre: Avenue Marnix 17, B - 1000 Brussels

Foreword

The text of document 47/2082/FDIS, future edition 2 of IEC 60749-21, prepared by IEC TC 47, Semiconductor devices, was submitted to the IEC-CENELEC parallel vote and was approved by CENELEC as EN 60749-21 on 2011-05-12.

This European Standard supersedes EN 60749-21:2005.

EN 60749-21:2011 cancels and replaces EN 60749-21:2005 and constitutes a technical revision. The significant change is the inclusion of Pb (lead)–free backward compatibility.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CEN and CENELEC shall not be held responsible for identifying any or all such patent rights.

The following dates were fixed:

- latest date by which the EN has to be implemented at national level by publication of an identical national standard or by endorsement
- (dop) 2012-02-12
- latest date by which the national standards conflicting with the EN have to be withdrawn

(dow) 2014-05-12

Annex ZA has been added by CENELEC. ND ARD PREVIEW

(standards.iteh.ai)

Endorsement notice

The text of the International Standard IEO 60749-21-2017 Was approved by CENELEC as a European Standard without any modification. 944d47f27d93/sist-en-60749-21-2011

In the official version, for Bibliography, the following notes have to be added for the standards indicated:

 IEC 60068 series
 NOTE
 Harmonized in EN 60068 series.

 IEC 60068-2-69:2007
 NOTE
 Harmonized as EN 60068-2-69:2007 (not modified).

 IEC 60749 series
 NOTE
 Harmonized in EN 60749 series.

 IEC 60749-15:2003
 NOTE
 Harmonized as EN 60749-15:2003 (not modified).

 IEC 60749-20:2008
 NOTE
 Harmonized as EN 60749-20:2009 (not modified).

Annex ZA (normative)

Normative references to international publications with their corresponding European publications

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE When an international publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

| <u>Publication</u> | <u>Year</u> | <u>Title</u> | EN/HD | <u>Year</u> |
|--------------------|-------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|-------------|
| IEC 61190-1-2 | 2007 | Attachment materials for electronic assembly - Part 1-2: Requirements for soldering pastes for high-quality interconnects in electronics assembly | EN 61190-1-2 | 2007 |
| IEC 61190-1-3 | 2007 | Attachment materials for electronic assembly - Part 1-3: Requirements for electronic grade solder alloys and fluxed and non-fluxed solid solders for electronic soldering applications | EN 61190-1-3 | 2007 |

iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN 60749-21:2011

iTeh STANDARD PREVIEW (standards.iteh.ai)



IEC 60749-21

Edition 2.0 2011-04

INTERNATIONAL STANDARD

NORME INTERNATIONALE

Semiconductor devices - Mechanical and climatic test methods - Part 21: Solderability (standards.iteh.ai)

Dispositifs à semiconducteur — Méthodes d'essai mécaniques et climatiques — Partie 21: Brasabilité indards.iteh.ai/catalog/standards/sist/e8639e90-2d40-4317-b7ae-944d47f27d93/sist-en-60749-21-2011

INTERNATIONAL
ELECTROTECHNICAL
COMMISSION

COMMISSION ELECTROTECHNIQUE INTERNATIONALE

PRICE CODE CODE PRIX S

ICS 31.080.01

ISBN 978-2-88912-433-6

CONTENTS

| 1 Scope 6 2 Normative references 6 3 Test apparatus 6 3.1 Solder bath 6 3.2 Dipping device 6 3.3 Optical equipment 7 3.4 Steam ageing equipment 7 3.5 Lighting equipment 7 3.6 Materials 7 3.6.1 Flux 7 3.6.2 Solder 7 3.7 SMD reflow equipment 8 3.7.1 Stencil or screen 8 3.7.2 Rubber squeegee or metal spatula 8 3.7.3 Test substrate 8 3.7.4 Solder paste 9 3.7.5 Reflow equipment ADARD PREVIEW 9 3.7.6 Flux removal solvent 9 4.1 Lead-free backward compatibility 9 4.2.1 General 9 4.2.2 Preconditioning by steam ageing 10 4.2.2 Preconditioning by steam ageing 10 4.2.2 Preconditioning by high temperature storage 11 4.3 Preconditioning by high temperature storage 11 4.3 Procedure for simulated board mounting reflow solderability testing of SMDs | FΟ | FOREWORD4 | | | |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|----------------------|----------|------------------------------------------------------------------------|----|
| 3.1 Solder bath | 1 | Scope6 | | | 6 |
| 3.1 Solder bath | 2 | Normative references | | | 6 |
| 3.2 Dipping device | 3 | | | | 6 |
| 3.2 Dipping device | | | • • | | |
| 3.3 Optical equipment | | _ | | | |
| 3.4 Steam ageing equipment | | | | ~ | |
| 3.5 Lighting equipment | | | • | • • | |
| 3.6.1 Flux | | | | | 7 |
| 3.6.2 Solder | | 3.6 | Materia | als | 7 |
| 3.7.1 Stencil or screen | | | 3.6.1 | Flux | 7 |
| 3.7.1 Stencil or screen | | | 3.6.2 | Solder | 7 |
| 3.7.2 Rubber squeegee or metal spatula | | 3.7 | SMD r | eflow equipment | 8 |
| 3.7.3 Test substrate | | | 3.7.1 | Stencil or screen | 8 |
| 3.7.4 Solder paste | | | 3.7.2 | Rubber squeegee or metal spatula | 8 |
| 3.7.5 Reflow equipment 3.7.6 Flux removal solvent DARD PREVIEW 9 4. Procedure (standards.iteh.ai) 9 4.1 Lead-free backward compatibility 9 4.2 Preconditioning SISTEN 60749-212011 10 4.2.1 General SISTEN 60749-212011 10 4.2.2 Preconditioning by steam ageing 10 4.2.3 Preconditioning by steam ageing 11 4.3 Procedure for dip and look solderability testing 11 4.3.1 General 11 4.3.2 Solder dip conditions 11 4.3.3 Procedure 11 4.4 Procedure for simulated board mounting reflow solderability testing 5MDs 19 4.4.1 General 19 4.4.2 Test equipment set-up 19 4.4.3 Specimen preparation and surface condition 20 4.4.4 Visual inspection 21 5 Summary 22 Figure 1 – Areas to be inspected for gullwing packages 15 Figure 2 – Areas to be inspected in rectangular components (SMD method) 17 Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method) 18 Figure 5 – Flat peak type reflow profile 20 Table 1 – Steam ageing conditions 10 | | | 3.7.3 | Test substrate | 8 |
| 4.1 Lead-free backward compatibility | | | 3.7.4 | Solder paste | 9 |
| 4.1 Lead-free backward compatibility | | | 3.7.5 | Reflow equipment | 9 |
| 4.1 Lead-free backward compatibility 9 4.2 Preconditioning SIST EN 60749-212011 10 4.2.1 General andards ich arcatalog standards/sixte8639e90-2d40-4317-b7ae- 10 4.2.2 Preconditioning by steam ageing 10 4.2.3 Preconditioning by high temperature storage 11 4.3 Procedure for dip and look solderability testing 11 4.3.1 General 11 4.3.2 Solder dip conditions 11 4.3.3 Procedure 11 4.4 Procedure for simulated board mounting reflow solderability testing of SMDs 19 4.4.1 General 19 4.4.2 Test equipment set-up 19 4.4.3 Specimen preparation and surface condition 20 4.4.4 Visual inspection 21 5 Summary 21 Bibliography 22 Figure 1 – Areas to be inspected for J-lead packages 15 Figure 2 – Areas to be inspected in rectangular components (SMD method) 17 Figure 5 – Flat peak type reflow profile 20 Table 1 – Steam ageing conditions 10< | | | | Flux removal solvent | 9 |
| 4.2 Preconditioning SISTEN 60749-212011 10 4.2.1 General dards itch arctatalog standards sixte8639e90-2d40-4317-b7ae- 10 4.2.2 Preconditioning by steam ageing 10 4.2.3 Preconditioning by high temperature storage 11 4.3 Procedure for dip and look solderability testing 11 4.3.1 General 11 4.3.2 Solder dip conditions 11 4.3.3 Procedure 11 4.4.1 General 19 4.4.2 Test equipment set-up 19 4.4.3 Specimen preparation and surface condition 20 4.4.4 Visual inspection 21 5 Summary 21 Bibliography 22 Figure 1 – Areas to be inspected for J-lead packages 15 Figure 2 – Areas to be inspected in rectangular components (SMD method) 17 Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method) 18 Figure 5 – Flat peak type reflow profile 20 Table 1 – Steam ageing conditions 10 | 4 | Proce | edure | (standards.iteh.ai) | 9 |
| 4.2 Preconditioning SISTEN 60749-212011 10 4.2.1 General dards itch arctatalog standards sixte8639e90-2d40-4317-b7ae- 10 4.2.2 Preconditioning by steam ageing 10 4.2.3 Preconditioning by high temperature storage 11 4.3 Procedure for dip and look solderability testing 11 4.3.1 General 11 4.3.2 Solder dip conditions 11 4.3.3 Procedure 11 4.4.1 General 19 4.4.2 Test equipment set-up 19 4.4.3 Specimen preparation and surface condition 20 4.4.4 Visual inspection 21 5 Summary 21 Bibliography 22 Figure 1 – Areas to be inspected for J-lead packages 15 Figure 2 – Areas to be inspected in rectangular components (SMD method) 17 Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method) 18 Figure 5 – Flat peak type reflow profile 20 Table 1 – Steam ageing conditions 10 | | 4.1 | Lead-f | ree backward compatibility | 9 |
| 4.2.3 Preconditioning by high temperature storage | | 4.2 | Precor | nditioningSIST EN 60749-21:2011 | 10 |
| 4.2.3 Preconditioning by high temperature storage | | | 4.2.1 | General | 10 |
| 4.2.3 Preconditioning by high temperature storage | | | 4.2.2 | Preconditioning by steam ageing | 10 |
| 4.3.1 General | | | | Preconditioning by high temperature storage | 11 |
| 4.3.2 Solder dip conditions | | 4.3 | Proced | dure for dip and look solderability testing | 11 |
| 4.3.3 Procedure | | | 4.3.1 | General | 11 |
| 4.4 Procedure for simulated board mounting reflow solderability testing of SMDs 19 4.4.1 General 19 4.4.2 Test equipment set-up 19 4.4.3 Specimen preparation and surface condition 20 4.4.4 Visual inspection 21 5 Summary 21 Bibliography 22 Figure 1 – Areas to be inspected for gullwing packages 15 Figure 2 – Areas to be inspected for J-lead packages 16 Figure 3 – Areas to be inspected in rectangular components (SMD method) 17 Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method) 18 Figure 5 – Flat peak type reflow profile 20 Table 1 – Steam ageing conditions 10 | | | 4.3.2 | Solder dip conditions | 11 |
| 4.4.1 General | | | 4.3.3 | Procedure | 11 |
| 4.4.2 Test equipment set-up | | 4.4 | Proced | dure for simulated board mounting reflow solderability testing of SMDs | 19 |
| 4.4.3 Specimen preparation and surface condition 20 4.4.4 Visual inspection 21 5 Summary 21 Bibliography 22 Figure 1 – Areas to be inspected for gullwing packages 15 Figure 2 – Areas to be inspected for J-lead packages 16 Figure 3 – Areas to be inspected in rectangular components (SMD method) 17 Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method) 18 Figure 5 – Flat peak type reflow profile 20 Table 1 – Steam ageing conditions 10 | | | 4.4.1 | General | 19 |
| 4.4.4 Visual inspection | | | 4.4.2 | Test equipment set-up | 19 |
| 5 Summary | | | 4.4.3 | Specimen preparation and surface condition | 20 |
| Figure 1 – Areas to be inspected for gullwing packages | | | 4.4.4 | Visual inspection | 21 |
| Figure 1 – Areas to be inspected for gullwing packages | 5 | Sum | mary | | 21 |
| Figure 2 – Areas to be inspected for J-lead packages | Bib | liogra | phy | | 22 |
| Figure 2 – Areas to be inspected for J-lead packages | Fig | ure 1 | _ Areas | to be inspected for gullwing packages | 15 |
| Figure 3 – Areas to be inspected in rectangular components (SMD method) | _ | | | | |
| Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method) | | | | | |
| Figure 5 – Flat peak type reflow profile | _ | | | | |
| Table 1 – Steam ageing conditions | _ | | | , , , , , , , , , , , , , , , , , , , , | |
| | Fig | ure 5 | – Flat p | eak type reflow profile | 20 |
| | Tab | ole 1 – | - Steam | ageing conditions | 10 |
| | | | | | |

| 60749-21 | © | IEC:2011 |
|----------|---|----------|
|----------|---|----------|

| 2 | |
|---|--|
| | |

| Table 3 – Solder dip test conditions | 11 |
|-----------------------------------------------------|----|
| Table 4 – Maximum limits of solder bath contaminant | 13 |

iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN 60749-21:2011

https://standards.iteh.ai/catalog/standards/sist/e8639e90-2d40-4317-b7ae-944d47f27d93/sist-en-60749-21-2011

INTERNATIONAL ELECTROTECHNICAL COMMISSION

SEMICONDUCTOR DEVICES – MECHANICAL AND CLIMATIC TEST METHODS –

Part 21: Solderability

FOREWORD

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter
- https://standards.iteh.ai/catalog/standards/sist/e8639e90-2d40-4317-b7ae
 5) IEC itself does not provide any attestation of conformity. Independent certification bodies provide conformity assessment services and, in some areas, access to IEC marks of conformity. IEC is not responsible for any services carried out by independent certification bodies.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

International Standard IEC 60749-21 has been prepared by IEC technical committee 47: Semiconductor devices.

This standard cancels and replaces the first edition published in 2004 and constitutes a technical revision. The significant change is the inclusion of Pb (lead)–free backward compatibility.

The text of this standard is based on the following documents:

| FDIS | Report on voting |
|--------------|------------------|
| 47/2082/FDIS | 47/2089/RVD |

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

60749-21 © IEC:2011

- 5 -

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 60749 series, under the general title Semiconductor devices – Mechanical and climatic test methods can be found on the IEC website.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

- · reconfirmed,
- withdrawn,
- · replaced by a revised edition, or
- amended.

iTeh STANDARD PREVIEW (standards.iteh.ai)