

Edition 1.0 2019-05

# INTERNATIONAL STANDARD

# NORME INTERNATIONALE



Printed boards and printed board assemblies – Design and use – Part 6-4: Land pattern design – Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design

### IEC 61188-6-4:2019

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Cartes imprimées et cartes imprimées équipées — Conception et utilisation – Partie 6-4: Conception de la zone de report – Exigences génériques pour les dessins dimensionnels de composants montés en surface (CMS) du point de vue de la conception de la zone de report





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INTERNATIONAL ELECTROTECHNICAL COMMISSION

COMMISSION ELECTROTECHNIQUE INTERNATIONALE

ICS 31.180

ISBN 978-2-8322-6866-7

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## PRINTED BOARDS AND PRINTED BOARD ASSEMBLIES – DESIGN AND USE –

# Part 6-4: Land pattern design – Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design

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FDIS	Report on voting
91/1561/FDIS	91/1572/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 61188 series, published under the general title *Printed boards and printed board assemblies – Design and use*, can be found on the IEC website.

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# PRINTED BOARDS AND PRINTED BOARD ASSEMBLIES – DESIGN AND USE –

# Part 6-4: Land pattern design – Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design

### 1 Scope

This part of IEC 61188 specifies generic requirements for dimensional drawings of SMD from the viewpoint of land pattern design.

The purpose of this document is to prevent land pattern design issues caused by lack of information and/or misuse of the information from SMD outline drawing as well as to improve the utilization of IEC 61188 series.

This document is applicable to the SMD of semiconductor devices and electrical components.

# 2 Normative references STANDARD PREVIEW

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IEC 60194, Printed board design, manufacture and assembly – Terms and definitions

IEC 60194-2, Printed board design, manufacture and assembly – Vocabulary – Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies

## 3 Terms, definitions and symbols

For the purposes of this document, the terms and definitions given in IEC 60194 and IEC 60194-2 apply, and the reference symbols are shown in Table 1.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

Reference symbol	Definition
A	SMD height (from the mounting surface to the package upper surface)
A <sub>1</sub>	Stand-off height (distance from the mounting surface to the package bottom)
A2	Package height
A	Standard heel height for terminal
A <sub>4</sub>	Terminal height
A <sub>5</sub>	Terminal height (thickness)
A	SMD height (from the mounting surface to the package upper surface, maximum)
A <sub>7</sub>	SMD height from the mounting surface to the package top (excluding moving part)
A <sub>8</sub>	SMD height from the mounting surface to the package top (at the lock lever open state)
Ag	SMD height from the mounting surface to the upper surface of cap
Øb <sub>1</sub>	Terminal diameter for ball
Øb <sub>2</sub>	Ball diameter
C, C <sub>1</sub> , C <sub>2</sub>	Row spacing. Distance between land centers
CY <sub>1</sub>	Courtyard width
CY <sub>2</sub>	Courtyard length
D	Package width en STANDARD PREVIEW
Ď	Solder balls area width (distance between the centres of the ball of both ends)
Ε	Package length (Standards.iten.al)
Ē	Solder balls area length (distance between the centres of the ball of both ends)
E2	Position of auxiliary terminals atalog/standards/sist/e12cb1f5-4205-4d43-a87e-
E <sub>3</sub>	Upper surface cap width fe36069c771a/iec-61188-6-4-2019
F <sub>1</sub> , F <sub>2</sub>	Clearance between signal and center (GND) terminal
F <sub>3</sub>	Distance from package-end to terminal-end of signal terminal
F <sub>4</sub>	Distance between signal terminals at the package corners
G, G <sub>1</sub> , G <sub>2</sub>	Distance between lands. Measured from inside edges
H <sub>D</sub>	SMD total width
H <sub>E</sub>	SMD total length
Η <sub>T</sub>	Height from the tray stage to the package top
i	Terminal inflection point
J <sub>b</sub>	Protrusion length of land over the component terminal
J <sub>H</sub>	Heel protrusion length
J <sub>S</sub>	Side protrusion length
J <sub>T</sub>	Toe protrusion length
$k_1, k_2, k_4$	Land pattern length
k3	Distance between land patterns
K <sub>5</sub> , K <sub>6</sub>	Terminal notch length (toe)
K <sub>7</sub> , K <sub>8</sub>	Terminal notch height (heel)
L	Terminal flat part length (mounting surface side)

Length from the package end to a terminal tip

Package length at the lock lever open state

Position of auxiliary terminals Auxiliary terminal pitch

L<sub>0</sub>, L<sub>1</sub> L<sub>3</sub>

 $L_4$ 

 $L_5$ 

## Table 1 – Reference symbols used in this document

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Reference symbol	Definition
L <sub>6</sub>	Upper surface cap position
L <sub>7</sub>	Upper surface cap length
L <sub>P</sub> , L <sub>P1</sub> , L <sub>P2</sub>	Terminal length (mounting surface side),
	Projected terminal length (when part of the terminal is away from the mounting surface)
L <sub>P0</sub>	Terminal length (upper side)
ØМ	Diameter of auxiliary terminal
Ν	Coplanarity to mounting surface
P	Pitch
q	Land pattern design parameter
R	Terminal bend radius (inside)
R <sub>2</sub>	Terminal bend radius (outside)
S	Distance between the terminals. Measured from inside edges
$T_{T}$	Tray height from the tray stage to the surface that supports SMD
<i>u</i> <sub>1</sub> , <i>u</i> <sub>2</sub>	Allowance for courtyard
ØW	SMD diameter (terminal)
W <sub>1,</sub> W <sub>2</sub>	Terminal width
W <sub>3</sub>	Bottom centre (GND) terminal length
W <sub>4</sub>	Bottom centre (GND) terminal width ARD PREVIEW
W <sub>G</sub>	Groove width (standards.iteh.ai)
W <sub>P</sub>	Position tolerance at terminal end
W <sub>V</sub>	Variation range of terminal tip outer position-4:2019
Х	Land widths://standards.iteh.ai/catalog/standards/sist/e12cb1f5-4205-4d43-a87e-
Y	Land length
ØY	Land diameter
$Z_{1}Z_{1}Z_{2}$	Distance between lands. Measured from outside edges
α, β	(Datum symbols)
η	Terminal horizontal angle formed by the groove centre line and the line form the egress to the tip of the terminal
θ	Terminal angle

### 4 Applicable SMDs

Applicable SMDs are as described below:

a) end-terminal type (components with rectangular or square terminal and cylindrical components with end cap terminal);

NOTE 1 In IEC 61191-2, "toe" solder fillet height is specified but "heel" solder fillet height is not specified for this type of SMD.

b) gull-wing terminals type (e.g. SOP, QFP), inward L-shaped ribbon terminals type and under-body L type (e.g. vertical form aluminium electrolytic capacitor, connector);

NOTE 2 In IEC 61191-2, "heel" solder fillet height is required but "toe" solder fillet height is not required for these SMD.

c) bottom surface terminal type (e.g. BGA, QFN and LGA) and flat lug terminals type.

NOTE 3 These SMDs have characteristics different from a) or b).

## **5** Requirements

### 5.1 Figures and dimensional symbols

The figures shown in this document are indicated as examples of typical SMDs. However, it is not the purpose to specify the rule of drawings (such as how to pull out a dimension line and dimensional symbols to be used).

-9-

The dimensional symbols in this document are used to show the various cases in order to indicate common requirements for land pattern designs. This document is not intended to integrate dimensional symbols.

NOTE The dimensional symbols used in this document are referring to existing International Standards and industry standards. It is difficult to integrate the dimensional symbols because the definitions of the dimensional symbols are different between these standards. For this reason, the dimensional symbols in this document can differ depending on the figure. The dimensional symbols in this document give priority to coordination with land pattern shape notation given by the IEC 61188-5 series. As a result, the notation for some SMDs can differ from the industry standards.

### 5.2 Common requirements

### 5.2.1 General

The common requirements for dimensional drawings of SMDs from the viewpoint of land pattern design are described in 5.2.2 to 5.2.10.

To explain a basic relation between dimensional drawings of SMDs and land pattern design, a representative case is shown in Figure 1 (gull-wing terminals type; 4-pin S-terminals). The quoted reference symbols in 5.2.2 to 5.2.10 are based on Figure 1.

### 5.2.2 Requirements for solder joint fillet design<sup>19</sup>

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To design a land that is a part of the land pattern and makes a solder joint to each terminal of the SMD, the dimensional drawing of the SMD shall have the terminal's dimensions ( $L_P$  and  $W_1$  in Figure 1) and the dimensions that specify the location of the terminal ( $H_E$  and P in Figure 1). Each dimension should be indicated by a nominal value with its tolerances.

The reasons are as shown below:

a) As a principle, each terminal of the SMD will have its own land.

Land width (*X*) and land length (*Y*) are given by the following formulas:

$$X = W_1 + 2 \times J_S$$

$$Y = J_{\mathsf{T}} + L_{\mathsf{P}} + J_{\mathsf{H}}$$

where

X is the land width [mm];

Y is the land length [mm];

 $W_1$  is the terminal width [mm];

 $L_{\mathsf{P}}$  is the terminal length [mm];

 $J_{T}$  is the toe protrusion length [mm];

 $J_{H}$  is the heel protrusion length [mm];

 $J_{\rm S}$  is the side protrusion length [mm].

b) As a principle, the location of each land is decided based on the dimensions, such as  $H_E$  and P in Figure 1a), which indicate the physical relationship of the soldering terminals of the SMD.

### 5.2.3 Requirements for courtyard design

The outermost dimensions of the SMD shall be given (e.g.  $H_E$ , E, D and A in Figure 1). This is because the courtyard is designed by taking into account the outermost shape after mounting the SMD on the land pattern.

NOTE The courtyard, which is a monopolization area for the SMD on a PCB, is designed based on the shape of the mount state (when the SMD is located on the lands), to prevent mechanical interferences at the time of mounting or assembly. If solder paste openings are outside the maximum extent of the package outline, plus terminals plus lands, the courtyard can be designed based on the outermost shape including solder paste openings. In addition, the courtyard is sometimes designed in consideration of reworkability, electrical insulation, stability of optical inspection, and the like. Therefore, three-dimensional information is required for the courtyard design.

### 5.2.4 Height parameters

The height of the soldering terminal,  $A_5$  in Figure 1a), shall be given in dimensional drawings of the SMD to determine the land pattern design.

NOTE Generally, the height of soldering terminal is used for the solder joint fillet design to estimate the necessary amount of solder. The height of the SMD is used for spatial design of the PCB assembly, and also used for defining the mounting machine parameters, such as the floating height of the vacuum adsorption nozzle.

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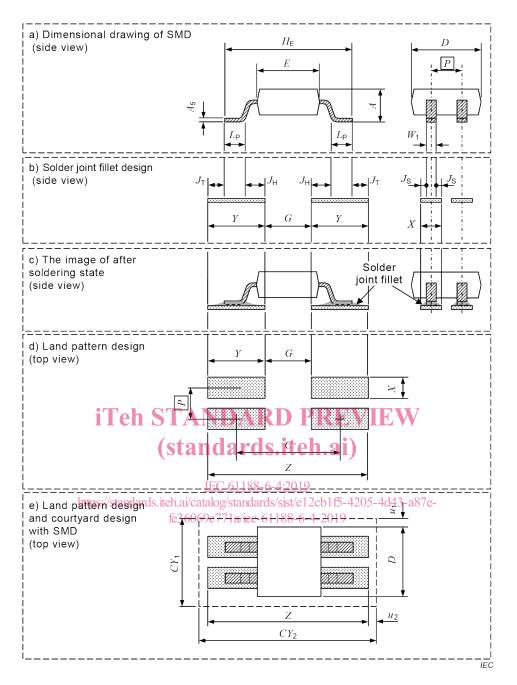


Figure 1 (1 of 2)

Rey	
$H_{E}$	SMD total length
Ε	Package length
D	Package width
W <sub>1</sub>	Terminal width
P	Pitch
A	SMD height (from the mounting surface to the package upper surface)
$A_5$	Terminal height (thickness)
LP	Terminal length
$J_{T}$	Toe protrusion length
$J_{H}$	Heel protrusion length
$J_{S}$	Side protrusion length
Х	Land width
Y	Land length
G	Distance between lands. Measured from inside edges
С	Row spacing. Distance between land centers
Ζ	Distance between lands. Measured from outside edges
$CY_1$	Courtyard width
CY <sub>2</sub>	Courtyard length
<i>u</i> <sub>1</sub> , <i>u</i> <sub>2</sub>	Allowance for courtyard

Sample image: gull-wing terminals type; 4-pin S-terminals.

NOTE In Figure 1e), the area enclosed by dashed lines is the courtyard.

# Figure 1 – Example of the dimensional relationship to the drawings of SMDs, the land pattern design and the after soldering state (2 of 2)

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### IEC 61188-6-4:2019

# 5.2.5 Bottom views://standards.iteh.ai/catalog/standards/sist/e12cb1f5-4205-4d43-a87e-

In the case of a 2D drawing, a figure of the bottom (attachment side) view should be given in addition to a top view, a side view, and a front view based on trigonometry. If the shape of the SMD can be recognized from the figure of the top view and the side view only, the figures of the front view and the bottom view may be omitted.

NOTE The examples of SMDs shown in Figure 6 and Figure 9 omit the figures of their front and bottom (attachment side) views.

### 5.2.6 Detail view

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If an SMD has a terminal with complicated shape, an additional detail view should be given. If the SMD shape is asymmetrical, the figure showing the relation of the position of the body and terminals shall be given.

### 5.2.7 Distinguish between metal and resin

In any 2D drawing or 3D data, the conductive metal part and the insulated resin part shall be clearly distinguished, at least for the bottom and the side of the SMD.

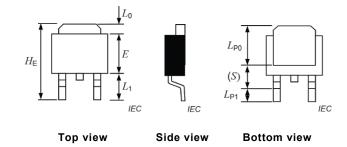
### 5.2.8 Consistency of various dimensions

The nominal dimension for any part shall be fixed in one value for land pattern design. For this purpose, the following conditions shall be met:

a) In a 2D drawing and 3D data, the dimension of the SMD shall be given by the nominal value and its tolerances. However, in the case of a 2D drawing, when the nominal value can be considered as the centre value of the maximum value and the minimum value, the nominal value may be omitted.

- b) In a 2D drawing for the SMD that has two or more soldering terminals, the dimension of soldering terminals, other metal parts and each soldering terminal distance shall be given in addition to an SMD outline. However, such distance can be given as a reference value.
- c) The sum of each part's dimensions shall be consistent with the nominal value of the total width or the total length of the SMD.

With these conditions, even if an asymmetrical SMD, the arrangement of the spatial relationship of all the parts is possible. The example of a case where the sum of each part's dimensions is consistent with the nominal value of the total length  $H_E$  is shown in Figure 2.



### EXAMPLE:

The total length of the SMD is equal to the sum of the length of the parts in the top view and in the bottom view.

	$H_{E} = L_0 + E + L_1 = L_{P0} + S + L_{P1}$
where	iTeh STANDARD PREVIEW
$H_{E}$	is the SMD total length [mm]:
Ε	is the package length [mm], dards.iteh.ai)
L <sub>0</sub>	is the length from the package end to a terminal tip [mm];
$L_1$	is the length from the package end to a terminal tip [mm];
L <sub>P0</sub>	https://standards.ite/ ai/catalog/standards/sist/e12cb1f5-4205-4d43-a87e- is the terminal length [200]/271a/isca (1188 ( 4 2010
L <sub>P1</sub>	fe36069c771a/iec-61188-6-4-2019 is the terminal length [mm];
S	is the distance between the terminals measured from inside edges [mm].

### Figure 2 – Example of dimensional consistency for an asymmetrical SMD

#### 5.2.9 The relation between a land pattern and an element-placement position

When a recommended land pattern along with the outline drawing of an SMD is shown, the information that shows the geometrical relation between a recommended land pattern and the position of the mounted SMD shall be given.

For an asymmetric SMD, draw the common centre line for the outline drawing of the SMD and the recommended land pattern so that the SMD's position on the land pattern can be clearly recognized (refer to Figure 3).