



SLOVENSKI STANDARD
SIST ETS 300 147 E3:2003
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Prenos in multipleksiranje (TM) – Sinhrona digitalna hierarhija (SDH) – Struktura multipleksiranja

Transmission and Multiplexing (TM); Synchronous Digital Hierarchy (SDH); Multiplexing structure

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Foreword

This European Telecommunication Standard (ETS) has been produced by the Transmission and Multiplexing (TM) Technical Committee of the European Telecommunications Standards Institute (ETSI).

This third edition of ETS 300 147 supersedes the second edition of ETS 300 147 (1995).

This ETS provides inter-vendor and inter-operator compatibility and is based on ITU-T Recommendation G.707 [1].

Transposition dates	
Date of adoption:	7 March 1997
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1 Scope

This European Telecommunication Standard (ETS) specifies the hierarchical bit rates, the multiplexing structure and the mapping schemes to be used in the transmission networks based on the Synchronous Digital Hierarchy (SDH).

2 Normative references

This ETS incorporates by dated and undated reference, provisions from other publications. These normative references are cited at the appropriate places in the text and the publications are listed hereafter. For dated references, subsequent amendments to or revisions of any of these publications apply to this ETS only when incorporated in it by amendment or revision. For undated references the latest edition of the publication referred to applies.

- [1] ITU-T Recommendation G.707 (1996): "Network node interface for the synchronous digital hierarchy".
- [2] ITU-T Recommendation G.702: "Digital hierarchy bit rates".

3 Definitions, abbreviations and conventions

3.1 Definitions

For the purposes of this ETS, the definitions given in section 3 of the ITU-T Recommendation G.707 [1] apply.

3.2 Abbreviations

For the purposes of this ETS the following abbreviations apply:

AIS	Alarm Indication Signal
APS	Automatic Protection Switching
ATM	Asynchronous Transfer Mode
AU-n	Administrative Unit-n
AUG	Administrative Unit Group
BIP-X	Bit Interleaved Parity-X
DCC	Data Communication Channel
MS-RDI	Multiplex Section Remote Defect Indication
MS-REI	Multiplex Section Remote Error Indication
NNI	Network Node Interface
POH	Path OverHead
SDH	Synchronous Digital Hierarchy
SOH	Section OverHead
STM(-N)	Synchronous Transport Module (-N)
TU-n	Tributary Unit-n
TUG(-n)	Tributary Unit Group (-n)
VC-n	Virtual Container-n

3.3 Conventions

The order of transmission of information in all diagrams in the ITU-T Recommendation G.707 [1] is first from left to right and then from top to bottom. Within each byte, the most significant bit is transmitted first. The most significant bit (bit 1) is illustrated at the left in all diagrams.