# I

# Test Suite Overview

<table>
<tr><td colspan="4" align="center">**Test Suite Structure**</td></tr>
<tr><td colspan="4">**Suite Name** : ISO_8882_2</td></tr>
<tr><td colspan="4">**Standards Ref** : ISO/IEC 7776:1995</td></tr>
<tr><td colspan="4">**PICS Ref** : ISO/IEC 7776:1995</td></tr>
<tr><td colspan="4">**PIXIT Ref** : ISO/IEC 8882–2 3rd edition</td></tr>
<tr><td colspan="4">**Test Method(s)** : Remote Embedded Single Layer</td></tr>
<tr><td colspan="4">**Comments** :</td></tr>
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| Test Group Reference | Selection Ref | Test Group Objective | Page Nr |
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| DL1/ | DP_STABLE | Verification of the Disconnected Phase | |
| DL1/DL1_PROPER/ | | Proper frames | |
| DL1/DL1_IMPROPER/ | | Improper frames | |
| DL1/DL1_INOPPORTUNE/ | | Inopportune frames | |
| DL2/ | SEND_DISC | Verification of IUT Initiated Link Disconnection Phase | |
| DL2/DL2_PROPER/ | | Proper frames | |
| DL2/DL2_IMPROPER/ | | Improper frames | |
| DL2/DL2_INOPPORTUNE/ | NOT_CCITT_80 | Inopportune frames | |
| DL3/ | SEND_SABM | Verification of Link Set–up Phase | |
| DL3/DL3_PROPER/ | | Proper frames | |
| DL3/DL3_IMPROPER/ | | Improper frames | |
| DL3/DL3_INOPPORTUNE/ | NOT_CCITT_80 | Inopportune frames | |
| DL4/ | | Verification of Information Transfer Phase | |
| DL4/DL4_PROPER/ | | Proper frames | |
| DL4/DL4_IMPROPER/ | | Improper frames | |
| DL4/DL4_INOPPORTUNE/ | | Inopportune frames | |
| DL5/ | FRMR_STABLE | Verification of Frame Reject Condition | |
| DL5/DL5_PROPER/ | | Proper frames | |
| DL5/DL5_IMPROPER/ | | Improper frames | |
| DL5/DL5_INOPPORTUNE/ | | Inopportune frames | |
| DL6/ | SEND_RNR | Verification of IUT Busy Condition | |
| DL6/DL6_PROPER/ | | Proper frames | |
| DL6/DL6_IMPROPER/ | | Improper frames | |
| DL6/DL6_INOPPORTUNE/ | | Inopportune frames | |
| DL7/ | | Verification of Sent Reject Condition | |
| DL7/DL7_PROPER/ | | Proper frames | |
| DL7/DL7_IMPROPER/ | | Improper frames | |
| DL7/DL7_INOPPORTUNE/ | | Inopportune frames | |
| DL8/ | | System Parameters and Error Recovery Tests | |

**Detailed Comments :**

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| | DL1_100 | | | |
| DL1/DL1_PROPER/ | DL1_101 | | Verify that the IUT sends a DM/F=1 in response to a DISC/P=1 in the disconnected phase. | |
| DL1/DL1_PROPER/ | DL1_102 | | Verify that the IUT sends a DM/F=0 in response to a DISC/P=0 in the disconnected phase. | |
| DL1/DL1_PROPER/ | DL1_103 | | Verify that the IUT sends a UA/F=1 or a DM/F=1 in response to a SABM/P=1 in the disconnected phase. | |
| DL1/DL1_PROPER/ | DL1_104 | | Verify that the IUT sends a UA/F=0 or a DM/F=0 in response to a SABM/P=0 in the disconnected phase. | |
| DL1/DL1_PROPER/ | DL1_105 | | Verify that the IUT reacts properly in the disconnected phase on receiving a DM/F=0. | |
| DL1/DL1_IMPROPER/ | DL1_201 | | Verify that,in the disconnected phase, the IUT upon receipt of a command frame with undefined or not implemented control field with P=1, either discards the frame or sends a DM/F=1 response frame. | |
| DL1/DL1_IMPROPER/ | DL1_202 | | Verify that the IUT discards a response frame with an undefined or not implemented control field with F=1 in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_203 | | Verify that the IUT discards a SABM/P=1 with an address different from A,B,C or D in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_204 | | Verify that the IUT discards a SABM/P=1 with an FCS error in the disconnected phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL1/DL1_IMPROPER/ | DL1_205 | | Verify that the IUT discards a DM/F=1 in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_206 | | Verify that the IUT discards a DM/F=0 with an information field in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_207 | | Verify that, in the disconnected phase,the IUT upon receipt of a SABM/P=1 with an information field,either discards the frame or sends a DM/F=1 response frame. | |
| DL1/DL1_IMPROPER/ | DL1_208 | | Verify that, in the disconnected phase,the IUT upon receipt of a UA/F=0 with an information field,while in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_209 | | Verify that, in the disconnected phase,the IUT upon receipt of a RR/P=1 with an information field, either discards the frame or sends a DM/F=1 response frame. | |
| DL1/DL1_IMPROPER/ | DL1_210 | | Verify that, in the disconnected phase, the IUT upon receipt of a RNR/P=1 with an information field,either discards the frame or sends a DM/F=1 response frame. | |
| DL1/DL1_IMPROPER/ | DL1_211 | | Verify that, in the disconnected phase, the IUT upon receipt of a REJ/P=1 with an informtion field, either discards the frame or sends a DM/F=1 response frame. | |
| DL1/DL1_IMPROPER/ | DL1_212 | | Verify that the IUT discards an I/P=0 frame containing an information field longer than the maximum established length in the disconnected phase. | |

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| Test Group Reference | Test Case Id | Selection Ref | Description | Page Nr |
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| DL1/DL1_IMPROPER/ | DL1_213 | | Verify that the IUT discards a DISC/P=0 with an information field in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_214 | | Verify that the IUT discards a DISC/P=1 with an information field in the disconnected phase. | |
| DL1/DL1_IMPROPER/ | DL1_215 | | Verify that the IUT discards a SABM/P=1 with an information field in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_301 | | Verify that the IUT sends a DM/F=1 in response to an I/P=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_302 | | Verify that the IUT sends a DM/F=1 in response to an RR/P=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_303 | | Verify that the IUT sends a DM/F=1 in response to an RNR/P=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_304 | | Verify that the IUT sends a DM/F=1 in response to an REJ/P=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_305 | | Verify that the IUT discards a UA/F=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_306 | | Verify that the IUT discards a UA/F=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_307 | | Verify that the IUT discards an FRMR/F=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_308 | | Verify that the IUT discards an FRMR/F=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_309 | | Verify that the IUT discards an I/P=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_310 | | Verify that the IUT discards an RR/F=0 in the disconnected phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL1/DL1_INOPPORTUNE/ | DL1_311 | | Verify that the IUT discards an RNR/F=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_312 | | Verify that the IUT discards an REJ/F=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_313 | | Verify that the IUT discards an RR/F=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_314 | | Verify that the IUT discards an RNR/F=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_315 | | Verify that the IUT discards an REJ/F=1 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_316 | | Verify that the IUT discards an RR/P=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_317 | | Verify that the IUT discards an RNR/P=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_318 | | Verify that the IUT discards an REJ/P=0 in the disconnected phase. | |
| DL1/DL1_INOPPORTUNE/ | DL1_319 | | Verify that the IUT discards an I/P=0 frame with no information field in the disconnected phase. | |
| DL2/DL2_PROPER/ | DL2_101 | NOT_UNRESP | Verify that the IUT sends a UA/F=0 in response to a DISC/P=0 received in the link disconnection phase. This situation corresponds to a DISC and DISC collision. | |
| DL2/DL2_PROPER/ | DL2_102 | NOT_UNRESP | Verify that the IUT sends a UA/F=1 in response to a DISC/P=1 received in the link disconnection phase. This situation corresponds to a DISC and DISC collision. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL2/DL2_PROPER/ | DL2_103 | | Verify that the IUT sends a DM/F=0 in response to a SABM /P=0 received in the link disconnection phase. This situation corresponds to a DISC and SABM collision. The IUT is expected to enter the disconnected phase. | |
| DL2/DL2_PROPER/ | DL2_104 | | Verify that the IUT sends a DM/F=1 in response to a SABM/P=1 received in the link disconection phase. This situation corresponds to a DISC and SABM collision. The IUT is expected to enter the disconnected phase. | |
| DL2/DL2_PROPER/ | DL2_105 | NOT_UNRESP | Verify proper IUT behavior in the link disconnection phase upon receiving a proper DM response. | |
| DL2/DL2_PROPER/ | DL2_106 | | Verify that in the link disconnection phase, upon receiving a DM/F=0 response, when the last DISC command from the IUT was with P=1, the IUT shall consider the DM response to represent a collision condition and shall remain in the link disconnection phase. | |
| DL2/DL2_PROPER/ | DL2_107 | NOT_UNRESP | Verify proper IUT behaviour in the link disconnection phase upon receiving a proper UA response. | |
| DL2/DL2_IMPROPER/ | DL2_201 | | Verify that, in the link disconnection phase, the IUT discards a command frame with an undefined or not implemented control field with P=1. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL2/DL2_IMPROPER/ | DL2_202 | | Verify that the IUT discards a response frame with an undefined or not implemented control field with F=1 in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_203 | | Verify that the IUT discards a DISC/P=0 with information field in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_204 | | Verify that the IUT discards a SABM/P=1 with an information field in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_205 | | Verify that the IUT discards a UA with an appropriate F bit setting, but with an added information field in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_206 | | Verify that the IUT discards a DM with an appropriate F bit setting, but with an additional information field in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_207 | | Verify that the IUT discards an I/P=1 frame containing an information field longer than the maximum established length in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_208 | | Verify that the IUT discards an RR/P=1 with an information field in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_209 | | Verify that the IUT discards an RNR/P=1 with an information field in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_210 | | Verify that the IUT discards an REJ/P=1 with an information field in the link disconnection phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL2/DL2_IMPROPER/ | DL2_211 | SEND_DISC | Verify that the IUT discards a DISC with an address different from A, B, C or D in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_212 | | Verify that the IUT discards a DISC/P=0 with an FCS error, in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_213 | CCITT_80 | Verify that in the case of X.25 1980, if the IUT is in the link disconnection phase and if the last DISC command from the IUT was with P=0, then a DM/F=1 shall be discarded and the IUT will remain in the link disconnection phase. | |
| DL2/DL2_IMPROPER/ | DL2_214 | | Verify that in the link disconnection phase,upon receiving an improper UA response (invalid F bit), the IUT shall discrad the UA and remain in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_301 | | Verify that the IUT discards an RR/P=1 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_302 | | Verify that the IUT discards an RR/P=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_303 | | Verify that the IUT discards an RNR/P=1 in the link disconnection phase | |
| DL2/DL2_INOPPORTUNE/ | DL2_304 | | Verify that the IUT discards an RNR/P=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_305 | | Verify that the IUT discards an REJ/P=1 in the link disconnection phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL2/DL2_INOPPORTUNE/ | DL2_306 | | Verify that the IUT discards an REJ/P=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_307 | | Verify that the IUT discards an FRMR/F=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_308 | | Verify that the IUT discards an RR/F=1 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_309 | | Verify that the IUT discards an RR/F=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_310 | | Verify that the IUT discards an RNR/F=1 in the link disconnection phase | |
| DL2/DL2_INOPPORTUNE/ | DL2_311 | | Verify that the IUT discards an RNR/F=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_312 | | Verify that the IUT discards an REJ/F=1 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_313 | | Verify that the IUT discards an REJ/F=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_314 | | Verify that the IUT discards an an FRMR/F=1 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_315 | | Verify that the IUT discards an I/P=0 in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_316 | | Verify that the IUT discards an I/P=1 received in the link disconnection phase. | |
| DL2/DL2_INOPPORTUNE/ | DL2_317 | | Verify that the IUT discards an I/P=0 frame with no information field in the link disconnection phase. | |
| DL3/DL3_PROPER/ | DL3_101 | | Verify that the IUT sends a UA/F=0 in response to a SABM/P=0 received in the link set–up phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL3/DL3_PROPER/ | DL3_102 | | Verify that the IUT sends a UA/F=1 in response to a SABM/P=1 received in the link set–up phase. | |
| DL3/DL3_PROPER/ | DL3_103 | | Verify that the IUT sends a DM/F=0 in response to a DISC/P=0 in the link set–up phase. This situation corresponds to a SABM and DISC collision. | |
| DL3/DL3_PROPER/ | DL3_104 | | Verify that the IUT sends a DM/F=1 in response to a DISC/P=1 in the link set–up phase. This situation corresponds to a SABM and DISC collision. | |
| DL3/DL3_PROPER/ | DL3_105 | | Verify that in the link set–up phase, upon receiving a proper UA response, the IUT shall enter the information transfer phase. | |
| DL3/DL3_PROPER/ | DL3_106 | | Verify that in the link set–up phase,the IUT shall ignore a DM/F=0, in case of a SABM and DM collision where SABM from IUT has P=1. | |
| DL3/DL3_PROPER/ | DL3_107 | NOT_UNRESP | Verify that in the link set–up phase, if the last SABM command from the IUT was with P=1, then upon receiving a DM/F=1 response, the IUT either sends a DISC/P=1 or a SABM/P=1 or a DM/F=0 or does not respond. | |
| DL3/DL3_IMPROPER/ | DL3_201 | | Verify that the IUT discards a command frame with an undefined or not implemented control field with P=1 in the link set–up phase. | |
| DL3/DL3_IMPROPER/ | DL3_202 | | Verify that the IUT discards a response frame with an undefined or not implemented control field in the link set–up phase. | |

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<tr><td colspan="5" align="center">**Test Case Index**</td></tr>
<tr><td>**Test Group Reference**</td><td>**Test Case Id**</td><td>**Selection Ref**</td><td>**Description**</td><td>**Page Nr**</td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_203</td><td></td><td>Verify that the IUT discards a UA with an appropriate F bit setting but with an additional information field in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_204</td><td></td><td>Verify that the IUT discards a DISC/P=0 with information field in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_205</td><td></td><td>Verify that the IUT discards a SABM/P=1 with information field in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_206</td><td></td><td>Verify that the IUT discards a DM with an appropriate F bit but with an information field in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_207</td><td></td><td>Verify that the IUT discards an I/P=1 with an information field which is longer than the maximum established length in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_208</td><td></td><td>Verify that the IUT discards an I/P=0 with no information field while in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_209</td><td></td><td>Verify that the IUT discards an RR/P=1 with an information field in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_210</td><td></td><td>Verify that the IUT discards an RNR/P=1 with an inforamtion field in the link set–up phase.</td><td></td></tr>
<tr><td>DL3/DL3_IMPROPER/</td><td>DL3_211</td><td></td><td>Verify that the IUT discards an REJ/P=1 with an information field in the link set–up phase.</td><td></td></tr>
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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL3/DL3_IMPROPER/ | DL3_212 | | Verify that the IUT discards a SABM/P=1 with an address different from A, B, C or D while in the link set–up phase. | |
| DL3/DL3_IMPROPER/ | DL3_213 | | Verify that the IUT discards a SABM/P=1 with an FCS error in the link set–up phase. | |
| DL3/DL3_IMPROPER/ | DL3_214 | CCITT_80 | Verify that in the link set–up phase,if an IUT is of X.25 1980 type, then it would discard a DM/F=1 if the last SABM from the IUT was P=0. | |
| DL3/DL3_IMPROPER/ | DL3_215 | | Verify that the IUT discards a UA with an invalid F bit in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_301 | | Verify that the IUT discards an RR/P=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_302 | | Verify that the IUT discards an RR/P=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_303 | | Verify that the IUT discards an RR/F=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_304 | | Verify that the IUT discards an RR/F=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_305 | | Verify that the IUT discards an FRMR/F=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_306 | | Verify that the IUT discards an FRMR/F=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_307 | | Verify that the IUT discards an I/P=0 received in the link set–up phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL3/DL3_INOPPORTUNE/ | DL3_308 | | Verify that the IUT discards an I/P=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_309 | | Verify that the IUT discards an RNR/P=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_310 | | Verify that the IUT discards an RNR/F=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_311 | | Verify that the IUT discards an RNR/P=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_312 | | Verify that the IUT discards an REJ/P=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_313 | | Verify that the IUT discards an REJ/F=0 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_314 | | Verify that the IUT discards an REJ/P=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_315 | | Verify that the IUT discards an RNR/F=1 received in the link set–up phase. | |
| DL3/DL3_INOPPORTUNE/ | DL3_316 | | Verify that the IUT discards an REJ/F=1 received in the link set–up phase. | |
| DL4/DL4_PROPER/ | DL4_101 | | Verify that the IUT sends a UA/F=1 in response to a DISC/P=1 received in the information transfer phase. | |
| DL4/DL4_PROPER/ | DL4_102 | | Verify that the IUT upon receiving a SABM/P=1 in the information transfer phase either sends a UA/F=1 or it sends a DM/F=1. | |
| DL4/DL4_PROPER/ | DL4_103 | | Verify proper IUT behavior on receiving FRMR/F=0 in the information transfer phase. | |

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| **Test Group Reference** | **Test Case Id** | **Selection Ref** | **Description** | **Page Nr** |
| DL4/DL4_PROPER/ | DL4_104 | | Verify that the IUT acknowledges a a valid I/P=0 frame. | |
| DL4/DL4_PROPER/ | DL4_105 | | Verify that the IUT acknowledges a valid I/P=1 frame. | |
| DL4/DL4_PROPER/ | DL4_106 | | Verify that the IUT responds to a proper RR/P=1 command in the information transfer phase. | |
| DL4/DL4_PROPER/ | DL4_107 | | Verify that in the information transfer phase if the IUT receives an REJ/P=1 command frame, then the IUT shall set its send state variable V(S) to N(R) of the received REJ frame control field and shall retransmit the corresponding I frame. Prior to this transmission the IUT shall send an RR/F=1 response to the received REJ/P=1 command frame. | |
| DL4/DL4_PROPER/ | DL4_108 | | Verify that in the information transfer phase if the IUT receives an REJ/P=0 command frame, then the IUT shall set its send state variable V(S) to N(R) of the received REJ frame control field and shall retransmit the corresponding I frame. | |
| DL4/DL4_PROPER/ | DL4_109 | | Verify that in the information transfer phase if the IUT receives an REJ/F=0 respond frame, then the IUT shall set its send state variable V(S) to N(R) of the received REJ frame control field and shall retransmit the corresponding I frame. | |

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