



# SLOVENSKI STANDARD

## SIST EN 4660-002:2011

01-december-2011

---

### Aeronavtika - Modularne in odprte letalske elektronske arhitekture - 002. del: CFM

Aerospace series - Modular and Open Avionics Architectures - Part 002: Common Functional Modules

Luft- und Raumfahrt - Modulare und offene Avionikarchitekturen - Teil 002: CFM

Série aérospatiale - Architectures Avioniques Modulaires et Ouvertes - Partie 002: CFM  
(standards.iteh.ai)

Ta slovenski standard je istoveten z: **EN 4660-002:2011**

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011>

#### **ICS:**

49.090	Oprema in instrumenti v zračnih in vesoljskih plovilih	On-board equipment and instruments
--------	--------------------------------------------------------	------------------------------------

**SIST EN 4660-002:2011**

**en**

**iTeh STANDARD PREVIEW**  
**(standards.iteh.ai)**

SIST EN 4660-002:2011

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011>

EUROPEAN STANDARD  
NORME EUROPÉENNE  
EUROPÄISCHE NORM

**EN 4660-002**

February 2011

ICS 49.090

English Version

**Aerospace series - Modular and Open Avionics Architectures -  
Part 002: Common Functional Modules**

Série aérospatiale - Architectures Avioniques Modulaires et  
Ouvertes - Partie 002: CFM

Luft- und Raumfahrt - Modulare und offene  
Avionikarchitekturen - Teil 002: CFM

This European Standard was approved by CEN on 26 June 2010.

CEN members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration. Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CEN member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CEN member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

CEN members are the national standards bodies of Austria, Belgium, Bulgaria, Croatia, Cyprus, Czech Republic, Denmark, Estonia, Finland, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, Netherlands, Norway, Poland, Portugal, Romania, Slovakia, Slovenia, Spain, Sweden, Switzerland and United Kingdom.

[SIST EN 4660-002:2011](https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011)

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011>



EUROPEAN COMMITTEE FOR STANDARDIZATION  
COMITÉ EUROPÉEN DE NORMALISATION  
EUROPÄISCHES KOMITEE FÜR NORMUNG

**Management Centre: Avenue Marnix 17, B-1000 Brussels**

## Contents

Page

Foreword.....	4
0 Introduction .....	5
0.1 Purpose.....	5
0.2 Document structure.....	6
1 Scope .....	6
1.1 Relationship with other ASAAC Standards .....	6
2 Normative references .....	7
3 Terms, definitions and abbreviations .....	7
3.1 Terms and definitions .....	7
3.2 Abbreviations .....	8
3.3 Conventions used in this Standard .....	10
4 CFM Definition.....	10
4.1 Generic CFM.....	11
4.2 Module Support Unit.....	13
4.3 Module Processing Capability.....	20
4.4 Network Interface Unit (NIU) and Routing Unit (RU) .....	29
4.5 Module Power Supply Element .....	29
4.6 Module Physical Interface (MPI) .....	31
5 Common Functional Module Interfaces .....	31
5.1 Module Logical Interface (MLI).....	31
5.2 Module Physical Interface (MPI).....	31
5.3 MOS Interface.....	32
6 CFM System Support and Guidelines.....	32
6.1 Fault Management .....	33
6.2 Fault Detection .....	33
6.3 Fault Masking.....	33
6.4 Fault Confinement .....	33
6.5 Safety and Security.....	34
Annex A (informative) Performance Sheet for all Common Functional Modules.....	36
A.1 Data Processor Module.....	36
A.2 Signal Processing Module .....	37
A.3 Graphic Processing Module .....	38
A.4 Mass Memory Module .....	38
A.5 Network Support Module .....	39
A.6 Power Conversion Module.....	39

## Figures

Page

Figure 1 — ASAAC Standard Documentation Hierarchy.....	5
Figure 2 — Functional representation of a generic CFM.....	11
Figure 3 — IMA Common Functional Modules – Graphical Composition .....	21
Figure 4 — The Power Supply Distribution functions of the PCM .....	26
Figure 5 — Power Supply Element functions .....	30
Figure 6 — Software Architecture Model - Three Layer Stack.....	32

**Tables**

Page

<b>Table 1 — CFM Embedded Information – Read Only .....</b>	<b>14</b>
<b>Table 2 — CFM Embedded Information – Read / Write .....</b>	<b>15</b>
<b>Table 3 — PCM output characteristics .....</b>	<b>27</b>
<b>Table 4 — PSE input voltage characteristics .....</b>	<b>30</b>
<b>Table A-1 — Performance sheet for a DPM .....</b>	<b>36</b>
<b>Table A-2 — Performance sheet for a SPM.....</b>	<b>37</b>
<b>Table A-3 — Performance sheet for a GPM .....</b>	<b>38</b>
<b>Table A-4 — Performance sheet for a MMM .....</b>	<b>38</b>
<b>Table A-5 — Performance sheet for a NSM .....</b>	<b>39</b>
<b>Table A-6 — Performance sheet for a PCM .....</b>	<b>39</b>

**iTeh STANDARD PREVIEW**  
**(standards.iteh.ai)**

[SIST EN 4660-002:2011](https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011)

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011>

**EN 4660-002:2011 (E)****Foreword**

This document (EN 4660-002:2011) has been prepared by the Aerospace and Defence Industries Association of Europe - Standardization (ASD-STAN).

After enquiries and votes carried out in accordance with the rules of this Association, this Standard has received the approval of the National Associations and the Official Services of the member countries of ASD, prior to its presentation to CEN.

This European Standard shall be given the status of a national standard, either by publication of an identical text or by endorsement, at the latest by August 2011, and conflicting national standards shall be withdrawn at the latest by August 2011.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CEN [and/or CENELEC] shall not be held responsible for identifying any or all such patent rights.

According to the CEN/CENELEC Internal Regulations, the national standards organizations of the following countries are bound to implement this European Standard: Austria, Belgium, Bulgaria, Croatia, Cyprus, Czech Republic, Denmark, Estonia, Finland, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, Netherlands, Norway, Poland, Portugal, Romania, Slovakia, Slovenia, Spain, Sweden, Switzerland and the United Kingdom.

**ITEH STANDARD PREVIEW**  
**(standards.iteh.ai)**

SIST EN 4660-002:2011

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011>

## 0 Introduction

### 0.1 Purpose

This document was produced under the ASAAC Phase II Contract.

The purpose of the ASAAC Programme is to define and validate a set of open architecture standards, concepts and guidelines for Advanced Avionics Architectures (A3) in order to meet the three main ASAAC drivers. The standards, concepts and guidelines produced by the Programme are to be applicable to both new aircraft and update programmes.

The three main drivers for the ASAAC Programme are:

1. Reduced life cycle costs.
2. Improved mission performance.
3. Improved operational performance.

The Standards are organised as a set of documents including:

- A set of agreed standards that describe, using a top down approach, the Architecture overview to all interfaces required to implement the core within avionics systems,
- The guidelines for system implementation through application of the standards.

The document hierarchy is given hereafter *(in this figure, the current document is highlighted)*

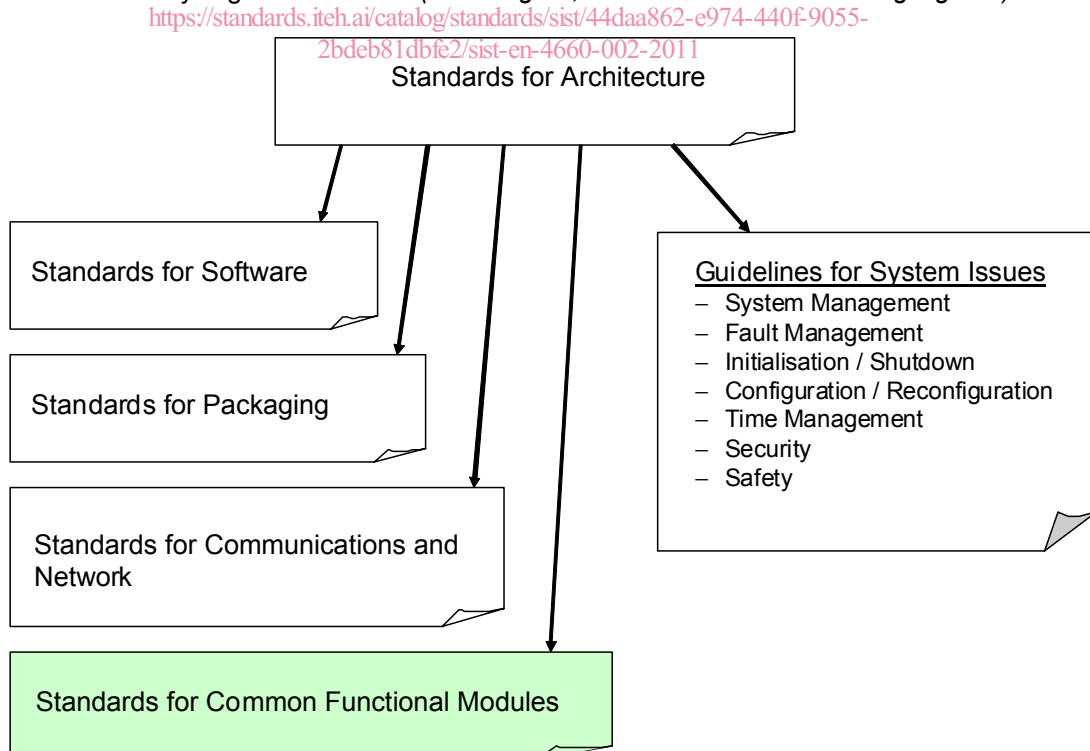


Figure 1 — ASAAC Standard Documentation Hierarchy

**EN 4660-002:2011 (E)****0.2 Document structure**

The document contains the following clauses:

Clause 1, scope of the document.

Clause 2, normative references.

Clause 3, the terms, definitions and abbreviations.

Clauses 4 and 5 provide CFM concept definition, requirements and standards.

Clause 6 provides guidelines for implementation of standards.

Performance sheets for each of the CFMs are attached to the end of the document. These sheets contain a list of attributes to be defined by the system designer and used by the CFM provider.

**1 Scope**

This standard defines the functionality and principle interfaces for the Common Functional Module (CFM) to ensure the interoperability of Common Functional Modules and provides design guidelines to assist in implementation of such a CFM. It is one of a set of standards that define an ASAAC (Allied Standard Avionics Architecture Council) Integrated Modular Avionics System.

This definition of interfaces and functionality allows a CFM design that is interoperable with all other CFM to this standard, that is technology transparent, that is open to a multi-vendor market and that can make the best use of COTS technologies.

Although the physical organisation and implementation of a CFM should remain the manufacturer's choice, in accordance with the best use of the current technology, it is necessary to define a structure for each CFM in order to achieve a logical definition of the CFM with a defined functionality. This definition includes:

- The Generic CFM, which defines the generic functionality applicable to the complete set of CFMs. The generic functionality is defined in 4.1.
- The processing capability, which defines the unique functionality associated with each CFM type within the set. This functionality is defined in 4.3.
- The logical and physical interfaces that enable CFMs to be interoperable and interchangeable, these are defined in Clause 6.
- The functionality required by a CFM to support the operation of the System is defined in Clause 6.

**1.1 Relationship with other ASAAC Standards**

The definition of the complete CFM is partitioned and is covered by the following ASAAC standards:

- CFM Mechanical properties and physical Interfaces – ASAAC Standards for Packaging.
- CFM Communication functions – ASAAC Standards for Software.
- CFM Network interface – ASAAC Standards for Communications and Network.
- CFM Software architecture – ASAAC Standards for Software.
- CFM Functional requirements – This document.



## 2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

ISO 1540, *Aerospace — Characteristics of aircraft electrical systems*

EN 4660-001, *Aerospace series — Modular and Open Avionics Architectures — Part 001: Architecture*

EN 4660-003, *Aerospace series — Modular and Open Avionics Architectures — Part 003: Communications/Network*

EN 4660-004, *Aerospace series — Modular and Open Avionics Architectures — Part 004: Packaging*

EN 4660-005, *Aerospace series — Modular and Open Avionics Architectures — Part 005: Software*

ASAAC2-GUI-32450-001-CPG Issue 01, *Final Draft of Guidelines for System Issues* <sup>1)</sup>

— Volume 1 — System Management.

— Volume 2 — Fault Management.

— Volume 3 — Initialisation and Shutdown.

— Volume 4 — Configuration / Reconfiguration.

— Volume 5 — Time Management.

— Volume 6 — Security.

— Volume 7 — Safety.

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2bdeb81dbfe2/sist-en-4660-002-2011>

## 3 Terms, definitions and abbreviations

### 3.1 Terms and definitions

Use of “shall”, “should” and “may” within the standards observe the following rules:

- The word SHALL in the text express a mandatory requirement of the standard.
- The word SHOULD in the text expresses a recommendation or advice on implementing such a requirement of the standard. It is expected that such recommendations or advice will be followed unless good reasons are stated for not doing so.

---

1) Published by: Allied Standard Avionics Architecture Council.

**EN 4660-002:2011 (E)**

— The word MAY in the text expresses a permissible practice or action. It does not express a requirement of the standard.

*Open System:* A system with characteristics that comply with specified, publicly maintained, readily available standards and that therefore can be connected to other systems that comply with these same standards.

**3.2 Abbreviations**

<b>2D</b>	: Two Dimensional
<b>3D</b>	: Three Dimensional
<b>A3</b>	: Advanced Avionics Architecture
<b>AGT</b>	: Absolute Global Time
<b>ALT</b>	: Absolute Local Time
<b>APOS</b>	: Application to Operating System Interface
<b>ASAAC</b>	: Allied Standard Avionics Architecture Council
<b>BIT</b>	: Built-in Test
<b>CBIT</b>	: Continuous BIT
<b>CFM</b>	: Common Functional Module
<b>CORBA</b>	: Common Object Request Broker Architecture
<b>COTS</b>	: Commercial Off The Shelf
<b>CRC</b>	: Cyclic Redundancy Check
<b>dc</b>	: Direct Current
<b>DPM</b>	: Data Processing Module
<b>DSP</b>	: Digital Signal Processor
<b>EDAC</b>	: Error Detection And Correction
<b>FFT</b>	: Fast Fourier Transformation
<b>FIR</b>	: Finite Impulse response Filter
<b>FMECA</b>	: Fault Mode Effect and Criticality Analysis
<b>GPM</b>	: Graphic Processing Module
<b>GSM</b>	: Generic System Management
<b>HW</b>	: Hardware
<b>HDD</b>	: Head-Down Display
<b>HMD</b>	: Helmet Mounted Display
<b>HUD</b>	: Head-Up Display
<b>IBIT</b>	: Initiated BIT
<b>ID</b>	: Identification

<b>IDL</b>	: Interface Definition Language
<b>IEEE</b>	: Institute of Electrical and Electronics Engineers
<b>IFFT</b>	: Inverse Fast Fourier Transformation
<b>IMA</b>	: Integrated Modular Avionics
<b>ISO</b>	: International Standards Organisation
<b>ITM</b>	: Integrated Test and Maintenance
<b>JTAG</b>	: Joint Test Action Group
<b>MC</b>	: Module Controller
<b>MIS</b>	: Module Initialisation Support
<b>MLI</b>	: Module Logical Interface
<b>MMM</b>	: Mass Memory Module
<b>MOS</b>	: Module Support Layer to Operating System Interface
<b>MPI</b>	: Module Physical Interface
<b>MSL</b>	: Module Support Layer
<b>MSU</b>	: Module Support Unit
<b>MTP</b>	: Maintenance Test Port
<b>N/A</b>	: Not Applicable
<b>NIU</b>	: Network Interface Unit
<b>NSM</b>	: Network Support Module
<b>OMG</b>	: Object Management Group
<b>O/P</b>	: Output
<b>OS</b>	: Operating System
<b>OSL</b>	: Operating System Layer
<b>PBIT</b>	: Power-up / power-down BIT
<b>PCM</b>	: Power Conversion Module
<b>PCU</b>	: Power Conversion Unit
<b>PE</b>	: Processing Element
<b>PMS</b>	: Power Management System
<b>PSA</b>	: Power Switch Array
<b>PSE</b>	: Power Supply Element
<b>PU</b>	: Processing Unit
<b>RC</b>	: Reference Clock
<b>RLT</b>	: Relative Local Time

STANDARD PREVIEW  
(standards.iteh.ai)

<https://standards.iteh.ai/catalog/standards/sist/44daa862-e974-440f-9055-2b1e81dbfe2/sist-en-4660-002-2011>

**EN 4660-002:2011 (E)**

<b>RTBP</b>	: Runtime Blueprints
<b>RU</b>	: Routing Unit
<b>SPM</b>	: Signal Processing Module
<b>TC</b>	: Transfer Connection
<b>TLS</b>	: Three Layer Stack
<b>Vdc</b>	: Voltage dc

**3.3 Conventions used in this Standard**

The Interface Definition Language (IDL) as defined in the Common Object Request Broker Architecture (CORBA) 2.3 is used to express the MOS services as programming language independent services in this document.

The conventions used in this document are as follows:

**3.3.1 Special Fonts**

Words that have a special meaning appear in specific fonts or font styles. All code listings, reserved words and the name of actual data structures, constants, and routines are shown in *Courier*.

**3.3.2 Naming Conventions**

Parameter and variable names contain only words with lower case letters, which are separated by underscore.

Example      `vc_message`

NOTE      Upper and lower case letters are treated as the same letter.

**4 CFM Definition**

The Common Functional Modules (CFMs) are line replaceable items and provide an ASAAC IMA system with a computational capability, network support capability and power conversion capability. The following set of modules have been defined for use within an IMA core processing system:

- Signal Processing Module (SPM).
- Data Processing Module (DPM).
- Graphics Processing Module (GPM).
- Mass Memory Module (MMM).
- Network Support Module (NSM).
- Power Conversion Module (PCM).

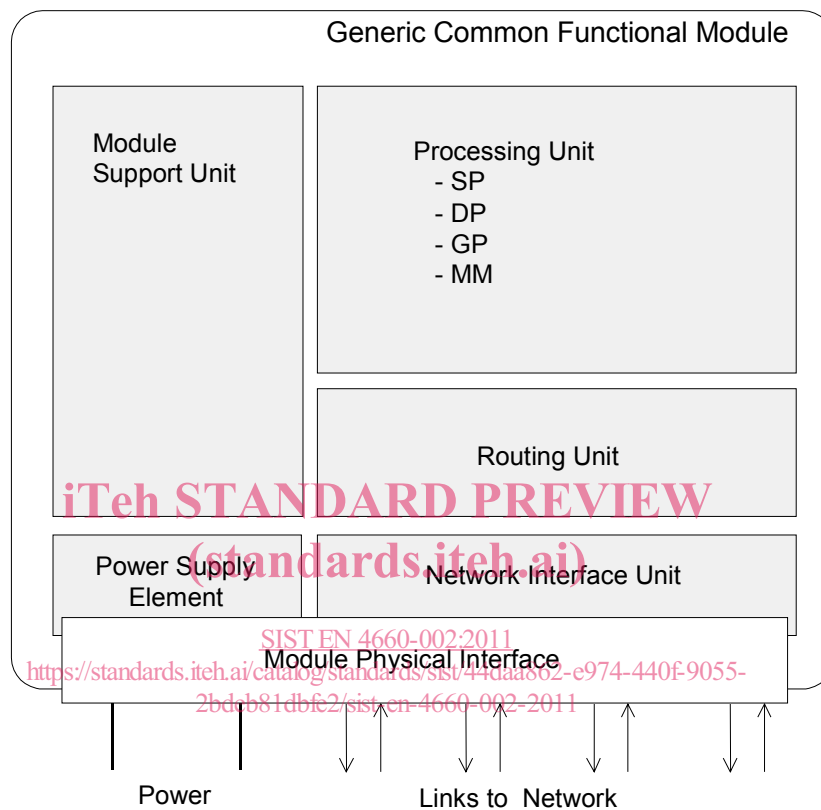
This set of CFMs complies with the generic CFM format defined in this clause.

It is assumed that a System Design Specification will be raised for each specific project implementation in which the detailed performance requirements for each CFM will appear.

## 4.1 Generic CFM

### 4.1.1 Generic CFM – Description

The internal architecture of each CFM consists of a set of functional elements that are applied to each CFM implementation. These are shown graphically in Figure 2 and are detailed below. All functions, with the exception of the Processing Unit, are generic to each CFM type.



**Figure 2 — Functional representation of a generic CFM**  
(For PCM and NSM refer to Figure 3)

- The Module Support Unit (MSU) controls and monitors the module and provides common functions such as Built-in-Test (BIT) control, module initialisation, time management, status recording/reporting and support for MLI (Clause 5), system management and debugging.
- The Processing Unit (PU) provides the specific function of a CFM, for example data processing, signal processing, mass storage. These are defined in 4.3.
- The Module Physical Interface (MPI) defines the physical characteristics of the module and implements the mechanical, optical, electrical and cooling interfaces. These are detailed in Clause 5 and are fully defined in EN 4660-004.
- The Routing Unit (RU) provides the internal communications capability of the CFM and interconnects the Network Interface Unit (NIU) with the Processing Unit (PU) and the Module Support Unit (MSU). The RU also provides a direct coupling between a network input link and a network output link. The RU is controlled by the MSU.