

**SLOVENSKI  
STANDARD**

**SIST-TP CLC/R217-003:2004**

september 2004

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**Veljavnost modelov EXPRESS**

Validation of EXPRESS models

**iTeh STANDARD PREVIEW  
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SIST-TP CLC/R217-003:2004  
<https://standards.iteh.ai/catalog/standards/sist/e198624c-fa8f-4691-84fc-f90273cb4a63/sist-tp-clc-r217-003-2004>

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ICS 35.080

Referenčna številka  
SIST-TP CLC/R217-003:2004(en)

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## REPORT

October 1995

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RENUMBERED TITLE PAGE FOR ENGLISH VERSION

**Validation of EXPRESS models**

This CENELEC Report has been prepared by the Technical Committee CENELEC TC 217, Electronic Design Automation (EDA) (former TC 117). It was approved by the Technical Committee on 1994-10-25 and endorsed by the CENELEC Technical Board on 1994-12-06.

**THE STANDARD PREVIEW**

CENELEC members are the national electrotechnical committees of Austria, Belgium, Denmark, Finland, France, Germany, Greece, Iceland, Ireland, Italy, Luxembourg, Netherlands, Norway, Portugal, Spain, Sweden, Switzerland and United Kingdom.

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**CENELEC**

European Committee for Electrotechnical Standardization  
Comité Européen de Normalisation Electrotechnique  
Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B - 1050 Brussels

Following decision D085/095 of the CENELEC Technical Board, all CLC/TC numbers starting from 100 have been increased by 100 to become e.g. 217.

Consequently, this technical report has been renumbered **R217-003**.

The renumbered title page is to be added to all copies of R117-003:1995.

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