

SLOVENSKI STANDARD SIST ETS 300 174/A1 E1:2003

01-december-2003

Ca fYÿb]'j]X]_]'fB5Ł'Ë'8][]HUbc'_cX]fUb'Y'_ca dcbYbh'hY'Yj]n]'g_]\ 'g][bUcj 'nUUd']_UM]'Y'n'XcXUbc'_U_cj cgh'c'j 'cVa c '1 '' (Ë() 'A V]h#g

Network Aspects (NA); Digital coding of component television signals for contribution quality applications in the range 34 - 45 Mbit/s

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ICS:

33.160.25 Televizijski sprejemniki Television receivers 35.040 Nabori znakov in kodiranje Character sets and informacij information coding

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AMENDMENT

ETS 300 174 A1

August 1997

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ICS: 33.020

Key words: Coding, TV

This amendment A1 modifies
the European Telecommunication Standard ETS 300 174 (1992)
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Network Aspects (NA);

Digital coding of component television signals for contribution quality applications in the range 34 - 45 Mbit/s

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ETS 300 174: November 1992/A1: August 1997

Foreword

This amendment to ETS 300 174 (1992) has been produced by the Network Aspects (NA) Technical Committee of the European Telecommunications Standards Institute (ETSI).

Transposition dates			
Date of adoption of this amendment:	22 August 1997		
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Amendments

Page 33, amendment to subclause 8.1.2

Replace the SN_i by:

Stripe Number for the ith stripe SNi

range is from 0 to 71 {50 Hz system}:

0 to 35 {first field} 36 to 71 {second field} the MSB is set to "0"

range is from 0 to 61 {60 Hz system}:

0 to 30 {first field} 31 to 61 (second field) the two MSB's are set to "0"

Page 33, amendment to subclause 8.1.2

Replace CRC_i by:

CRC_i Cyclic Redundancy Code for the ith stripe

16 bits

(to be applied to all bits of the encoded stripe excluding SSW). R.V. R.V.

The generator polynomial is 1 + x² (+x¹2+x¹2 ards.iteh.ai)

The CRC calculation registers are initialized to zero before the start of each stripe.

Page 34, amendment to subclause 3.1.3833e8d17bcd/sist-ets-300-174-a1-e1-2003

Replace FS by:

FS

Field sequence	Frame	Field	VA	
000	1	1	1	
001	1	2	1	
010	2	3	0	
011	2	4	0	
-	-	-	1	
-	-	-	1	
-	-	-	0	
111	4	8	0	
NOTE: This table applies for PAL and SECAM. The VA information is applicable to PAL only.				

8 bits

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Page 34, amendment to subclause 8.1.3

Replace BA by:

BA Burst Amplitude (for PAL and NTSC only).

The peak to peak amplitude of the subcarrier burst is quantized as a CCIR Recommendation 601 luminance signal, with the MSB omitted.

Page 34, amendment to subclause 8.1.3

Replace SCP by:

SCP Subcarrier Phase (for PAL and NTSC only).

Instantaneous phase of the reference subcarrier at the field-synchronization datum respectively field start as defined in CCIR Report 624-3 [8], MSB first.

Scale: 0 = ([360/256] * 0) 1 = ([360/256] * 1) ... = ... 255 = ([360/256] * 255)

Page 40, amendment to subclause 9.2.4

Replace the first paragraph by:

The packets defined above (19 octets maximum) are transmitted within High-Level Data Link Control (HDLC) frames (see ISO Standard 3309-2 [11]) on the 8 kHz supervision channel provided by bit S of the container. In all packets LSB (bit 0) is sent first.

Page 43, amendment to subclause 9.3.4

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Replace the first paragraph byrds.iteh.ai/catalog/standards/sist/696d4c93-b900-4599-999d-0833e8d17bcd/sist-ets-300-174-a1-e1-2003

Teletext messages are formed by the complete data unit specified for the system. For system B teletext in 525/60 systems, a dummy octet, set to zero, is added at the end of the data unit. The message length is therefore as shown in table 12. The first bit after the run-in sequence is MSB of the first octet of the data field.

Page 49, amendment to subclause 10.2.3, table 14

Replace in table 14; FRAME NUMBER 3, column m₃ by:

"1" if A' channel is 1544 Kbit/s (see note 3)

Page 53, amendment to subclause 11.1

Replace the sentence below Figure 18 by:

The initial value at the beginning of the first frame is:

LSB -> 001111101

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Page 54, amendment to subclause 11.2

Replace K by:

Κ

(6 bit) indicates the frames where S is a stuffing octet according to the 14+ J*15 law with J having values between 0 and 45:

K = 111111 for frames 14, 29, 44, etc.; K = 000000 for all other frames.

Page 59, amendment to subclause 12.2.2

Replace after the definition of function $g X_{n+1}$ by:

Q1, Q2, Q3 (XOR) QI, Q4,
$$(16\overline{Q2} + \overline{Q0}) \mod 31^*$$

R1, R2 (XOR) RI, R3, R4, R5, R6, $(R1 + 16\overline{R0}) \mod 31^*$

then $X_{n+1} = \underbrace{S1, S2, S3 (XOR) SI, S4, (2S2 + 2\overline{S0}) \mod 127^*}$

T1, T2 (XOR) TI, T3, T4, T5, T6, $(2T1 + 2\overline{T0}) \mod 127^*$

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Page 60, amendment to subclause 12.2.2, figure 23

Replace figure 23 by:

