# INTERNATIONAL STANDARD

### IEC 60747-4-1

QC 750115 First edition 2000-06

Semiconductor devices - Discrete devices -

Part 4-1:

Microwave diodes and transistors – Microwave field effect transistors – Blank detail specification

Dispositifs à semiconducteurs – Dispositifs discrets –

Partie 4-1;

Diodes et transistors hyperfréquences – Transistors hyperfréquences à effet de champ – Spécification particulière-cadre 800-36864678867676ec-60747-4-1-2000



### Numbering

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Information relating to the date of the reconfirmation of the publication is available in the IEC catalogue.

Information on the subjects under consideration and work in progress undertaken by the technical committee which has prepared this publication, as well as the list of publications issued, is to be found at the following IEC sources:

- IEC web site\*
- Catalogue of IEC publications
   Published yearly with regular updates
   (On-line catalogue)\*
- IEC Bulletin
   Available both at the IEC web sites and as a printed periodical

### Terminology, graphical and letter symbols

For general terminology, readers are referred to IEC 60050: International Electrotechnical Vocabulary (IEV).

For graphical symbols, and letter symbols and signs approved by the IEC for control of the symbols and signs approved by the IEC for control of the symbols and signs approved by the IEC for control of the symbols and signs approved by the IEC for control of the symbols in the symbols for use on equipment. Index, survey and compilation of the single sheets and IEC 60617: Graphical symbols for diagrams.

See web site address on title page.

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PRICE CODE



### INTERNATIONAL ELECTROTECHNICAL COMMISSION

#### SEMICONDUCTOR DEVICES - DISCRETE DEVICES -

## Part 4-1: Microwave diodes and transistors – Microwave field effect transistors – Blank detail specification

#### **FOREWORD**

- 1) The IEC (International Electrotechnical Commission) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of the IEC is to promote international co-operation on all questions concerning standardization in the electronal and electronic fields. To this end and in addition to other activities, the IEC publishes International Standards. Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. The IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of the IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested National Committees.
- 3) The documents produced have the form of recommendations for international use and are published in the form of standards, technical specifications, technical reports or guides and they are accepted by the National Committees in that sense.
- 4) In order to promote international unification, IEC National Committees undertake to apply IEC International Standards transparently to the maximum extent possible in their national and regional standards. Any divergence between the IEC Standard and the corresponding national or regional standard shall be clearly indicated in the latter.
- 5) The IEC provides no marking procedure to indicate its approval and cannot be rendered responsible for any equipment declared to be in conformity with one of its standards.
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International Standard EC 60747-4-1 has been prepared by subcommittee 47E: Discrete semiconductor devices of EC technical committee 47: Semiconductor devices.

The text of this standard is based on the following documents:

FDIS	Report on voting
47E/145/FDIS	47E/154/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 3.

Annex A forms an integral part of this standard.

The QC number that appears on the front cover of this publication is the specification number in the IEC Quality Assessment System for Electronic Components (IECQ).

### Other IEC publications quoted in this standard:

Publication Nos.

IEC 60191-2:1966, Mechanical standardization of semiconductor devices – Part 2: Dimensions

IEC 60747-8-1:1987, Semiconductor devices – Discrete devices – Part 8: Field-effect transistors – Section One: Blank detail specification for single-gate field-effect transistors up to 5 W and 1 GHz

IEC 60747-10:1991, Semiconductor devices – Part 10: Generic specification for discrete devices and integrated circuits

IEC 70747-11:1985, Semiconductor devices – Part 11: Sectional specification for discrete devices

IEC 60749:1996, Semiconductor devices - Mechanical and climatic test methods

The committee has decided that the contents of this publication will remain unchanged until 2005. At this date, the publication will be

- reconfirmed;
- withdrawn;
- · replaced by a revised edition, or
- amended.

A bilingual version of this standard may be issued at a later date.

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### SEMICONDUCTOR DEVICES - DISCRETE DEVICES -

## Part 4-1: Microwave diodes and transistors – Microwave field effect transistors – Blank detail specification

#### INTRODUCTION

The IEC Quality Assessment for Electronic Components is operated in accordance with the statutes of the IEC and under the authority of the IEC. The object of this system is to define quality assessment procedures in such a manner that electronic components released by one participating country as conforming with the requirements of an applicable specification are equally acceptable in all other participating countries without the need for further testing.

This blank detail specification is one of a series of blank detail specifications for semiconductor devices and shall be used with the following IEC publications:

- 60747-10/QC 700000: Semiconductor devices Part 10: Generic specification for discrete devices and integrated circuits.
- 60747-11/QC 750100: Semiconductor devices Part 11: Sectional specification for discrete devices.

#### Required information

Numbers shown in square brackets on this and the following pages correspond to the following items of required information, which shall be entered in the spaces provided.

Identification of the detail specification

- [1] The name of the National Standards Organization under whose authority the detail specification is issued.
- [2] The IECQ number of the detail specification.
- [3] The numbers and issue numbers of the generic and sectional specifications.
- [4] The national number of the detail specification, date of issue and any further information required by the national system.

Identification of the component

- [5] Type of component.
- [6] Information on typical construction and applications. If a device is designed to satisfy several applications, this shall be stated here. Characteristics, limits and inspection requirements for these applications shall be met.
  - If a device is electrostatic sensitive, or contains hazardous materials, for example beryllium oxide, a caution statement shall be added in the detail specification.
- [7] Outline drawing and/or reference to the relevant standard for outlines.
- [8] Category of assessed quality.
- [9] Reference data on the most important properties to permit comparison between component types.

.....

[Throughout this standard, the texts given in square brackets are intended for guidance to the specification writer and shall not be included in the detail specification.]

[Throughout this standard, when a characteristic or rating applies, "X" denotes that a value shall be inserted in the detail specification.]

(and possibly of body from which specification	[1] [Number of IECQ detail specification, plus issue number and/or date.] [2]
is available.]	QC 750115
ELECTRONIC COMPONENT OF ASSESSED QUALITY IN ACCORDANCE WITH	[3] National number of detail specification. [4]
Generic specification: Publication IEC 60747-10/QC 700000	[This box need not be used if national number repeats IECQ number.]
Sectional specification: Publication IEC 60747-11/QC 750000	Topouto 120 & Humbor.
[and national references if different.]	
BLANK DETAIL SPECIFICATION FOR MICROWAVE F	FIELD EFFECT TRANSISTORS / [5]
[Type number(s) of the relevant device(s).]	
Ordering information: see clause 7 of this standard.	
1 Mechanical description	2 Short description
Outline references: IEC 60191-2 [mandatory if available] and/or national [if there is no IEC outline.]	[7] Microwave field effect transistors [6]  Semiconductor material: [6]
Outline drawing: [may be transferred to or given with more details in clause 10 of this standard.]	Encapsulation [cavity or non-cavity].  Application(s): see clause 5 of this standard.  Caution: Observe precautions for handling
Terminal identification (https://sca	ELECTROSTATIC SENSITIVE DEVICES.
	[if applicable]
symbols.]  Marking:	\-\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Marking: [letters and figures, of colour code.]  [The detail specification shall prescribe the information	3 Categories of assessed quality
[drawing showing pin assignments, including graphical symbols.]  Marking: [letters and figures, or colour code.] [The detail specification shall prescribe the information be marked on the device, if any]  [See 2.5 of generic specification and/or clause 6 of this standard.]	3 Categories of assessed quality  [from 2.6 of the generic specification.] 6-0074[8]

Information about manufacturers who have components qualified to this detail specification is available in the current Qualified Products sist.

### 4 Limiting values (absolute maximum rating system) common to all applications

These values apply over the operating temperature range, unless otherwise specified.

[Repeat only subclause numbers used, with title. Any additional values shall be given at the appropriate place, but without subclause number(s).]

[Curves should preferably be given under clause 10 of this standard.]

Categories type A: power device

Categories type B: low-noise device

Sub-	Limiting values	Letter symbols	Type A		Type B	
clause			Min.	Max.	Min.	Max.
4.1	Ambient or case temperature	T <sub>amb/case</sub>	×	1/	\×	×
4.2	Storage temperature	$T_{stg}$	$\langle \times \rangle$	V×1	X	×
4.3	Drain-source voltage under specified conditions	$V_{ m DSX}$ or $V_{ m DSS}$ or $V_{ m DSR}$		1/		×
4.4	Gate-source reverse voltage	V <sub>GSR</sub> (		$\searrow$		×
4.5	Gate-drain voltage with source open-circuited	$V_{GDO}$	0	> ×		×
4.6	Drain current	10 d		×		×
4.7	Channel temperature	T <sub>ch</sub> or T	gital	×		×
4.8	Total power dissipation (see note)	Plot	· ·	1. a1)		×

NOTE Maximum value over the specified range of operating ambient or reference-point temperatures. Any special requirements for ventilation and/or mounting shall be stated.

### 5 Electrical characteristics

See clause 8 of this standard for inspection requirements.

[Repeat only subclause numbers used, with title. Any additional characteristics shall be given at the appropriate place but without subclause number.]

[When several devices are defined in the same detail specification, the relevant values shall be given on successive lines, avoiding repeating identical values.]

[Curves should preferably be given under clause 10 of this standard.]

Subclause	Characteristics and conditions at $T_{amb}$ or $T_{case} = 25$ °C unless otherwise specified (see clause 4 of the generic specification)	Letter symbols	Values				
			Тур	Type A		Type B	
			Min.	Max.	Min.	Max.	
5.1	Drain current with gate short-circuited to source: value at specified $V_{\rm DS}$	I <sub>DSS</sub>	×	×	×	×	A2b
5.2	Gate-source cut-off voltage: value at specified $V_{\rm DS}$ and $I_{\rm DS}$	$V_{GSoff}$	×	×	×	×	A2b
5.3	Gate-source breakdown voltage: value at specified $I_{\rm G}$ or	$V_{(BR)GS}$	×		×		A2b
	gate cut-off current, with drain short-circuited to source: value at specified $V_{\rm GS}$	I <sub>GSS</sub>		×	$\langle \langle \rangle \rangle$	×	A2b
5.4	Thermal resistance channel to case: value at specified $T_{\text{case}}$ and $I_{\text{G}}$	$R_{th(j-c)}$					C2d
5.5	Output power at 1 dB-gain compression: value at specified $f$ , $V_{\rm DS}$ and $I_{\rm DS}$ or	P <sub>o(1dB)</sub>	× <				A4
	output power: value at specified $f$ , $V_{\rm DS}$ , $I_{\rm DS}$ and $P_{\rm in}$	Po	X	1/	$\searrow$		A4
5.6	Power gain at 1 dB-gain compression: value at specified $f$ , $V_{DS}$ and $I_{DS}$ (see note)	G <sub>R(1)(B)</sub>	7×		<b>/</b>		A4
5.7	Power added efficiency: value at specified $f$ , $V_{\rm DS}$ , $I_{\rm DS}$ and $P_{\rm in}$	$\eta_{add}$					A4
5.8	Noise figure: value at specified f, VDS and IDS	F	site	h.a	i)	×	A4
5.9	Associated gain: value at specified $f$ , $V_{DS}$ and $I_{DS}$	Gas	evie	W	×		A4
5.10	Maximum frequency of oscillation: value at specified f, Vps and Vps	f <sub>max</sub>			×		C2a
5.11 standards.it	Maximum available gain: value at specified f. Vos and IDS	$G_{a(max)}$	<u>0</u> 1c4-980	c-368e4	× •e7a8b7	6/iec-6(	C2a 747-4-1
5.12	Amplitudes and angles of S-parameter: values at specified f, V <sub>Ds</sub> and I <sub>Ds</sub> , where	S <sub>11amp</sub>	×	×	×	×	C2a
	appropriate	<i>S</i> <sub>11ang</sub>	×	×	×	×	
		S <sub>12amp</sub>	×	×	×	×	
^		<i>S</i> <sub>12ang</sub>	×	×	×	×	
		S <sub>21amp</sub>	×	×	×	×	
		<i>\$</i> 21ang	×	×	×	×	
		S <sub>22amp</sub>	×	×	×	×	
		<b>S</b> 22ang	×	×	×	×	

NOTE Power gain at 1 dB-gain compression can be omitted if using output power at specified input power.

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### 6 Marking

[Any particular information other than given in box [7] (clause 1) and/or 2.5 of the generic specification shall be given here.]

### 7 Ordering information

[The following minimum information is necessary to order a specific device, unless otherwise specified:

- precise type reference (and nominal voltage value, if required);
- IECQ reference of the detail specification with issue number and/or date when relevant;
- category of assessed quality as defined in 3.7 of the sectional specification and, if required, screening sequence as defined in 3.6 of the sectional specification;
- any other particulars.]

### 8 Test conditions and inspection requirements

[These are given in the following tables, where the values and exact test conditions to be used shall be specified as required for a given type and as required by the relevant test in the relevant publication. Only those characteristics listed in clause 5 for the given application(s) shall be tested.]

[The choice between alternative tests or test methods shall be made when a detail specification is written.]

[When several devices are included in the same detail specification, the relevant conditions and/or values should be given on successive lines, where possible avoiding repetition or identical conditions and/or values.]

Throughout the following text, reference to subclause numbers is made with respect to the generic specification unless otherwise stated and test methods are quoted from clause 4 of the sectional specification.

[For sampling requirements, either refer to or reproduce values of 3.7 of the sectional specification, according to applicable category(ies) of assessed quality.]

[For group A, the choice between AQL or LTPD system shall be made in the detail specification.]

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