



SLOVENSKI STANDARD
SIST EN 190101:2002

01-september-2002

Family specification: Digital integrated circuits

Family Specification: Digital integrated TTL circuits - Series 54, 64, 74, 84

Familienspezifikation: Digitale integrierte TTL-Schaltungen - Serien 54, 64, 74, 84

Spécification de famille: Circuits intégrés digitaux TTL - Séries 54, 64, 74, 84

Ta slovenski standard je istoveten z: EN 190101:1994

[SIST EN 190101:2002](https://standards.iteh.ai/catalog/standards/sist/d0de0b71-ca2a-4ecd-969f-b9cd98654718/sist-en-190101-2002)

<https://standards.iteh.ai/catalog/standards/sist/d0de0b71-ca2a-4ecd-969f-b9cd98654718/sist-en-190101-2002>

ICS:

31.200	Integrirana vezja, mikroelektronika	Integrated circuits. Microelectronics
--------	--	--

SIST EN 190101:2002

en

iTeh STANDARD PREVIEW
(standards.iteh.ai)

[SIST EN 190101:2002](#)

<https://standards.iteh.ai/catalog/standards/sist/d0de0b71-ea2a-4ecc-969f-b9cd98654718/sist-en-190101-2002>

EUROPEAN STANDARD
 NORME EUROPÉENNE
 EUROPÄISCHE NORM

EN 190101

May 1994

UDC

Supersedes CECC 90101 Issue 2:1988

Descriptors: Quality, electronic components, digital integrated TTL circuits

English version

Family Specification: Digital Integrated TTL Circuits Series 54, 64, 74, 84

Spécification de famille:
 Circuits intégrés digitaux TTL
 Séries 54, 64, 74, 84

Familienpezifikation:
 Digitale integrierte
 TTL-Schaltungen
 Serien 54, 64, 74, 84

iTeh STANDARD PREVIEW
(standards.iteh.ai)

This European Standard was approved by the CENELEC Electronic Components Committee (CECC) on 30 April 1994. CENELEC members are bound to comply with CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the General Secretariat of the CECC or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CECC General Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Denmark, Finland, France, Germany, Greece, Iceland, Ireland, Italy, Luxembourg, Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and United Kingdom. The membership of the CECC is identical, with the exception of the national electrotechnical committees of Greece, Iceland and Luxembourg.

CECC

European Committee for Electrotechnical Standardization
 Comité Européen de Normalisation Electrotechnique
 Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B-1050 Brussels

Foreword

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby acceptable in all member countries without further testing.

This European Standard was prepared by CECC WG 9, "Integrated Circuits".

The text of the draft based on document CECC 90101 Issue 2:1988 (with A1) was submitted to the formal vote for conversion to a European Standard; together with the voting report, circulated as document CECC(Secretariat)3534 it was approved by CECC as EN 190101 on 30 April 1994.

The following dates were fixed:

- | | | |
|---|------------------|---|
| — latest date of announcement of the EN at national level | (doa) 1994-09-01 | SIST EN 190101:2002 |
| — latest date of publication of an identical national standard ^a | (dop) 1995-03-01 | |
| — latest date of withdrawal of conflicting national standards ^a | (dow) 1996-03-01 | |

^a National Standard (excluding National implementation of IECQ Specifications)

STANDARD PREVIEW
(standards.iteh.ai)

Förderverein für Elektrotechnische Normung (FEN) e. V.
Cenelec Electronic Components Committee

CECC



Système Harmonisé d'Assurance de la Qualité
des Composants Electroniques

SPECIFICATION DE FAMILLE:
**CIRCUITS INTEGRES
DIGITAUX TTL**

Séries 54, 64, 74, 84

ITeH STANDARD PREVIEW
(standards.iteh.ai)

Harmonized System of Quality Assessment for
Electronic Components

SIST EN 190101:2002

FAMILY SPECIFICATION:

**DIGITAL INTEGRATED
TTL CIRCUITS**

Series 54, 64, 74, 84

Harmonisiertes Gütebestätigungssystem für
Bauelemente der Elektronik

FAMILIEN-SPEZIFIKATION:
**DIGITALE INTEGRIERTE
TTL-SCHALTUNGEN**

Serien 54, 64, 74, 84

2

Edition
Issue
Ausgabe

CECC 90 101

1988

Contents

	Page
Foreword	iii
1 Limiting conditions of use for the family	2
2 Recommended operating conditions and associated characteristics for the family	2
3 Test methods and procedures	3
4 Inspection requirements	6
Figure 1 — Diagram for switching parameters	3
Figure 2 — Signal waveform at the input of the component under test	3

iTeh STANDARD PREVIEW
(standards.iteh.ai)

[SIST EN 190101:2002](https://standards.iteh.ai/catalog/standards/sist/d0de0b71-ca2a-4ecd-969f-b9cd98654718/sist-en-190101-2002)

<https://standards.iteh.ai/catalog/standards/sist/d0de0b71-ca2a-4ecd-969f-b9cd98654718/sist-en-190101-2002>

Foreword

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby accepted by all member countries without further testing.

This specification has been formally approved by the CECC, and has been prepared for those countries taking part in the System who wish to issue national harmonized specifications for TTL STANDARD DIGITAL INTEGRATED CIRCUITS. It should be read in conjunction with the current regulations for the CECC System.

Preface

This Family Specification was prepared by CECC WG9 "INTEGRATED CIRCUITS".

It is based, wherever possible, on the Publications of the International Electrotechnical Commission and in particular on IEC 747: Semiconductor devices: Discrete devices and integrated circuits, IEC 748: Semiconductor devices: Integrated circuits, IEC 749: Semiconductor devices: Mechanical and climatic test methods.

It contains general information on TTL Standard digital integrated circuits and defines the common characteristics for this family of integrated circuits.

Together with the device type detail specification (DS) of a component usually prepared nationally, this family specification forms a complete detail specification.

The text of this second Issue consists of the text of CECC 90101 Issue 1 (1979) amended in accordance with the ratified new material introduced by the following document.

Document	Date of Voting	Report on the Voting
CECC(Secretariat)2095	July 1987	CECC(Secretariat)2163

In accordance with the decision of the CECC Management Committee this specification is published initially in French and English. The German text will follow as soon as it has been prepared.


Effective date

This second Issue of CECC 90101 shall become effective for all new qualification approvals on 15 June 1988. Issue 1 will continue to remain effective to cover all past approvals.

iTeh STANDARD PREVIEW
(standards.iteh.ai)

SIST EN 190101:2002

<https://standards.iteh.ai/catalog/standards/sist/d0de0b71-ca2a-4ecd-969f-b9cd98654718/sist-en-190101-2002>

	<p style="text-align: center;">CECC 90 101 ISSUE 2 - 1988</p>  <p>Page 1 Total number of pages : 6</p>
<p>ELECTRONIC COMPONENTS OF ASSESSED QUALITY IN ACCORDANCE WITH :</p> <p>CECC 90 000 : Generic specification for Monolithic integrated circuits (GS) CECC 90 100 : Sectional specification for Digital monolithic integrated circuits (SS)</p>	
<p>OUTLINE AND DIMENSIONS : (See DS for the specific type)</p> <p>TERMINAL CONNECTIONS (See DS for the specific type)</p>	<p style="text-align: center;">FAMILY SPECIFICATION FOR TTL STANDARD DIGITAL INTEGRATED CIRCUITS</p> <p style="text-align: center;">Series 54, 64, 74, 84</p> <p><i>NOTE : This family specification shall be completed by a DS in accordance with this specification covering one or more specific types of circuits.</i></p> <p>TYPICAL CONSTRUCTION : Silicon monolithic bipolar integrated circuits, cavity/ non-cavity packages.</p> <p>CAUTION : These are electrostatic sensitive devices.</p> <p>ASSESSMENT LEVELS : P, Y, L</p>
<p style="text-align: center;"><u>CONTENTS</u></p> <p style="text-align: center;">54, 64, 74, 84</p> <ol style="list-style-type: none"> 1 - LIMITING CONDITIONS OF USE FOR THE FAMILY 2 - RECOMMENDED OPERATING CONDITIONS AND ASSOCIATED CHARACTERISTICS FOR THE FAMILY 3 - TEST METHODS AND PROCEDURES 4 - INSPECTION REQUIREMENTS 	
<p>Information about manufacturers who have components qualified to a detail specification written in accordance with this family specification is available in the current CECC 00 200 : Qualified Products List.</p>	