

SLOVENSKI STANDARD SIST EN 190106:2002

01-september-2002

Family specification: TTL advanced low power Schottky circuits - Series 54ALS, 74ALS

Family Specification: TTL advanced low power Schottky digital integrated circuits - Series 54ALS, 74ALS

Familienspezifikation: Digitale integrierte TTL advanced low power Schottky-Schaltungen - Serien 54ALS, 74AL\$Teh STANDARD PREVIEW

Spécification de famille: Circuits intégrés logiques TTL Schottky avancée faible consommation - Séries 54ALS, 74ALS_{IST EN 190106:2002}

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May 1994

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Descriptors: Quality, electronic components, TTL advanced low power Schottky digital integrated circuits

English version

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iTeh STANDARD PREVIEW

This European Standard was approved by the CENELEC Electronic Components Committee (CECC) on 8 May 1994. CENELEC members are bound to comply with CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the General Secretariat of the CECC or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CECC General Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Denmark, Finland, France, Germany, Greece, Iceland, Ireland, Italy, Luxembourg, Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and United Kingdom. The membership of the CECC is identical, with the exception of the national electrotechnical committees of Greece, Iceland and Luxembourg.

CECC

European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B-1050 Brussels

Foreword

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby acceptable in all member countries without further testing.

This European Standard was prepared by CECC WG 9, "Integrated Circuits".

The text of the draft based on document CECC 90106 Issue 2:1987 (with A1) was submitted to the formal vote for conversion to a European Standard; together with the voting report, circulated as document CECC(Secretariat)3543 it DARD PREVIEW was approved by CECC as EN 190106 on 8 May 1994.

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The following dates were fixed:

- latest date of announcement of the https://standards.iteh.ai/catalog/standards/sist/dc891aa2-8803-4593-a550-EN at national level (doa) 1994-09-01 d54/sist-en-190106-2002
- latest date of publication
 of an identical national
 standard^a (dop) 1995-03-01
- latest date of withdrawal of conflicting national standards^a

(dow) 1996-03-01

^a National Standard (excluding National implementation of IECQ Specifications)

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CECC 90 106

Förderverein für Elektrotechnische Normung (FEN) e. V. Förderverein für Elektrotechnische Normung (FEN) e. V.

Cenelec Electronic Components Committee



English version



Harmonized System of Quality Assessment for **Electronic Components**

FAMILY SPEZIFICATION:

TTL ADVANCED LOW POWER SCHOTTKY DIGITAL INTEGRATED CIRCUITS

Système Harmonisé d'Assurance de la Qualité des Composants Electroniques 190106:2002

SPECIFICATION 6 DE 2 April 19 SE / dc 89 1 aa 2 SPECIFICATION 6 DE 2 April 19 SE / dc 89 1 aa 2 SPECIFICATION 6 DE 2 April 19 SE / dc 89 1 aa 2 SPECIFICATION 6 DE 2 April 19 SE / dc 89 1 aa 2 SPECIFICATION 6 DE 2 April 19 SPECIFICATION 6 DE 2 APR -8803-4593-a550-

CIRCUITS INTEGRES LOGIQUES TTL SCHOTTKY AVANCEE FAIBLE CONSOMMATION

> Harmonislertes Gütebestätigungssystem für Bauelemente der Elektronik

Edition Issue Ausgabe

CECC 90 106

1987

EN 190106:1994

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Foreword

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The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby accepted by all member countries without further testing.

This specification his been formally approved by the CECC, and has been prepared for those countries taking part in the System who wish to issue national harmonized specifications for TTL ADVANCED LOW POWER SCHOTTKY DIGITAL INTEGRATED CIRCUITS. It should be read in conjunction with the current regulations for the CECC System.

At the date of printing of this specification the member countries of the CECC are Austria, Belgium, Denmark, Finland, France, Germany, Ireland, Italy, the Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and the United Kingdom.

Preface

This Family Detail Specification was prepared by CECC WG9 "INTEGRATED CIRCUITS".

It is based, wherever possible, on the Publications of the International Electrotechnical Commission and in particular on IEC 747: Semiconductor devices: Discrete devices and integrated circuits, IEC 748: Semiconductor devices; Integrated circuits, IEC 749: Semiconductor devices: Mechanical and climatic test methods.

It contains general information on TTL Advanced Low Power Schottky digital integrated circuits and defines the common characteristics for this family of integrated circuits.

Together with the device type detail specification (DS) of a component usually prepared nationally, this family detail specification forms a complete detail specification.

The text of this second Issue consists of the text of CECC 90106 Issue 1 (1984) amended in accordance with the ratified new material introduced by the following document.

Document Date of Voting Report on the Voting
CECC (Secretariat) 1968 September 1986 CECC (Secretariat) 2010

In accordance with the decision of the CECC Management Committee this specification is published initially in French and English. The German text will follow as soon as it has been prepared.

Effective date

This second Issue of CECC 90106 shall become effective for all new qualification approvals on 1 April 1987. Issue 1 will continue to remain effective to cover all past approvals.

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ELECTRONIC COMPONENTS OF ASSESSED QUALITY IN ACCORDANCE WITH:

CECC 90000: Generic specification for Monolithic integrated circuits (GS)

CECC 90100: Sectional specification for Digital monolithic integrated circuit (SS)

OUTLINE AND DIMENSIONS

(See DS for the specific type)

TERMINAL CONNECTIONS

(See DS for the specific type) STANDARII

(standards.i

SIST EN 190106

https://standards.iteh.ai/catalog/standards/si 663033f21d54/sist-en-1 FAMILY DETAIL SPECIFICATION FOR TTL ADVANCED LOW POWER SCHOTTKY DIGITAL INTEGRATED CIRCUITS

Series 54 ALS, 74 ALS

NOTE This family detail specification shall be completed by a DS in accordance with this specification covering one or more specific types of circuits.

TYPICAL CONSTRUCTION:

Silicon monolithic bipolar integrated circuits, cavity/non-cavity packages.

ASSESSMENT LEVELS: P, Y, L

CONTENTS

54 ALS, 74 ALS

- 1 LIMITING CONDITIONS OF USE FOR THE FAMILY
- 2 RECOMMENDED OPERATING CONDITIONS AND ASSOCIATED CHARACTERISTICS FOR THE FAMILY
- 3 TEST METHODS AND PROCEDURES
- 4 INSPECTION REQUIREMENTS

Information about manufacturers who have components qualified to a detail specification written in accordance with this family detail specification is available in the current CECC 00200: Qualified Products List.

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