



**SLOVENSKI STANDARD**  
**SIST EN 190110:2002**

**01-september-2002**

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**Blank detail specification: Digital microprocessor integrated circuits**

Blank Detail Specification: Digital microprocessor integrated circuits

Vordruck für Bauartspezifikation: Integrierte Mikroprozessor-Schaltungen

Spécification particulière cadre: Microprocesseurs logiques à circuits intégrés

**Ta slovenski standard je istoveten z: EN 190110:1994**

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**ICS:**

31.200	Integrirana vezja, mikroelektronika	Integrated circuits. Microelectronics
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EUROPEAN STANDARD  
 NORME EUROPÉENNE  
 EUROPÄISCHE NORM

EN 190110

May 1994

UDC

Supersedes CECC 90110 Issue 2:1993

Descriptors: Quality, electronic components, digital microprocessor integrated circuits

English version

## Blank Detail Specification: Digital Microprocessor Integrated Circuits

Spécification particulière cadre:  
 Microprocesseurs logiques à circuits  
 intégrés

Vordruck für Bauartspezifikation:  
 Integrierte Mikroprozessor- Schaltungen

### iTeh STANDARD PREVIEW (standards.iteh.ai)

This European Standard was approved by the CENELEC Electronic Components Committee (CECC) on 8 May 1994. CENELEC members are bound to comply with CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the General Secretariat of the CECC or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CECC General Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Denmark, Finland, France, Germany, Greece, Iceland, Ireland, Italy, Luxembourg, Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and United Kingdom. The membership of the CECC is identical, with the exception of the national electrotechnical committees of Greece, Iceland and Luxembourg.

## CECC

European Committee for Electrotechnical Standardization  
 Comité Européen de Normalisation Electrotechnique  
 Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B-1050 Brussels

## Foreword

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby acceptable in all member countries without further testing.

This European Standard was prepared by CECC WG 9, "Integrated Circuits".

The text of the draft based on document CECC 90110 Issue 2:1993 was submitted to the formal vote for conversion to a European Standard; together with the voting report, circulated as document CECC(Secretariat)3545 it was approved by CECC as EN 190110 on 8 May 1994.

The following dates were fixed:

- latest date of announcement of the EN at national level (doa) 1994-08-15
- latest date of publication of an identical national standard<sup>a</sup> (dop) 1995-02-15
- latest date of withdrawal of conflicting national standards<sup>a</sup> (dow) 1996-02-15

<sup>a</sup> National Standard (excluding National implementation of IECQ Specifications)

This specification has been formally approved by the CECC, and has been prepared for those countries taking part in the System who wish to issue national harmonized specifications for DIGITAL MICROPROCESSOR INTEGRATED CIRCUITS AND ASSOCIATED DIGITAL PERIPHERAL INTEGRATED CIRCUITS. It should be read in conjunction with the current regulations for the CECC System.

At the date of printing of this specification, the member countries of the CECC are Austria, Belgium, Denmark, Finland, France, Germany, Ireland, Italy, the Netherlands, Norway, Portugal, Spain, Sweden, Switzerland and the United Kingdom, and copies of it can be obtained from the addresses shown on the blue fly sheet.

## Preface

This blank detail specification (BDS) was prepared by CECC WG9: "Integrated circuits".

It is based, wherever possible, on the Publications of the International Electrotechnical Commission and in particular on IEC 747: *Semiconductor devices — Discrete devices and integrated circuits* and on IEC 748: *Semiconductor devices — Integrated circuits*.

The text of this second issue consists of the text of CECC 90110 Issue 1:1986, amended in accordance with the ratified new material introduced by the following document:

Document	Date of Voting	Report on the Voting
CECC (Secretariat)2667	February 1991	CECC (Secretariat)2789

It is recognized that the layout proposed cannot be applied to all detail specifications based on this document. For instance, it may be preferable to indicate the limiting values in the form of a table when several similar devices appear in the same detail specification.

## AVIS

In accordance with the decision of the CECC Management Committee, this specification is published initially in English and French. The German text will follow as soon as it has been prepared.

This Issue 2 of CECC 90110 shall become effective for all new qualification approvals from **15 October 1993**. Issue 1 remains valid for existing qualification approvals.

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## General

The following information is given for guidance.

## Scope

This BDS relates to:

- digital microprocessors, generally in accordance with IEC 747 and IEC 748: *Semiconductor devices — Discrete devices and integrated circuits*
- digital integrated circuits which are primarily designed or intended for use with microprocessors.

Analogue and interface aspects of such devices shall be added to the Detail Specification in accordance with CECC 90200 and CECC 90300.

## Related documents

See 2.1 of CECC 90100 and 2.2 of CECC 90000.

## Structure of Detail Specifications

Clause numbering of DS shall be in accordance with that of this document.

## Units, symbols and terminology

See 2.3 of CECC 90100 and 2.3 of CECC 90000.

## Application of Quality Assessment Procedures



See 3 of CECC 90100 and CECC 90000.

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EN 190110:1994

Layout of front page of detail specification

<b>Specification available from :</b> [1]	<b>CECC 90 110-xxx</b> [2] 	
<b>ELECTRONIC COMPONENTS OF ASSESSED QUALITY IN ACCORDANCE WITH :</b> [3]	Page 1	Total number of pages :
<b>OUTLINE AND DIMENSIONS</b> [7]  <b>TERMINAL CONNECTIONS</b>	[4]	
	<b>DETAIL SPECIFICATION FOR</b> [5]  <b>iTeh STANDARD PREVIEW</b> <b>(standards.itech.ai)</b> <a href="https://standards.itech.ai/catalog/standards/sist/cd593876-cf74-47b4-86ed-bfcc294c0ec9/sist-en-190110-2002">SIST EN 190110:2002</a> <a href="https://standards.itech.ai/catalog/standards/sist/cd593876-cf74-47b4-86ed-bfcc294c0ec9/sist-en-190110-2002">https://standards.itech.ai/catalog/standards/sist/cd593876-cf74-47b4-86ed-bfcc294c0ec9/sist-en-190110-2002</a>	
	<b>TYPICAL CONSTRUCTION :</b> [6]	
	<b>CAUTION :</b> These are electrostatic sensitive devices. 	
<b>ASSESSMENT LEVEL(S) :</b> [8]		

<b>1 TYPE DESCRIPTION</b>	[9]
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Information about manufacturers who have components qualified to this detail specification is available in the current CECC 00 200 : Qualified Products List.



**FRONT PAGE**

The front page of the DS shall be laid out as shown on the page before.

The numbers between square brackets correspond to the following indications which shall be given:

**Identification of the DS and of the component:**

- [1] The name of the National Standards Organization under whose authority the DS is published and, if applicable, the organization from whom the DS is available.
- [2] The CECC Symbol and the CECC number allotted to the DS by the CECC General Secretariat.
- [3] The number and issue number of the CECC generic or sectional specification as relevant; also national reference if different.
- [4] If different from the CECC number, the national number of the DS, date of issue and any further information required by the national system, together with any amendment numbers.
- [5] Type number, a short description of the type by:
  - function
  - performance, for example variants based on speed, temperature, power etc.
- [6] Information on typical construction
  - material and type of construction (silicon, monolithic, bipolar, MOS, C MOS, etc.).
 For [5] and [6], the text to be given in the DS should be suitable for an entry in CECC 00200 (QPL) and CECC 00300 (Library List).
- [7] An outline drawing with main dimensions which are of importance for interchangeability, and/or reference to the appropriate national or international document for outlines. Alternatively, this drawing may be given in an Annex to the DS.
- [8] Quality assessment level(s).
- [9] Reference data giving information on the most important properties of the component, which allow comparison between the various component types intended for the same, or for similar, applications.

The DS shall give a brief description including the following:

- technology
- use (if applicable)
- number, type and length of registers (if applicable)
- number, type and width of buses (if applicable)
- word lengths (if applicable)
- addressable memory (if applicable)
- number of terminals
- supply voltage(s)
- any other special feature.

**Identification of the component and supplementary information:**

Description of the materials for the package (for example, glass, ceramic, metal, plastic) and information relating to the mounting (welding, soldering), lead material and finish.

Inside the sketch of the package, the terminal connections to the inputs, outputs or other important points of the circuit shall be identified. This can be shown by a functional block diagram.

Description of the numbering of the terminals with the identification of pin number 1.

Marking on the device in accordance with the GS (see 2.5 of CECC 90000).

**2 Operating characteristics**

The following characteristics shall apply over the full operating temperature range and the supply voltage range unless otherwise specified.

**2.1 General description**

The following characteristics shall be given if they are not adequately defined in clause 1.