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Smart Body Area Network (SmartBAN); Enhanced Ultra-Low Power Physical Layer (standards.iteh.ai)

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Contents

Intellectual Property Rights	4
Foreword.....	4
Modal verbs terminology.....	4
1 Scope	5
2 References	5
2.1 Normative references	5
2.2 Informative references.....	5
3 Definition of terms, symbols and abbreviations.....	5
3.1 Terms.....	5
3.2 Symbols.....	6
3.3 Abbreviations	6
4 Introduction and Background.....	6
5 General PHY Framework.....	6
5.0 Introduction	6
5.1 Frequency Spectrum.....	6
6 Packet Formats	7
6.1 PPDU.....	7
6.1.0 PPDU Structure.....	7
6.1.1 Preamble	7
6.1.2 Sync.....	7
6.1.3 PLCP Header	7
6.1.3.0 PLCP Header Structure	7
6.1.3.1 Packet Length.....	8
6.1.3.2 PHY Scheme.....	8
6.1.3.3 BCH Parity Bits	8
6.1.3.4 Header Parity.....	8
6.1.4 PSDU	8
6.1.5 Frame Parity.....	8
7 Modulation and Error Control.....	8
7.1 PPDU Formation	8
7.2 Modulation	9
7.3 Repetition and FEC	9
7.3.1 Repetition.....	9
7.3.2 BCH (127,113, $t=2$) Encoding.....	10
7.3.3 BCH (36, 22, $t=2$) Encoding	10
7.4 Scrambling	10
8 Other Requirements.....	11
8.1 Packet Length.....	11
8.2 CCA.....	12
Annex A (informative): Bibliography.....	13
History	14

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Foreword

This Technical Specification (TS) has been produced by ETSI Technical Committee Smart Body Area Network (SmartBAN).

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Modal verbs terminology

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1 Scope

The present document specifies the ultra-low power physical layer (PHY) SmartBAN.

The present document applies to short range, wireless communication between wearable sensors devices and the hub coordinator. The present document specifies the PHY for transmitting on the medium.

The present document describes:

- packet formats;
- modulation;
- forward error correction.

2 References

2.1 Normative references

References are either specific (identified by date of publication and/or edition number or version number) or non-specific. For specific references, only the cited version applies. For non-specific references, the latest version of the referenced document (including any amendments) applies.

Referenced documents which are not found to be publicly available in the expected location might be found at <https://docbox.etsi.org/Reference>.

NOTE: While any hyperlinks included in this clause were valid at the time of publication ETSI cannot guarantee their long term validity.

The following referenced documents are necessary for the application of the present document.

- [1] ETSI TS 103 325 (V1.1.1) (04-2015): "Smart Body Area Network (SmartBAN); Low Complexity Medium Access Control (MAC) for SmartBAN".

2.2 Informative references

References are either specific (identified by date of publication and/or edition number or version number) or non-specific. For specific references, only the cited version applies. For non-specific references, the latest version of the reference document (including any amendments) applies.

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The following referenced documents are not necessary for the application of the present document but they assist the user with regard to a particular subject area.

Not applicable.

3 Definition of terms, symbols and abbreviations

3.1 Terms

Void.

3.2 Symbols

For the purposes of the present document, the following symbols apply:

D	Delay
⊕	eXclusive OR (XOR)

3.3 Abbreviations

For the purposes of the present document, the following abbreviations apply:

BAN	Body Area Network
BCH	Bose–Chaudhuri–Hocquenghem
CCA	Clear Channel Assessment
CRC	Cyclic Redundancy Check
ED	Energy Detection
FEC	Forward Error Correction
GFSK	Gaussian Frequency Shift Keying
IFS	Inter-Frame Spacing
ISM	Industrial, Scientific and Medical
MAC	Medium Access Control
MPDU	MAC Protocol Data Unit
PHY	Physical Layer
PLCP	Physical Layer Convergence Protocol
PPDU	PHY Protocol Data Unit
PSDU	Physical Layer Service Data Unit
Sync.	Synchronization

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4 Introduction and Background

Modern medical and health monitoring equipment are moving towards the trend of wireless connectivity between the data collection or control centre and the medical devices or sensors. Therefore, the need for a standardized communication interface and protocol between the actors are required. This network of actors performing some medical monitoring or functions is called a Smart Body Area Network (SmartBAN).

The present document specifies the physical layer procedure for SmartBAN.

5 General PHY Framework

5.0 Introduction

This clause provides the basic PHY framework for the nodes and hubs.

5.1 Frequency Spectrum

The frequency of operation shall fall within 2 401 - 2 481 MHz. The channels shall be arranged in blocks of 2 MHz with centre frequencies:

$$f_c = 2\,402 + 2 \times n \text{ MHz, for } n = 0 \text{ to } 39 \quad (1)$$

where n is the channel number.

Table 1 shows the mapping of the channel number to data channel number and control channel number.

Table 1: Mapping of Channel Number to Data and Control Channel Numbers

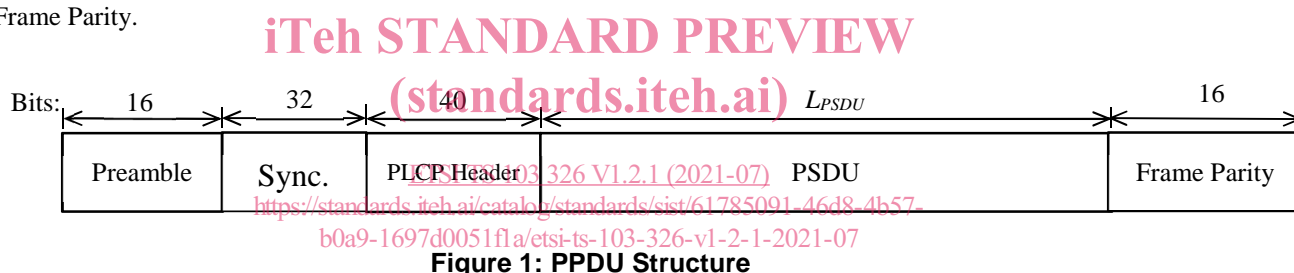
Channel Number	Centre Frequency (MHz)	Channel Type	Data Channel Number	Control Channel Number
0	2 402	Control		0
1	2 404	Data	0	
...		Data		
11	2 424	Data	10	
12	2 426	Control		1
13	2 428	Data	11	
...		Data		
38	2 478	Data	36	
39	2 480	Control		2

6 Packet Formats

6.1 PPDU

6.1.0 PPDU Structure

Figure 1 shows the PPDU structure. The PPDU consists of Preamble, Synchronization (Sync.), PLCP Header, PSDU and Frame Parity.

**Figure 1: PPDU Structure**

6.1.1 Preamble

PPDUs have a sixteen-bit preamble used for frequency synchronization, timing synchronization, and automatic gain control.

The preamble for all PPDUs shall be 1010101010101010.

6.1.2 Sync.

The Sync. field indicates the synchronization pattern that shall be 10000111101100101000011110110010.

6.1.3 PLCP Header

6.1.3.0 PLCP Header Structure

The PLCP Header is structured as illustrated in Figure 2. The PLCP header consists of the Packet Length, PHY Scheme, Reserved, BCH Parity Bits, and the Header Parity fields.

The PLCP header may be scrambled by the procedure described in clause 7.4.

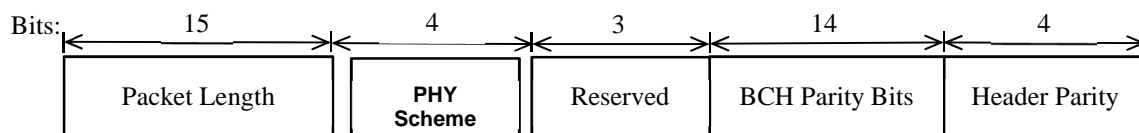


Figure 2: PLCP Header Structure

6.1.3.1 Packet Length

The packet length field indicates the length of the PSDU.

6.1.3.2 PHY Scheme

The PHY Scheme field describes the Forward Error Control (FEC) type and the repetition type the PPDU employs. The mapping of the field bits is as described in Table 2.

6.1.3.3 BCH Parity Bits

The BCH Parity Bits field is generated using a BCH (36, 22, $t = 2$) code defined in clause 7.3.3 to protect the Packet Length, PHY Scheme, and Reserved fields.

6.1.3.4 Header Parity

The Header Parity field is generated by the CRC polynomial $1 + x + x^4$ on the Packet Length, PHY Scheme, Reserved, and BCH Parity Bits fields.

Table 2: PHY Scheme field bit mapping

Field Value b0 b1	FEC Type	Field Value b2 b3	Repetition Type
00	None	00	None
01	BCH (127, 113, 2)	01	2
10	Reserved	10	4
11	Reserved	11	Reserved

6.1.4 PSDU

The Physical Layer Service Data Unit (PSDU) is either an encoded or unencoded MAC Protocol Data Unit (MPDU) as defined in clause 6.1 of ETSI TS 103 325 [1]. The MPDU may be encoded using a BCH (127, 113, $t = 2$) code. The encoding procedure is described in clause 7.3. The PSDU may be scrambled using the procedure described in clause 7.4.

6.1.5 Frame Parity

The Frame Parity field shall contain a 16 bit CRC sequence of the PSDU generated using the generator polynomial:

$$x^{16} + x^{12} + x^5 + 1 \quad (2)$$

7 Modulation and Error Control

7.1 PPDU Formation

The PPDU is formed from the following process described in Figure 3. The dashed boxes represent optional support.

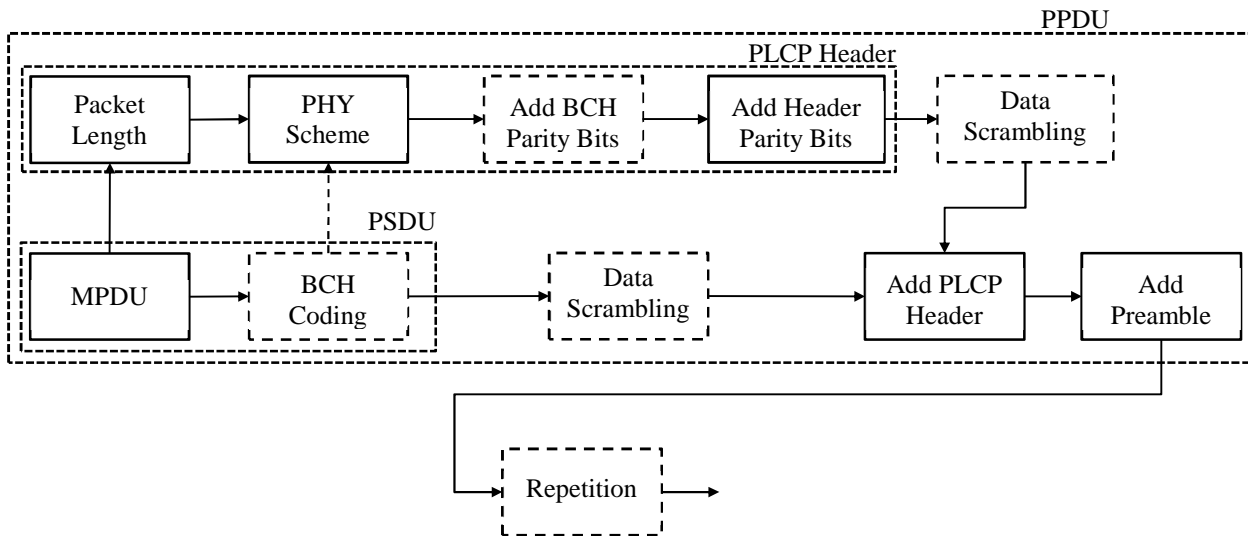


Figure 3: Transmitter Physical Layer Chain

7.2 Modulation

The modulation is Gaussian Frequency Shift Keying (GFSK) with a time-bandwidth product (BT) $BT = 0,5$, and modulation index $h = 0,5$.

A symbol rate, T_{sym} , of 1 MSymbols/s shall be supported in both Control and Data Channels. Table 3 shows the possible information rate according to the combination of symbol rate, code rate and the number of repetitions.

Table 3: PHY Throughput

Channel (Data/Control)	Information Flow	Symbol Rate (MSymbols/s)	Code Rate	Repetition	Information Rate (Mbps)
Data/Control	Downlink/Uplink	1,0	1	1	1,0
Data/Control	Downlink/Uplink	1,0	1	2	0,5
Data/Control	Downlink/Uplink	1,0	1	4	0,25
Data/Control	Downlink/Uplink	1,0	113/127	1	0,89
Data/Control	Downlink/Uplink	1,0	113/127	2	0,44
Data/Control	Downlink/Uplink	1,0	113/127	4	0,22

7.3 Repetition and FEC

7.3.1 Repetition

The hubs and nodes may implement repetition coding to reduce errors if required. Should repetition coding be implemented, this should be indicated in the PHY Scheme field in clause 6.1.3.4. Two repetition schemes should be supported, 2-repetition, repeating the entire PPDU 2 times, and 4-repetition, repeating the entire PPDU 4 times.

When repetition is employed, the original PPDU along with its repeated versions should be treated as one single PPDU.

An example of 2-repetition and 4-repetition is shown in Figure 4.

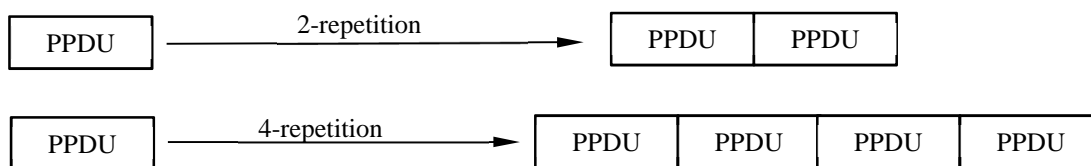


Figure 4: An example of 2-repetition and 4-repetition

7.3.2 BCH (127, 113, $t = 2$) Encoding

For error correction control of MPDU, a systematic BCH (127, 113, $t = 2$) code may be employed. t indicates the maximum number of bits that can be corrected. The generator polynomial of the BCH (127, 113, $t = 2$) code is:

$$g(x) = x^{14} + x^9 + x^8 + x^6 + x^5 + x^4 + x^2 + x + 1. \quad (3)$$

The encoding process is as follows:

- 1) Calculate the number of padding bits, $N_{padding}$. The number of padding bits depends on the length of the MPDU, L_{MPDU} , and can be calculated as:

$$N_{padding} = \left\lceil \frac{L_{MPDU}}{k} \right\rceil \times k - L_{MPDU} \quad (4)$$

where $k = 113$

- 2) Append $N_{padding}$ zero bits to the end of the MPDU.
- 3) Partition the padded MPDU into subpackets with length of k .
- 4) Compute the parity bits for each subpackets using the generator polynomial $g(x)$.
- 5) Remove $N_{padding}$ bits from the last subpacket.
- 6) Append the parity bits generated for each subpacket to each subpacket.
- 7) Reassemble the expanded subpackets in the same order they were dissembled to produce the PSDU.

7.3.3 BCH (36, 22, $t=2$) Encoding

For error correction control of the Packet Length, PHY Scheme, and Reserved fields of the PLCP header, a systematic BCH (36, 22, $t = 2$) code should be employed. The BCH code is a shortened code derived from the primitive BCH (127, 113, $t = 2$) described in clause 7.3.2. The encoding process is as follows:

- 1) Set $N_{padding} = 91$.
- 2) Append $N_{padding}$ zero bits to the end of the Packet Length, PHY Scheme, and Reserved fields, the resulting 113 bits are treated as a subpacket in clause 7.3.2.
- 3) Compute the parity bits for the subpacket using the generator polynomial $g(x)$ in equation (3),
- 4) Remove $N_{padding}$ bits from the subpacket.
- 5) Append the generated parity bits to the subpacket.

7.4 Scrambling

A data scrambler may be supported when necessary. The scrambling sequence is generated by the scrambling polynomial is $1 + x^{14} + x^{15}$, with an initial state of 000100100001010. The output of the data scrambler is:

$$x[n] = x[n-14] \oplus x[n-15] \quad (5)$$