FINAL DRAFT

AMENDMENT

ISO/IEC 14443-2:2010 FDAM 5

ISO/IEC JTC 1/SC 17

Secretariat: BSI

Voting begins on: **2014-02-07**

Voting terminates on: 2014-04-07

Identification cards — Contactless integrated circuit cards — Proximity cards —

Part 2:

Radio frequency power and signal

iTeh STANDARD PREVIEW

(stamENDMENT 5) Bit rates of 3fc/4, fc, 3fc/2 and 2fc from PCD to PICC

ISO/IEC 14443-2:2010/FDAmd 5

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Partie 2: Interface radiofréquence et des signaux de communication AMENDEMENT 5: Débits binaires de 3fc/4, fc, 3fc/2 et 2fc de PCD à PICC

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The committee responsible for this document is ISO/IEC JTC 1, *Information technology*, SC 17, *Card and personal identification*. ISO/IEC 14443-2:2010/FDAmd 5 https://standards.iteh.ai/catalog/standards/sist/0ee5df06-2a43-4e32-84a2-

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Identification cards — Contactless integrated circuit cards — Proximity cards —

Part 2: Radio frequency power and signal interface

AMENDMENT 5: Bit rates of 3fc/4, fc, 3fc/2 and 2fc from PCD to PICC

Page 2, Clause 4

Add the following new symbols to the list in alphabetical order:

AP	actual phase value
ACP	actual constellation point
EPI	elementary phase interval NDARD PREVIEW
etu	elementary time unit standards.iteh.ai)
ISI	inter symbol interference/IEC 14443-2:2010/FDAmd 5
ISI _d	https://standards.iteh.ai/catalog/standards/sist/0ee5df06-2a43-4e32-84a2- inter symbol interference1angle-iec-14443-2-2010-fdamd-5
ISI _m	inter symbol interference magnitude
NP	nominal phase value
P _H	complex constellation point of the maximum NP
P_L	complex constellation point of the minimum NP
PNP	previous nominal phase
PR	phase range
PSK	phase shift keying
#	number

Page 6, 8.1.1

Replace the subclause with the following:

"The bit rate for the transmission during initialization and anticollision shall be fc/128 (~106 kbit/s). The bit rate for the transmission after initialization and anticollision shall be one of the following: — fc/128 (~106 kbit/s),

- *fc*/64 (~212 kbit/s),
- *fc*/32 (~424 kbit/s),
- *fc*/16 (~848 kbit/s),
- *fc*/8 (~1,70 Mbit/s),
- *fc*/4 (~3,39 Mbit/s),
- *fc*/2 (~6,78 Mbit/s),
- 3fc/4 (~10,17 Mbit/s),
- *fc* (~13,56 Mbit/s),
- 3fc/2 (~20,34 Mbit/s),
- 2fc (~27,12 Mbit/s)."

Page 1	14
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Add new subclause 8.1.2.4:

"8.1.2.4 Modulation for bit rates of 3*fc*/4, *fc*, 3*fc*/2 and 2*fc* iTeh STÄNDARD PREVIEW See A.1." (standards.iteh.ai)

Page 15

ISO/IEC 14443-2:2010/FDAmd 5 https://standards.iteh.ai/catalog/standards/sist/0ee5df06-2a43-4e32-84a2-Add new subclause 8.1.3.3: 58a930041e62/iso-iec-14443-2-2010-fdamd-5

"8.1.3.3 Bit representation and coding for bit rates of 3fc/4, fc, 3fc/2 and 2fc

See <u>A.2</u>."

Page 15, 8.2.1

Replace the paragraph with the following:

"The bit rate for the transmission during initialization and anticollision shall be fc/128 (~106 kbit/s).

The bit rate for the transmission after initialization and anticollision shall be one of the following:

- *fc*/128 (~106 kbit/s),
- *fc*/64 (~212 kbit/s),
- *fc*/32 (~424 kbit/s),
- *fc*/16 (~848 kbit/s),
- fc/8 (~1,70 Mbit/s),
- *fc*/4 (~3,39 Mbit/s),
- fc/2 (~6,78 Mbit/s),"

Pages 17 to 18, 9.1.1

Replace: "— fc/2 (~6,78 Mbit/s)." with the following:

- *"— fc*/2 (~6,78 Mbit/s),
- *3fc*/4 (~10,17 Mbit/s),
- *fc* (~13,56 Mbit/s),
- 3fc/2 (~20,34 Mbit/s),
- 2fc (~27,12 Mbit/s)."

Page 23, 9.1.2

Insert the following new subclause title:

"9.1.2.1 Modulation for bit rates of *fc*/128, *fc*/64, *fc*/32, *fc*/16, *fc*/8, *fc*/4 and *fc*/2"

Insert the following new subclause 9.1.2.2 title and text:

"9.1.2.2 Modulation for bit rates of 3*fc*/4, *fc*, 3*fc*/2 and 2*fc*

See <u>A.1</u>."

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Page 24, 9.1.3

Insert the following new subclause title: C 14443-2:2010/FDAmd 5

https://standards.iteh.ai/catalog/standards/sist/0ee5df06-2a43-4e32-84a2-"9.1.3.1 Bit representation and coding for bit rates of fc/128, fc/64, fc/32, fc/16, fc/8, fc/4 and fc/2"

Insert the following new subclause 9.1.3.2 title and text:

"9.1.3.2 Bit representation and coding for bit rates of 3fc/4, fc, 3fc/2 and 2fc

See <u>A.2</u>."

Page 24, 9.2.5

At the end of the document, following 9.2.5, add the following new <u>Annexes A</u>, <u>B</u> and <u>C</u>.

Annex A

(normative)

Bit rates of 3*fc*/4, *fc*, 3*fc*/2 and 2*fc* from PCD to PICC

A.1 Modulation for bit rates of 3*fc*/4, *fc*, 3*fc*/2 and 2*fc*

For communication from PCD to PICC using bit rates of 3fc/4, fc, 3fc/2 and 2fc information is encoded by PSK modulation of RF carrier of the operating field.

For bit rates of 3fc/4, fc, 3fc/2 and 2fc, information is encoded by PSK modulation of the RF carrier. The RF carrier is phase modulated with a NP at each etu. For each bit rate, the length of an etu and the number of NPs are specified in Table A.1.

Bit rate	etu	# of NP
3fc/4 (approximately 10,17 Mbit/s)	4/ <i>fc</i>	8
<i>fc</i> (approximately 13,56 Mbit/s)	4/fc	16
3fc/2 (approximately 20,34 Mbit/s)	DAR 2/fc	
2fc (~27,12 Mbit/s) (Stan	dards/jteh.ai	16

Table A.1 — etu and # of NPs	Table A	4.1 —	etu and	#	of NPs
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The difference between two consecutive NPs is defined as EPID specified in Table A.2 and illustrated in Figure A.2. https://standards.iteh.ai/catalog/standards/sist/0ee5df06-2a43-4e32-84a2-

58a930041e62/iso-iec-14443-2-2010-fdamd-5

Table A.2 — EPI

Bit rate	EPI
3 <i>fc</i> /4 (approximately 10,17 Mbit/s)	8°
<i>fc</i> (approximately 13,56 Mbit/s)	4°
3 <i>fc</i> /2 (approximately 20,34 Mbit/s)	8°
<i>2fc</i> (~27,12 Mbit/s)	4°

The difference between the angle of $P_{\rm H}$ and the angle of $P_{\rm L}$ defines the phase range PR as illustrated in Figure A.1.

The PCD and PICC shall respect the PR limits as specified in <u>Table A.3</u> and <u>Table A.4</u>.

Bit rate	Minimum PR	Maximum PR
3fc/4, 3fc/2	54°	58°
fc, 2fc	58°	62°

Bit rate	Minimum PR	Maximum PR
3fc/4, 3fc/2	52°	60°
fc, 2fc	56°	64°

A.1.1 NP tolerances

Due to the limited bandwidth channel, the intended NP phase modulation is affected by inter symbol interference (ISI) resulting in an ACP at the end of each etu. The angle of the ACP is defined as AP. This is described in a constellation diagram with ISI_m and ISI_d as specified below and illustrated in Figure A.2.

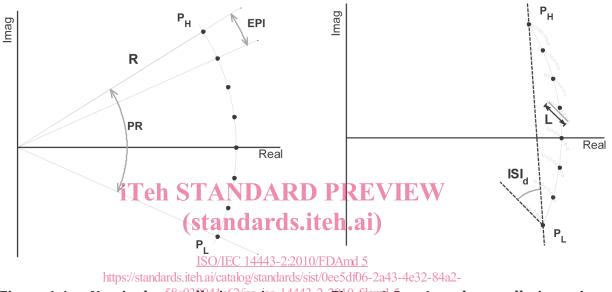


Figure A.1 — Nominal constellation points 4443-2-Figure A.2 — Actual constellation points

NOTE 1 NPs are indicated with small filled spots. ACPs are indicated with small circles.

NOTE 2 See <u>Annex A</u> for explanation on constellation diagrams. See <u>Annex B</u> for explanation on ISI.

L is the maximum distance of any two ACPs related to the same NP.

R is the signal amplitude.

 ISI_d is the rotation of all ACPs modulations related to one NP phase modulation. It is defined as the angle between the line through PH,PL and the line through any 2 ACPs with maximum distance related to the same NPV.

 ISI_m is the ISI magnitude normalized to the EPI. ISI_m = arcsin(L/R)/EPI.The PCD and PICC shall respect ISI_m limits for all ACPs as a function of ISI_d as specified in <u>Table A.5</u>, and <u>Table A.6</u>, and illustrated in Figure A.3.

	Condition	Min	Max
101	$abs(ISI_d) \le 90^{\circ}$	0	1.5 - abs(ISI _d)/90°
ISI _m	abs(ISI _d) > 90°	0	0,5

Table A.5 —	- ISI _m limits	s for PCD	transmission
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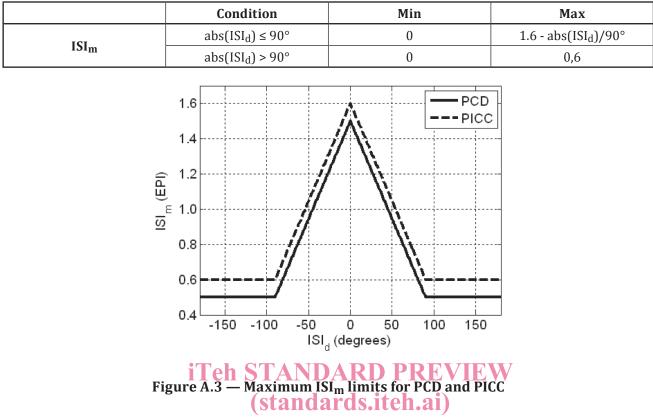


Table A.6 — ISI_m limits for PICC reception

NOTE 3 Future revisions of ISO/IEC 14443 and ISO/IEC 10373-6 may specify new NP tolerance values with corresponding test methods. ISO/IEC 14443-2:2010/FDAmd 5

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A.1.2 Phase noise

APs may also vary randomly due to phase noise.

The instantaneous phase error caused by noise is defined as the difference between the AP and the NP of 0° of an unmodulated signal sampled at the end of each etu. The differential phase error is defined as the difference of two consecutive instantaneous phase errors.

The normalized differential phase noise is the rms value of the differential phase error divided by EPI.

The normalized differential phase noise shall be lower than 0,033 for PCD transmission and lower than 0,035 for PICC reception.

NOTE Future revisions of ISO/IEC 14443 and ISO/IEC 10373-6 may specify new phase noise values with corresponding test methods.

A.2 Bit representation and coding for bit rates of 3fc/4, fc, 3fc/2 and 2fc

For bit rates 3fc/4 and 3fc/2 binary information shall be transmitted from PCD to PICC in units of 8 logic levels, building an information symbol of 3 bits. The 8 logic levels are represented by 8 NPs. The formation of 3 bit symbols from Bytes is illustrated in Figure A.4.

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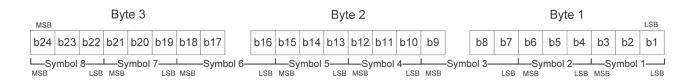


Figure A.4 — Binary information from PCD to PICC transmission for bit rates 3fc/4 and 3fc/2

For bit rates *fc* and *2fc* binary information shall be transmitted from PCD to PICC in units of 16 logic levels, building an information symbol of 4 bits. The 16 logic levels are represented by 16 NPs. The formation of 4 bit symbols from Bytes is illustrated in Figure A.5.

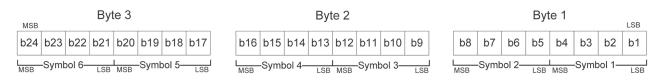


Figure A.5 — Binary information from PCD to PICC transmission for bit rates fc and 2fc

If the last transmitted symbol is incomplete, it shall be stuffed with one or two (0)b.

For end of communication, the PCD shall generate a sequence of 8 NPs of -180°. After the end of communication the PCD shall generate an unmodulated RF carrier with a NP of 0°.

A.2.1 Bit representation and coding for bit rates of 3fc/4 and 3fc/2

For start of communication the PCD(shall generate) a) sequence of 140 NPs starting with NP of etu #1 as specified in Table Al7, The phase of the unmodulated RE carrier is defined as NP = 0°. 58a930041e62/iso-iec-14443-2-2010-fdamd-5