



**INTERNATIONAL STANDARD ISO/IEC 23003-1:2007
TECHNICAL CORRIGENDUM 4**

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INTERNATIONAL ORGANIZATION FOR STANDARDIZATION • МЕЖДУНАРОДНАЯ ОРГАНИЗАЦИЯ ПО СТАНДАРТИЗАЦИИ • ORGANISATION INTERNATIONALE DE NORMALISATION
INTERNATIONAL ELECTROTECHNICAL COMMISSION • МЕЖДУНАРОДНАЯ ЭЛЕКТРОТЕХНИЧЕСКАЯ КОМИССИЯ • COMMISSION ÉLECTROTECHNIQUE INTERNATIONALE

Information technology — MPEG audio technologies —

Part 1: MPEG Surround

TECHNICAL CORRIGENDUM 4

Technologies de l'information — Technologies audio MPEG —

Partie 1: Ambiance MPEG

RECTIFICATIF TECHNIQUE 4

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[ISO/IEC 23003-1:2007/Cor.4:2012](https://standards.iteh.ai/catalog/standards/sist/fe7a3e8a-9215-4d05-bd2d-ISO/IEC/23003-1:2007/Cor.4:2012)

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Technical Corrigendum 4 to ISO/IEC 23003-1:2007 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 29, *Coding of audio, picture, multimedia and hypermedia information*.

In 3.3.1, replace:

$y = \log_2(x)$ is the base-2 logarithm of x .

with:

$y = \log_{10}(x)$ is the base-10 logarithm of x .

In 4.6, replace:

The surround gain is applied to each surround channel (i.e., left surround and right surround for a 5.1 configuration) in the PCM domain after QMF synthesis.

with:

The surround gain is applied to each surround channel in the PCM domain after QMF synthesis (i.e. left surround and right surround for a 5.1 configuration with the addition of rear left surround and rear right surround for a 727₂ or 757₂ configuration).

At the end of 4.6, add:

The application of pre- and post-gains is dependent on the output configuration according to the following table. In the case of binaural output, the surround gain is explicitly included in the formulas for power reconstruction (see 6.11.4)

Table X - Application of pre- and post-gains

	Stereo output	Binaural output	Multichannel output
Surround gain	no	yes	yes
LFE gain	no	no	yes
Downmix gain	no	no	yes

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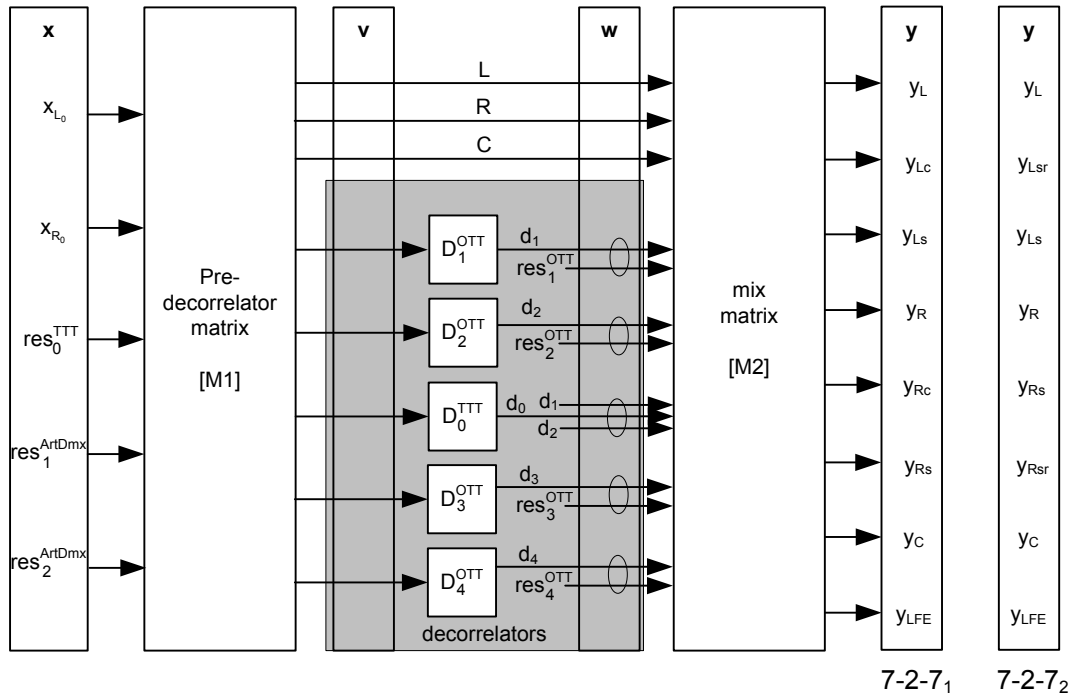
In 6.4.3.2.1 replace:

$$\mathbf{v}^{n,k} = \mathbf{M}_1^{n,k} \mathbf{x}^{n,k} = \mathbf{M}_1^{n,k} \begin{bmatrix} x_{L_0}^{n,k} \\ x_{R_0}^{n,k} \\ x_{res_0^{TTT}}^{n,k} \\ x_{res_1^{ArtDmx}}^{n,k} \\ x_{res_2^{ArtDmx}}^{n,k} \end{bmatrix} = \begin{bmatrix} v_L^{n,k} \\ v_R^{n,k} \\ v_C^{n,k} \\ v_{OTT_2}^{n,k} \\ v_{OTT_1}^{n,k} \\ v_{TTT_0}^{n,k} \end{bmatrix}$$

with:

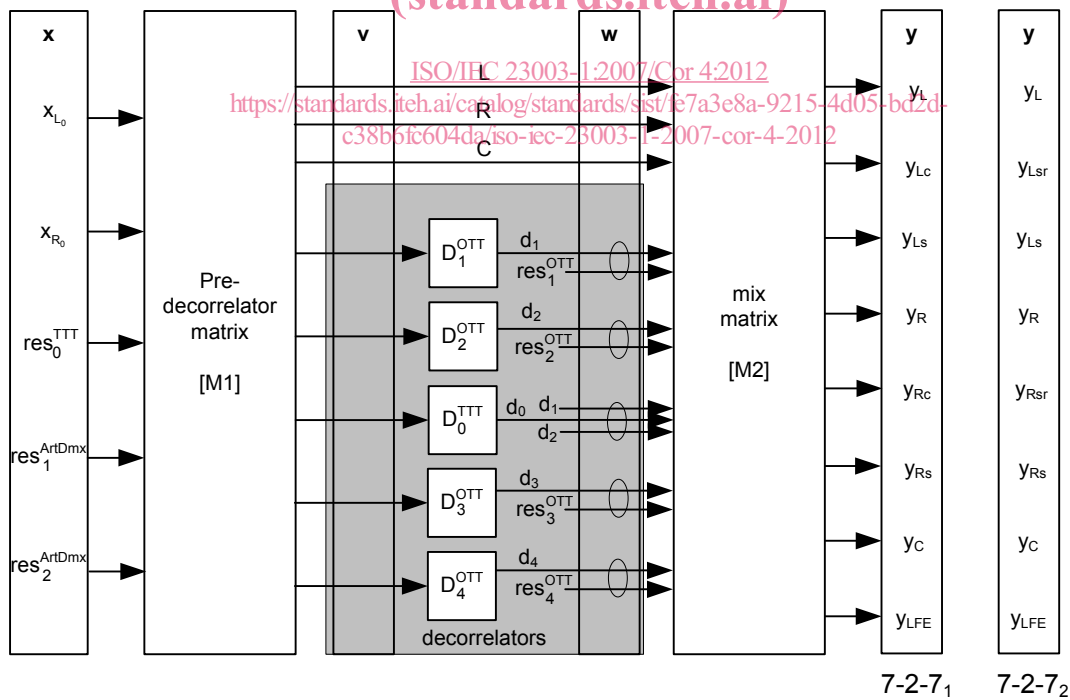
$$\mathbf{v}^{n,k} = \mathbf{M}_1^{n,k} \mathbf{x}^{n,k} = \mathbf{M}_1^{n,k} \begin{bmatrix} x_{L_0}^{n,k} \\ x_{R_0}^{n,k} \\ x_{res_0^{TTT}}^{n,k} \\ x_{res_1^{ArtDmx}}^{n,k} \\ x_{res_2^{ArtDmx}}^{n,k} \end{bmatrix} = \begin{bmatrix} v_L^{n,k} \\ v_R^{n,k} \\ v_C^{n,k} \\ v_{OTT_1}^{n,k} \\ v_{OTT_2}^{n,k} \\ v_{TTT_0}^{n,k} \end{bmatrix}$$

In 6.4.4.1, replace Figure 27:



with:

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In 6.4.4.1 replace (2 times):

Figure 26

with:

Figure 28

In 6.4.4.2.1 replace:

$$\mathbf{y}^{n,k} = \mathbf{M}_2^{n,k} \mathbf{w}^{n,k} = \mathbf{M}_2^{n,k} \begin{bmatrix} W_L^{n,k} \\ W_R^{n,k} \\ W_C^{n,k} \\ W_{OTT_1}^{n,k} \\ W_{OTT_2}^{n,k} \\ W_{TTT_0}^{n,k} \\ W_{OTT_3}^{n,k} \\ W_{OTT_4}^{n,k} \end{bmatrix} = \begin{bmatrix} \mathcal{Y}_L^{n,k} \\ \mathcal{Y}_{Lsr}^{n,k} \\ \mathcal{Y}_{Ls}^{n,k} \\ \mathcal{Y}_R^{n,k} \\ \mathcal{Y}_{Rs}^{n,k} \\ \mathcal{Y}_{Rsr}^{n,k} \\ \mathcal{Y}_C^{n,k} \\ \mathcal{Y}_{LFE}^{n,k} \end{bmatrix} \text{ for the 7-2-7}_2 \text{ configuration.}$$

with:

$$\mathbf{y}^{n,k} = \mathbf{M}_2^{n,k} \mathbf{w}^{n,k} = \mathbf{M}_2^{n,k} \begin{bmatrix} W_L^{n,k} \\ W_R^{n,k} \\ W_C^{n,k} \\ W_{OTT_1}^{n,k} \\ W_{OTT_2}^{n,k} \\ W_{TTT_0}^{n,k} \\ W_{OTT_3}^{n,k} \\ W_{OTT_4}^{n,k} \end{bmatrix} = \begin{bmatrix} \mathcal{Y}_L^{n,k} \\ \mathcal{Y}_{Lsr}^{n,k} \\ \mathcal{Y}_{Ls}^{n,k} \\ \mathcal{Y}_R^{n,k} \\ \mathcal{Y}_{Rs}^{n,k} \\ \mathcal{Y}_{Rsr}^{n,k} \\ \mathcal{Y}_C^{n,k} \\ \mathcal{Y}_{LFE}^{n,k} \end{bmatrix} \text{ for the 7-2-7}_2 \text{ configuration.}$$

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In 6.4.4.2.1 replace:

Figure 26

with:

Figure 28

In 6.4.5, Figure 32, replace (2 times):

CLD_3, ICC_3

with:

CLD_0, ICC_0

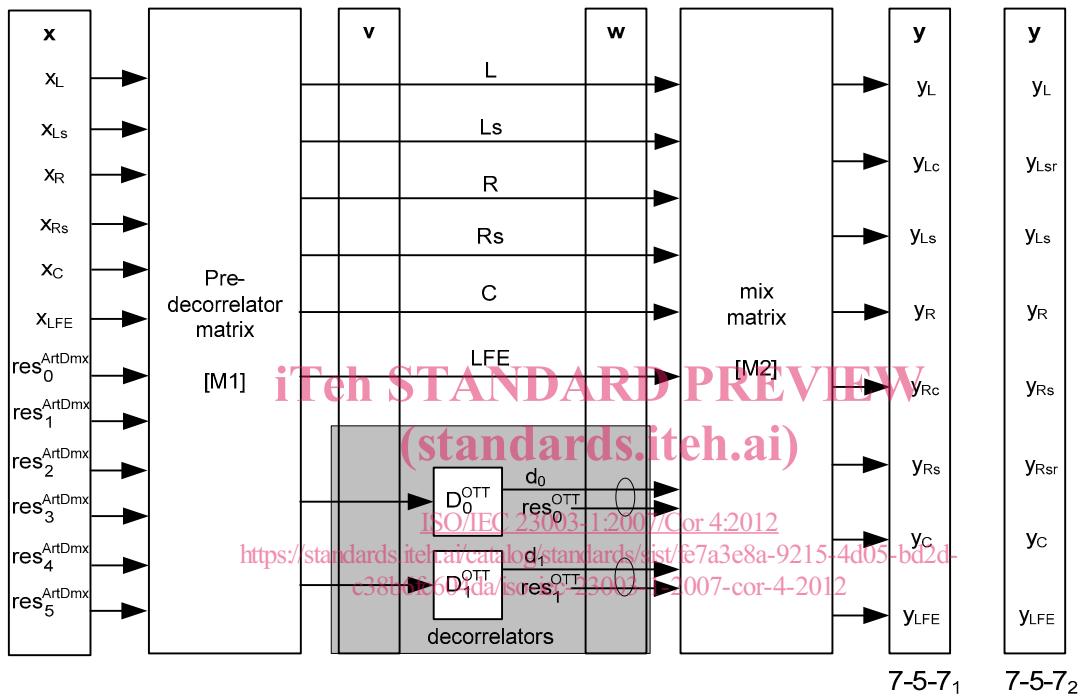
In 6.4.5, Figure 32 replace (2 times):

CLD_4, ICC_4

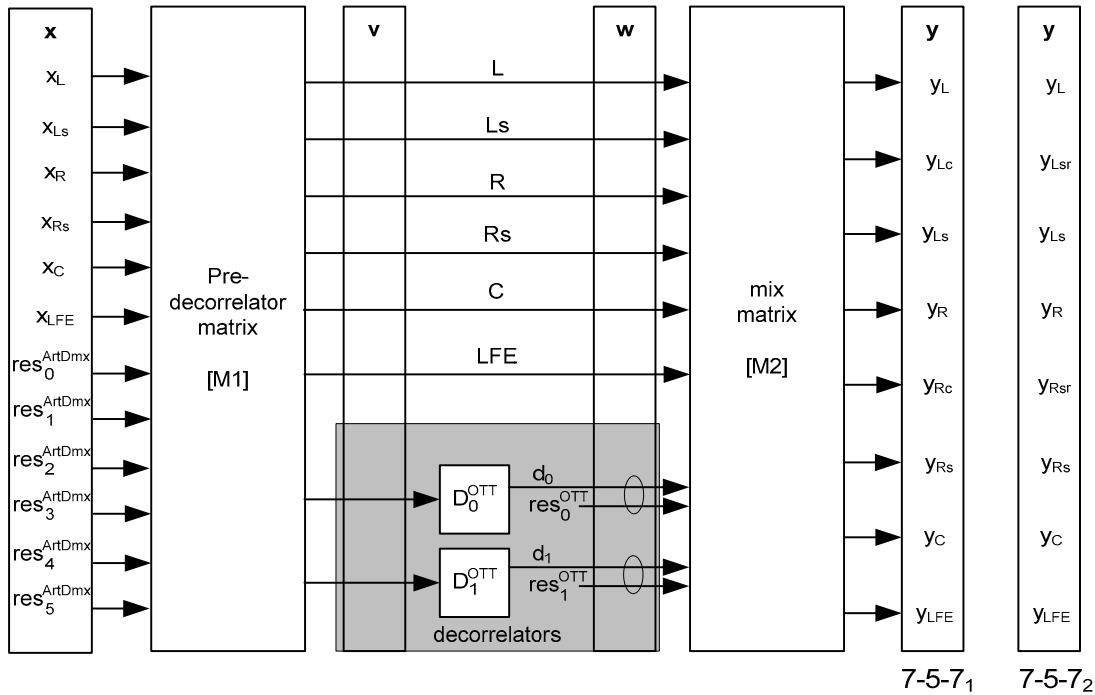
with:

CLD_1, ICC_1

In 6.4.5.1, replace Figure 30:



with:



In 6.4.5.2.1 replace:

Figure 26

with:

Figure 31

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In 6.5.2.2.4.2, replace:

Figure 26

with:

Figure 28

In 6.5.3.6, replace:

Figure 26

with:

Figure 28

In 6.5.3.7, replace:

Figure 26

with:

Figure 31

In 6.10.7.1, replace Table 103:

Low power decorrelators				
Configuration	Lattice IIR, LP0	Lattice IIR LP1	PS	No decorrelation
5-1-5 ₁	$D_0()$	$D_1()$	$D_2(), D_3()$	-
5-1-5 ₂	$D_0()$	$D_3(), D_4()$	$D_1()$	-
5-2-5	-	-	$D_1^{OTT}(), D_2^{OTT}()$	$D_0^{TTT}()$
7-2-7 ₁	$D_3(), D_4()$	-	$D_1^{OTT}(), D_2^{OTT}()$	$D_0^{TTT}()$
7-2-7 ₂	$D_3(), D_4()$	-	$D_1^{OTT}(), D_2^{OTT}()$	$D_0^{TTT}()$
7-5-7 ₁	-	-	$D_0(), D_1()$	-
7-5-7 ₂	-	-	$D_0(), D_1()$	-

with:

<https://standards.itech.ai/catalog/standards/sist/fe7a3e8a-9215-4d05-bd2d-c38b6fc604da/iso-iec-23003-1:2007/cor-4:2012>

Low power decorrelators				
Configuration	Lattice IIR, LP0	Lattice IIR, LP1	PS	No decorrelation
5-1-5 ₁	$D_0()$	$D_1()$	$D_2(), D_3()$	-
5-1-5 ₂	$D_0()$	$D_3(), D_4()$	$D_1()$	-
5-2-5	-	-	$D_1^{OTT}(), D_2^{OTT}()$	$D_0^{TTT}()$
7-2-7 ₁	$D_3(), D_4()$	-	$D_1^{OTT}(), D_2^{OTT}()$	$D_0^{TTT}()$
7-2-7 ₂	$D_3(), D_4()$	-	$D_1^{OTT}(), D_2^{OTT}()$	$D_0^{TTT}()$
7-5-7 ₁	-	-	$D_0(), D_1()$	-
7-5-7 ₂	-	-	$D_0(), D_1()$	-

In Annex G.3, replace all occurrences of:

S3AV

with:

S3AC