

### SLOVENSKI STANDARD SIST EN IEC 62228-1:2018

01-september-2018

### Integrirana vezja - Vrednotenje elektromagnetne združljivosti (EMC) oddajnikovsprejemnikov - 1. del: Splošni pogoji in definicije (IEC 62228-1:2018)

Integrated Circuits - EMC evaluation of transceivers - Part 1: General conditions and definitions (IEC 62228-1:2018)

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#### Ta slovenski standard je istoveten z: EN IEC 62228-1:2018 https://standards.iten.avcatalog/standards/sist/a2244c4-68c9-4b2c-9802-

4d55cbddc400/sist-en-iec-62228-1-2018

### ICS:

31.200	Integrirana vezja, mikroelektronika	Integrated circuits. Microelectronics
33.100.01	Elektromagnetna združljivost na splošno	Electromagnetic compatibility in general

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en

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#### SIST EN IEC 62228-1:2018

### EUROPEAN STANDARD NORME EUROPÉENNE **EUROPÄISCHE NORM**

### EN IEC 62228-1

June 2018

ICS 31.200

**English Version** 

### Integrated circuits - EMC evaluation of transceivers - Part 1: General conditions and definitions (IEC 62228-1:2018)

Circuits intégrés - Évaluation CEM des émetteursrécepteurs - Partie 1 : Conditions générales et définitions (IEC 62228-1:2018)

Integrierte Schaltungen - Bewertung der elektromagnetischen Verträglichkeit von Sende-Empfangsgeräten - Teil 1: Allgemeine Bedingungen und Festlegungen (IEC 62228-1:2018)

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European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Rue de la Science 23, B-1040 Brussels

#### European foreword

The text of document 47A/1018/CDV, future edition 1 of IEC 62228-1, prepared by IEC/SC 47A "Integrated circuits" of IEC/TC 47 "Semiconductor devices" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN IEC 62228-1:2018.

The following dates are fixed:

•	latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement	(dop)	2018-12-01
•	latest date by which the national standards conflicting with the document have to be withdrawn	(dow)	2021-06-01

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#### **Endorsement notice**

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### Annex ZA (normative)

## Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

Publication IEC 61967-1	<u>Year</u> -	<u>Title</u> Integrated circuits - Measurement of	<u>EN/HD</u> EN 61967-1	<u>Year</u> -
		GHz Part 1: General conditions and definitions		
IEC 61967-4	2002	Integrated circuits - Measurement of electromagnetic emissions, 150 kHz to 1 GHz Part 4: Measurement of conducted emissions - 1 ohm/150 ohm direct coupling	EN 61967-4	2002
+ A1	2006	methodrandard PREVI	L A1	2006
-	-	(standards.iteh.ai)	+ corrigendum Dec.	2006
IEC 62132-1	-	electromagnetic immunity - Part 1: General conditions and definitions 8-1:2018	EN 62132-1	-
IEC 62132-4	https://star	Integrated circuits Measurement of 68c9-4 electromagnetic immunity, 150 kHz to 1 GHz Part 4: Direct RF power injection method	EN 62132-4	-
IEC 62215-3	-	Integrated circuits - Measurement of impulse immunity Part 3: Non-	EN 62215-3	-
ISO 10605	-	Road vehicles - Test methods for electrical disturbances from electrostatic discharge	-	-

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### IEC 62228-1

Edition 1.0 2018-01

# INTERNATIONAL STANDARD

Integrated circuits **TEMC** evaluation of transceivers **VIEW** Part 1: General conditions and definitions iteh.ai)

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#### INTERNATIONAL ELECTROTECHNICAL COMMISSION

#### INTEGRATED CIRCUITS – EMC evaluation of transceivers –

#### Part 1: General conditions and definitions

#### FOREWORD

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International Standard IEC 62228-1 has been prepared by subcommittee 47A: Integrated circuits, of IEC technical committee 47: Semiconductor devices.

The text of this International Standard is based on the following documents:

CDV	Report on voting	
47A/1018/CDV	47A/1034/RVC	

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.