SLOVENSKI STANDARD
oSIST prEN 62228-1:2017

01-september-2017

Integrirana vezja - Vrednotenje elektromagnetne združljivosti (EMC) oddajnikov-sprejemnikov - 1. del: Splošni pogoji in definicije

Integrated Circuits - EMC evaluation of transceivers - Part 1: General conditions and definitions

Ta slovenski standard je istoveten z: **prEN 62228-1:2017**

ICS:

31.200	Integrirana vezja, mikroelektronika	Integrated circuits. Microelectronics

oSIST prEN 62228-1:2017 **en**

47A/1018/CDV

COMMITTEE DRAFT FOR VOTE (CDV)

PROJECT NUMBER: **IEC 62228-1 ED1**	
DATE OF CIRCULATION: **2017-07-07**	CLOSING DATE FOR VOTING: **2017-09-29**
SUPERSEDES DOCUMENTS: **47A/995/CD,47A/1013A/CC**	

IEC SC 47A : INTEGRATED CIRCUITS	
SECRETARIAT: Japan	SECRETARY: Mr Yoshinori FUKUBA
OF INTEREST TO THE FOLLOWING COMMITTEES:	PROPOSED HORIZONTAL STANDARD: ☐ – Other TC/SCs are requested to indicate their interest, if any, in this CDV to the secretary.
FUNCTIONS CONCERNED: ☒ EMC ☐ ENVIRONMENT	☐ QUALITY ASSURANCE ☐ SAFETY
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TITLE:

Integrated circuits - EMC evaluation of transceivers - Part 1: General conditions and definitions

NOTE FROM TC/SC OFFICERS:

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

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INTEGRATED CIRCUITS –
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Part 1: General conditions and definitions

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International Standard IEC 62228-1 has been prepared by subcommittee 47A: Integrated circuits, of IEC technical committee 47: Semiconductor devices.

The text of this standard is based on the following documents:

FDIS	Report on voting
47A/XX/FDIS	47A/XX/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 62228 series, published, in development or planned under the general title *Integrated circuits – EMC Evaluation of transceivers*, can be found below.

Part 1: General conditions and definitions

Part 2: LIN transceivers

Part 3: CAN transceivers

Part 4: FlexRay transceivers

Part 5: Ethernet transceivers

Part 6: PSI5 transceivers

Part 7: CXPI transceivers

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