



## DRAFT AMENDMENT ISO/IEC 14443-3.2:2011/DAM 4

ISO/IEC JTC 1

Secretariat: **ANSI**

Voting begins on  
**2015-06-18**

Voting terminates on  
**2015-08-18**

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# Identification cards — Contactless integrated circuit cards — Proximity cards —

## Part 3: Initialization and anticollision

### AMENDMENT 4: Handling rules of RFU bits

*Cartes d'identification — Cartes à circuit(s) intégré(s) sans contact — Cartes de proximité —*

*Partie 3: Initialisation et anticollision*

*AMENDEMENT 4: Règles de manipulation des bits RFU*

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ICS 35.240.15

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Published in Switzerland

## Foreword

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Amendment 4 to ISO/IEC 14443-3:2010 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 17, *Cards and personal identification*.

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## Identification cards — Contactless integration circuit cards - Proximity cards — Part 3: Initialization and Anticollision

### Amendment 4: Handling rules of RFU bits

*Page 5, Clause 4*

Add new paragraph after last dash:

“For the purposes of this part of ISO/IEC 14443, the following general rule applies:

A PICC or PCD sending RFU bits shall set these bits to the value indicated herein or to (0)b if no value is given. A PICC or PCD receiving RFU bits shall disregard the value of these bits and shall maintain and not change its function, unless explicitly stated otherwise.”

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*Page 6, Clause 6*

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Delete second paragraph:

*Page 15, 6.4.1*

Delete penultimate sentence

Replace last sentence with:

“A PICC receiving a short frame containing an RFU value shall interpret this as Error (see Figure 7) and shall not send a response.”

*Page 15, 6.4.3*

Replace following paragraph and NOTE:

“If the PICC responds with any modulation during a period of 1 ms after the end of the frame containing the HLTA command, this response shall be interpreted as 'not acknowledge'.

NOTE The PCD should apply an additional waiting time margin of 0,1 ms.”

with:

“When receiving a valid HLTA command in any state, the PICC shall not respond. The PCD shall not interpret any modulation received during a period of 1 ms after the end of the frame containing the HLTA command.”

NOTE In ACTIVE or ACTIVE\* state, the PICC may respond to an invalid HLTA command.”

Page 17, 6.5.2.1

Replace first sentence with:

“Table 4 specifies the coding of ATQA.”

Replace Table 4 with:

MSB											LSB					
b16	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	
Each bit RFU				Proprietary coding				UID size bit frame		RFU		Bit frame anticollision				

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Delete second paragraph after Table 4 and last paragraph.

Page 20, 6.5.3.2

Replace last paragraph and NOTE with:

“A PICC receiving an RFU value should interpret it as Error (see Figure 7) and should not send a response.”

Page 20, 6.5.3.3

Replace two paragraphs after Table 8 with:

“The PCD shall set NVB only to values defined in Table 8 except that for byte counts 6 and 7 only bit count of 0 is allowed.

A PICC receiving a byte count value not specified in Table 8 (b8 to b5), or receiving a bit count value not specified in Table 8 (b4 to b1) for byte count equal 2 to 5 or set to any value other than 0 for byte count equal 6 or 7, should interpret it as Error (see Figure 7) and should not send a response.”

Page 21, 6.5.3.4

Replace the paragraph after Table 9 with:

“For b3 = (1)b the PCD shall ignore any other bit of SAK. For b3 = (0)b the PCD shall interpret b6 and shall ignore any of the remaining bits of SAK.”

Page 23, 6.5.4

Replace

“A PICC sending uid0 with an RFU value is not compliant with this standard. A PICC sending a proprietary number shall fulfil all other requirements of the anticollision sequence including CT otherwise such a PICC is not compliant with this standard.”

with

“A PICC sending a proprietary number shall fulfil all other requirements of the anticollision sequence including CT.”

Page 24, Clause 7

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Delete second paragraph.

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Page 35, 7.7.3

Delete the first sentence of the paragraph after Table 22.

Page 36, 7.7.4

Replace Figure 22 with:

b8	b7	b6	b5	b4	b3	b2	b1
Each bit RFU			Extended ATQB supported	REQB / WUPB	N		

Figure 22 — Coding of PARAM

Delete second paragraph.

Delete the two paragraphs before Table 23.

Replace the paragraph after Table 23 with:

“Until the RFU values (101)b or (11x)b are assigned by ISO/IEC, a PICC receiving (b3 to b1) = (101)b or (11x)b shall interpret it as (b3 to b1) = (100)b (16 slots).”

Delete the paragraph before the note.

Page 39, 7.9.4

Delete last paragraph.

Page 39, 7.9.4.2

Replace the last paragraph with:

“b4 is RFU.”

Page 40, 7.9.4.3

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Delete the paragraph after NOTE1.

Replace last paragraph with: <https://standards.iteh.ai/catalog/standards/sist/554477fd-9129-46dd-8dae-a86df0875a50/iso-iec-14443-3-2011-damd-4-2>

“Until the RFU value 15 is assigned by ISO/IEC, a PCD receiving FWI = 15 shall interpret it as FWI = 4.”

Page 41, 7.9.4.4

Replace the last two paragraphs with:

b4 is RFU.

The PCD shall not continue communicating with a PICC that sets b4 to (1)b.

Page 41, 7.9.4.5 (Amended by ISO/IEC 14443-3:2011/Amd.2:2012)

Delete second paragraph

Replace third paragraph and NOTE with:

“Until the RFU values 'D' - 'F' are assigned by ISO/IEC, a PCD receiving Maximum Frame Size Code = 'D' - 'F' shall interpret it as Maximum Frame Size Code = 'C' (Maximum frame size = 4 096 bytes).

**NOTE** This PCD **requirement** is added for PCD's compatibility with future PICCs when ISO/IEC defines the behavior for the RFU values of 'D' - 'F'.”



Page 41, 7.9.4.6

Replace the first paragraph with:

“Table 29 defines the bit rates supported by the PICC.”

Replace last paragraph with:

“Until the RFU values with b4 = (1)b are assigned by ISO/IEC, a PCD receiving Bit\_Rate\_capability with b4 = (1)b shall interpret the Bit\_Rate\_capability byte as if (b8 to b1) = (00000000)b (only  $f_c/128$  in both directions).”

Page 42, 7.9.4.7

Replace whole subclause with:

#### “7.9.4.7 Extended ATQB (optional)

The optional Extended ATQB byte (optional 4<sup>th</sup> byte of protocol info field) consists of two parts:

- the least significant half byte (b4 to b1) is RFU;
- the most significant half byte (b8 to b5) defines the Start-up Frame Guard time Integer (SFGI).

The SFGI codes an integer value used to define the Start-up Frame Guard Time (SFGT).

The SFGT defines a specific guard time replacing TR2 which is needed by the PICC before it is ready to receive the next frame after it has sent the Answer to ATTRIB command. SFGI is coded in the range from 0 to 14. The value of 15 is RFU. The values in the range from 0 to 14 are used to calculate the SFGT with the formula given below. The default value of SFGI is 0.

$$\text{SFGT} = (256 \times 16 / f_c) \times 2^{\text{SFGI}}.$$

For SFGI = 0, SFGT is minimal (~ 302  $\mu$ s).

For SFGI = 14, SFGT is maximal (~ 4949 ms).

Until the RFU value 15 is assigned by ISO/IEC, a PCD receiving SFGI = 15 shall interpret it as SFGI = 0.

The PCD shall ignore (b4 to b1) and its interpretation of any other field of the whole frame shall not change.

When answering a REQB/WUPB command with bit b5 set to (0)b (no extended ATQB supported) the PICC shall not send the optional 4th byte in its ATQB response.”

Page 43, 7.10.3

Replace Figure 28 with: