

SLOVENSKI STANDARD SIST EN IEC 60747-16-6:2019

01-november-2019

Polprevodniški elementi - 16-6. del: Mikrovalovna integrirana vezja - Frekvenčni množitelji (IEC 60747-16-6:2019)

Semiconductor devices - Part 16-6: Microwave integrated circuits - Frequency multipliers (IEC 60747-16-6:2019)

Halbleiterbauelemente - Teil 16-6: Integrierte Mikrowellenschaltkreise - Frequenzvervielfachung (IEC 60747-16-6:2019) PREVIEW

Dispositifs à semiconducteurs - Partie 16-6. Circuits intégrés hyperfréquences - Multiplicateurs de fréquence (IEC 60747-16-6:2019)

https://standards.iteh.ai/catalog/standards/sist/e604b602-7116-41c7-87d6-

Ta slovenski standard je istoveten 2:4/sist-EN IEC 60747016-6:2019

ICS:

31.080.01 Polprevodniški elementi Semiconductor devices in

(naprave) na splošno general

31.200 Integrirana vezja, Integrated circuits.

mikroelektronika Microelectronics

SIST EN IEC 60747-16-6:2019 en

SIST EN IEC 60747-16-6:2019

iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN IEC 60747-16-6:2019 https://standards.iteh.ai/catalog/standards/sist/e604b602-7116-41c7-87d6-76a284d67684/sist-en-iec-60747-16-6-2019 EUROPEAN STANDARD NORME EUROPÉENNE EUROPÄISCHE NORM EN IEC 60747-16-6

August 2019

ICS 31.080.99

English Version

Semiconductor devices - Part 16-6: Microwave integrated circuits - Frequency multipliers (IEC 60747-16-6:2019)

Dispositifs à semiconducteurs - Partie 16-6: Circuits intégrés hyperfréquences - Multiplicateurs de fréquence (IEC 60747-16-6:2019)

Halbleiterbauelemente - Teil 16-6: Integrierte Mikrowellenschaltkreise - Frequenzvervielfachung (IEC 60747-16-6:2019)

This European Standard was approved by CENELEC on 2019-07-31. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CENELEC member.

I Ten STANDARD PREVIEW

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Bulgaria, Croatia, Cyprus, the Czech Republic, Denmark, Estonia, Finland, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, the Netherlands, Norway, Poland, Portugal, Republic of North Macedonia, Romania, Serbia, Slovakia, Slovenia, Spain, Sweden, Switzerland, Turkey and the United Kingdom.



European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Rue de la Science 23, B-1040 Brussels

EN IEC 60747-16-6:2019 (E)

European foreword

The text of document 47E/602/CDV, future edition 1 of IEC 60747-16-6, prepared by SC 47E "Discrete semiconductor devices" of IEC/TC 47 "Semiconductor devices" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN IEC 60747-16-6:2019.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2022-07-31

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CENELEC shall not be held responsible for identifying any or all such patent rights.

iTeh STANDARD PREVIEW Endorsement notice (standards.iten.ai)

The text of the International Standard IEC 60747-16-6:2019 was approved by CENELEC as a European Standard without any modification. Standard Standa

EN IEC 60747-16-6:2019 (E)

Annex ZA

(normative)

Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

<u>Publication</u>	<u>Year</u>	<u>Title</u>	EN/HD	<u>Year</u>			
IEC 60617	-	Graphical symbols for diagrams	-	-			
IEC 60747-1	2006	Semiconductor devices - Part 1: General	-	-			
+ A1	2010	eh STANDARD PREVI	EW	-			
IEC 60747-4	-	Semiconductor devices - Discrete devices Part 4: Microwave diodes and transistors		-			
IEC 60747-16-3	2002		: EN 60747-16-3	2002			
	Microwave integrated circuits 2 Frequency https://standards.jarcatalog/standards/sist/e604b602-7116-41c7-87d6- 76a284d67684/sist-en-iec-60747-16-6-2019						
+ A1	2009	/ Sales (46) / 66 (/ Sales eff 166 (66) / 1 / 16 (6 26) /	+ A1	2009			
+ A2	2017		+ A2	2017			
IEC 61340-5-1	-	Electrostatics - Part 5-1: Protection o electronic devices from electrostatic phenomena - General requirements		-			
IEC/TR 61340-5-2	-	Electrostatics Part 5-2: Protection o electronic devices from electrostatic phenomena - User guide		2-			

SIST EN IEC 60747-16-6:2019

iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN IEC 60747-16-6:2019 https://standards.iteh.ai/catalog/standards/sist/e604b602-7116-41c7-87d6-76a284d67684/sist-en-iec-60747-16-6-2019



IEC 60747-16-6

Edition 1.0 2019-06

INTERNATIONAL STANDARD

NORME INTERNATIONALE

Semiconductor devices – STANDARD PREVIEW
Part 16-6: Microwave integrated circuits – Frequency multipliers

Dispositifs à semiconducteurs Ten IEC 60747-16-6:2019

Partie 16-6: Circuits intégrés hyperfréquences + Multiplicateurs de fréquence

76a284d67684/sist-en-iec-60747-16-6-2019

INTERNATIONAL
ELECTROTECHNICAL
COMMISSION

COMMISSION ELECTROTECHNIQUE INTERNATIONALE

ICS 31.080.99 ISBN 978-2-8322-7081-3

Warning! Make sure that you obtained this publication from an authorized distributor.

Attention! Veuillez vous assurer que vous avez obtenu cette publication via un distributeur agréé.

- 2 - IEC 60747-16-6:2019 © IEC 2019

CONTENTS

F	FOREWORD4		
1	Scop	e	6
2	Norm	ative references	6
3	Term	s and definitions	6
4	Esse	ntial ratings and characteristics	8
	4.1	General requirements	8
	4.1.1	Circuit identification and types	
	4.1.2	•••	
	4.1.3	•	
	4.1.4	Package identification	8
	4.2	Application description	8
	4.2.1	Conformance to system and/or interface information	8
	4.2.2	Overall block diagram	8
	4.2.3	Reference data	8
	4.2.4	Electrical compatibility	8
	4.2.5	Associated devices	9
	4.3	Specification of the function	9
	4.3.1	Detailed block diagram – Functional blocks	9
	4.3.2		
	4.3.3	Function description and ards.iteh.ai	. 10
	4.4	Limiting values (absolute maximum rating system)	. 10
	4.4.1	Requirements <u>SIST-EN-IEC 60747-16-6:2019</u>	
	4.4.2		
	4.4.3	Temperatures 76a284d67684/sist-en-iec-60747-16-6-2019	. 11
	4.5	Operating conditions (within the specified operating temperature range)	
	4.6	Electrical characteristics	
	4.7	Mechanical and environmental ratings, characteristics and data	
	4.8	Additional information	
5	Meas	uring methods	. 13
	5.1	General	. 13
	5.1.1	General precautions	. 13
	5.1.2	Characteristic impedance	. 13
	5.1.3	Handling precautions	. 13
	5.1.4	71	
	5.2	Output power (P _O)	
	5.2.1	Purpose	
	5.2.2	ŭ	
	5.2.3	•	
	5.2.4	•	
	5.2.5		
	5.2.6	•	
	5.2.7	•	
	5.3	Conversion gain $(G_{\mathbf{C}})$	
	5.3.1	Purpose	
	5.3.2	ŭ	
	5.3.3	•	
	5.3.4	Circuit description and requirements	. 15

5.3.5	Precautions to be observed	15			
5.3.6	Measurement procedure	15			
5.3.7	Specified conditions	15			
5.4	Input return loss $(L_{ret(in)})$	15			
5.4.1	Purpose				
5.4.2	Circuit diagram	15			
5.4.3	Principle of measurement	16			
5.4.4	Circuit description and requirements	16			
5.4.5	Precautions to be observed				
5.4.6	Measurement procedure				
5.4.7	Specified conditions	17			
5.5	Output return loss ($L_{ret(out)}$)	17			
5.5.1	Purpose				
5.5.2	Circuit diagram	17			
5.5.3	Principle of measurement				
5.5.4	Circuit description and requirements				
5.5.5	Precautions to be observed				
5.5.6	Measurement procedure				
5.5.7	Specified conditions				
5.6	Fundamental isolation (P_0/P_1)				
5.6.1	PurposeTeh.STANDARD PREVIEW	19			
5.6.2					
5.6.3	Circuit diagram (Standards.iteh.ai) Principle of measurement	19			
5.6.4					
5.6.5	Circuit description and requirements. SIST EN IEC 60747-16-6:2019 Precautions to be observed https://standards.ten.acatalogs.tandards/sist/e604b602-7116-41c7-87d6-	19			
5.6.6	Measurement procedure 84/sistem jec-60747-16-6-2019	19			
5.6.7	Specified conditions				
5.7	n-th order harmonic isolation (P_{O}/P_{nth})	20			
5.7.1	Purpose				
5.7.2	Circuit diagram	20			
5.7.3	Principle of measurement	20			
5.7.4	Circuit description and requirements	20			
5.7.5	Precautions to be observed	20			
5.7.6	Measurement procedure	20			
5.7.7	Specified conditions	21			
5.8	Phase noise (𝒯(ƒ))	21			
5.8.1	Purpose	21			
5.8.2	Measuring methods				
E		4.0			
_	Circuit diagram for the measurement of the output power				
_	Circuit diagram for the measurement of the input return loss				
Figure 3 –	Circuit diagram for the measurement of the output return loss	17			
Figure 4 –	Circuit diagram for the measurement of the phase noise $\mathscr{L}(f)$ (method 1)	22			
Figure 4 – Circuit diagram for the measurement of the phase noise $\mathcal{S}(f)$ (method 1)					
Figure 6 – Circuit diagram for the measurement of the phase noise $\mathcal{L}(f)$ (method 3)24					
		<i></i>			
Table 1 – Comparison of phase noise measuring methods21					

-4 -

INTERNATIONAL ELECTROTECHNICAL COMMISSION

SEMICONDUCTOR DEVICES -

Part 16-6: Microwave integrated circuits – Frequency multipliers

FOREWORD

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, EC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.

 SIST EN IEC 60747-16-6:2019
- 5) IEC itself does not provide any attestation of conformity independent certification bodies provide conformity assessment services and, in some areas, access to IEC marks of conformity. IEC is not responsible for any services carried out by independent certification bodies.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

International Standard IEC 60747-16-6 has been prepared by subcommittee 47E: Discrete semiconductor devices, of IEC technical committee 47: Semiconductor devices.

The text of this International Standard is based on the following documents:

CDV	Report on voting
47E/602/CDV	47E/622A/RVC

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

The French version of this standard has not been voted upon.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

IEC 60747-16-6:2019 © IEC 2019

- 5 -

A list of all parts in the IEC 60747 series, published under the general title *Semiconductor devices*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

iTeh STANDARD PREVIEW (standards.iteh.ai)

SIST EN IEC 60747-16-6:2019 https://standards.iteh.ai/catalog/standards/sist/e604b602-7116-41c7-87d6-76a284d67684/sist-en-iec-60747-16-6-2019

IEC 60747-16-6:2019 © IEC 2019

SEMICONDUCTOR DEVICES -

- 6 **-**

Part 16-6: Microwave integrated circuits – Frequency multipliers

1 Scope

This part of IEC 60747 specifies the terminology, essential ratings and characteristics, and measuring methods of microwave integrated circuit frequency multipliers.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60617, Graphical symbols for diagrams (available at http://std.iec.ch/iec60617>)

IEC 60747-1:2006, Semiconductor devices Part 1. General VIEW IEC 60747-1:2006/AMD 1:2010 (standards.iteh.ai)

IEC 60747-4, Semiconductor devices – Discrete devices – Part 4: Microwave diodes and transistors SIST EN IEC 60747-16-6:2019

https://standards.iteh.ai/catalog/standards/sist/e604b602-7116-41c7-87d6-

IEC 60747-16-3:2002, Semiconductor devices icc Part 16-3:2 Microwave integrated circuits – Frequency converters

IEC 60747-16-3:2002/AMD 1:2009 IEC 60747-16-3:2002/AMD 2:2017

IEC 61340-5-1, Electrostatics – Part 5-1: Protection of electronic devices from electrostatic phenomena – General requirements

IEC TR 61340-5-2, Electrostatics – Part 5-2: Protection of electronic devices from electrostatic phenomena – User guide

3 Terms and definitions

For the purposes of this document, the following terms and definitions apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

3.1

output frequency

frequency which equals the input frequency multiplied by the multiplying factor N, where N is an integer