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## Information technology — MPEG audio technologies —

### Part 2: Spatial Audio Object Coding (SAOC)

AMENDMENT 3: Dialogue enhancement

iTeh STANDARD PREVIEW  
*Technologies de l'information — Technologies audio MPEG —  
(standards.iteh.ai)*

*AMENDEMENT 3: Rehaussement des dialogues*  
ISO/IEC 23003-2:2010/Amd.3:2015

<https://standards.iteh.ai/catalog/standards/sist/6f594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015>

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The procedures used to develop this document and those intended for its further maintenance are described in the ISO/IEC Directives, Part 1. In particular the different approval criteria needed for the different types of document should be noted. This document was drafted in accordance with the editorial rules of the ISO/IEC Directives, Part 2 (see [www.iso.org/directives](http://www.iso.org/directives)).

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The committee responsible for this document is ISO/IEC JTC 1, *Information technology*, Subcommittee SC 29, *Coding of audio, picture, multimedia and hypermedia information*.

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# Information technology — MPEG audio technologies —

## Part 2: Spatial Audio Object Coding (SAOC)

### AMENDMENT 3: Dialogue enhancement

*Add Clause 12, Spatial Audio Object Coding — Dialogue Enhancement (SAOC-DE):*

#### 12 Spatial Audio Object Coding — Dialogue Enhancement

##### 12.1 Introduction

This Clause specifies the SAOC Dialogue Enhancement (SAOC-DE) profile. The SAOC-DE decoder processing and bitstream syntax are defined according Clauses 1 to 9 with the following modifications.

###### — Basic structure of the SAOC transcoder/decoder

*Add in “[Table 2 — Operation modes of the SAOC](#)” the following text:*

**iTeh STANDARD PREVIEW**  
**Table 2 — Operation modes of the SAOC**

Output signal configuration	# of output channels	# of input channels	SAOC module mode	SAOC module output	MPS decoder required
<a href="https://standards.iteh.ai/catalog/standards/sist/61594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015">https://standards.iteh.ai/catalog/standards/sist/61594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015</a>					
mono/stereo/binaural 3-channel configuration	1, 2 or 3	1, 2 or 3	Decoder	PCM output	No
multi-channel configuration	> 2	1 or 2	Transcoder	MPS bitstream, downmix signal	Yes

###### — SAOC Profiles and Levels

*Add in “[Table 4 — SAOC Profiles and Levels](#)” the following text:*

**Table 4 — SAOC Profiles and Levels**

Profiles	Baseline profile				DE profile		LD profile		
	1	2	3	4	1	2	1	2	3
Levels									
Hybrid QMF bank	X	X	X	X	X	X	-	-	-
LD-QMF bank	-	-	-	-	-	-	X	X	X
Max number of residual channels	0	2	4	4	0	3	-	-	-
Max sampling rate [kHz]	48	48	48	96	48	48	48	48	48
Max number of objects	8	16	32	32	6	6	8	32	32
Max number of downmix channels	2	2	2	2	3	3	1	2	2
Min number of required output channels*)	2	2	2	2	1	1	2	2	5
Use of decorrelator	yes	yes	yes	yes	no	no	yes	yes	yes
PCU HQ decoder	12.2	20.4	33.9	67.8	12.4	22.1	8.4	20.7	39.3**)
PCU LP decoder	6.6	12.2	23.0	46.0	11.4	21.0	N/A	N/A	N/A

**Table 4 (continued)**

Profiles	Baseline profile				DE profile		LD profile		
PCU addition for transcoding	1.1	1.1	1.1	2.3	N/A	N/A	0.7	1.1	N/A
PCU reduction for integrated transcoding	-6.8	-6.8	-6.8	-6.8	N/A	N/A	-3.6	-6.5	N/A
RCU HQ decoder	5.7	9.8	13.5	17.5	6.3	12.3	3.6	4.2	17.9***)
RCU LP decoder	4.8	5.4	5.7	10.3	7.3	7.9	N/A	N/A	N/A
RCU reduction for integrated transcoding	-1.3	-1.3	-1.3	-1.3	N/A	N/A	-0.6	-1.3	N/A

Add below “[Table 4 – SAOC Profiles and Levels](#)” the following text:

MPS transcoding support for baseline and LD profile if the number of output channels > 2

Replace below “[Table 4 – SAOC Profiles and Levels](#)” the following text:

The SAOC decoder type is defined by the four conditions:

- Profile: baseline profile or LD profile

by

The SAOC decoder type is defined by the four conditions:

- Profile: baseline, LD or DE profile

Replace in “5.5 SAOC Profiles and Levels”:

For all profiles and levels the following features are supported:  
<https://standards.iec.avcatalog.standards/sist/61594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015>

- Decoding to mono/stereo/binaural output

by

For baseline and low-delay profiles:

- Decoding to mono/stereo/binaural output. Transcoding to 5.1 is supported

For Dialogue enhancement profile:

- Decoding to mono/stereo/3-channel output. No transcoding to 5.1 is supported
- Multi-channel background object (MBO) processing, DCU processing, MCU processing, separation metadata and send effects interface are not supported
- Post-downmix gain processing (PDG) is supported only in combination with post(processing) re-application processing step
- Insert effects interface is supported only if no modification range control (MRC) settings are transported in the bitstream

## 12.2 Terms and definitions

Add in “4.4 Variables”:

$N_{FGO}$  is the number of FGOs.

$\mathbf{D}_{FGO}$  is the downmix sub-matrix for FGOs.

$\mathbf{D}_{BGO}$	is the downmix sub-matrix for BGOs.
$m_{BGO}$	is the modification gain for BGOs.
$m_{FGO}$	is the modification gain for FGOs.
$m_G$	is the decoder limited modification gain.
$m_G^{input}$	is the input modification gain.

Add in "4.5 Abbreviated terms":

<b>BGO</b>	Background Object
<b>FGO</b>	Foreground Object
<b>DE</b>	Dialogue Enhancement

### 12.3 Payloads for SAOC-DE

The bitstream syntax of the SAOC-DE is not compatible with the Baseline and Low Delay profiles of SAOC. The following changes are applied for SAOC-DE profile in "6.1 Payloads for SAOC":

#### Introduction of DE bitstream elements

Replace in "Table 5 — Syntax of SAOCspecificConfig()  
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**Table 5 — Syntax of SAOCspecificConfig()**

Syntax	ISO/IEC 23003-2:2010/Amd 3:2015	No. of bits	Mnemonic
SAOCspecificConfig() {	<a href="https://standards.iteh.ai/catalog/standards/sist/61594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015">https://standards.iteh.ai/catalog/standards/sist/61594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015</a>		

by

**Table 5 — Syntax of SAOCDEspecificConfig()**

Syntax	No. of bits	Mnemonic
SAOCDEspecificConfig() {		

Replace in "Table 20 — Syntax of SAOCFrame():"

**Table 20 — Syntax of SAOCFrame()**

Syntax	No. of bits	Mnemonic
SAOCFrame() {		

by

**Table 20 — Syntax of SAOCDEFframe()**

Syntax	No. of bits	Mnemonic
SAOCDEFframe() {		

Replace in "Table 21 — SAOCFramingInfo():"

**Table 21 — Syntax of SAOCFramingInfo()**

Syntax	No. of bits	Mnemonic
SAOCFramingInfo()		
{		

by

**Table 21 — Syntax of SAOCDEFramingInfo()**

Syntax	No. of bits	Mnemonic
SAOCDEFramingInfo()		
{		

Replace in “Table 20 — Syntax of SAOCFrame()”:

**Table 20 — Syntax of SAOCFrame()**

Syntax	No. of bits	Mnemonic
SAOCFrame()		
{		
SAOCFramingInfo();		

by

**iTeh STANDARD PREVIEW****Table 20 — Syntax of SAOCDEFrame()  
(standards.iteh.ai)**

Syntax	No. of bits	Mnemonic
SAOCDEFrame()	<a href="#">ISO/IEC 23003-2:2010/Amd 3:2015</a>	
{	<a href="https://standards.iteh.ai/catalog/standards/sist/6f594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015">https://standards.iteh.ai/catalog/standards/sist/6f594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015</a>	
SAOCDEFramingInfo();		

**Introduction of SAOC version bitstream element**

Add in “Table 5 — Syntax of SAOCspecificConfig()”:

SAOCDEspecificConfig()		
{		
<b>bsVersion;</b>	<b>4</b>	<b>uimsbf</b>
if ( bsVersion == 0 ) {		
<b>bsSamplingFrequencyIndex;</b>	<b>4</b>	<b>uimsbf</b>

Add in “Table 5 — Syntax of SAOCspecificConfig()”:

SAOCExtensionConfig();		
}		
}		

Add in “Table 20 — Syntax of SAOCFrame()”:

SAOCDEFrame()		
{		
if ( bsVersion == 0 ) {		
SAOCDEFramingInfo();		

Add in “Table 20 — Syntax of SAOCFrame()”:

```
    SAOCExtensionFrame();  
}  
}
```

## **Disabling SAOC Low Delay mode signalization**

*Remove from “Table 5 — Syntax of SAOCSpecificConfig()”:*

**bsLowDelayMode;** 1 uimsbf

Replace in “Table 5 –Syntax of SAOCSSpecificConfig()”:

```
if ( bsLowDelayMode == 0 ) {  
    bsFrameLength;                                7      uimsbf  
} else {  
    bsFrameLength;                                5      uimsbf  
}
```

by

bsFrameLength; iTeh STANDARD PREVIEW 7 uimsbf

Replace in "Table 21 — SAOCFramingInfo": **(standards.iteh.ai)**

```
If ( bsLowDelayMode == 0 ) {           ISO/IEC 23003-2:2010/Amd 3:2015  
bsNumParamSets; https://standards.iteh.ai/catalog/standards/sist/6f594619-58b8-4ebc-bff6- 3           uimsbf  
} else {                           64d4016f1752/iso-iec-23003-2-2010-amd-3-2015  
  
bsNumParamSets;                   1           uimsbf  
}  
}
```

by

**bsNumParamSets;** **3** **uimsbf**

## Disabling absolute energy information transport

*Remove from “Table 5 — Syntax of SAOCSpecificConfig()”:*

**bsTransmitAbsNrg;** 1 uimsbf

Remove from “Table 20 — Syntax of SAOCFrame()”:

```
if ( bsTransmitAbsNrg ) {
    idxNRG = EcDataSaoc(NRG, 0, numBands);
}
```

### Modification on object information transport

Replace in “Table 5 — Syntax of SAOCspecificConfig()”:

<b>bsNumObjects;</b>	<b>5</b>	<b>uimsbf</b>
----------------------	----------	---------------

by

<b>bsNumObjects;</b>	<b>3</b>	<b>uimsbf</b>
<b>bsNumFGOs;</b>	<b>3</b>	<b>uimsbf</b>

### Modification on downmix channel number signalization

Replace in “Table 5 — Syntax of SAOCspecificConfig()”:

<b>bsNumDmxChannels;</b>	<b>1</b>	<b>uimsbf</b>
--------------------------	----------	---------------

by

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### Dual mode configuration information transport

ISO/IEC 23003-2:2010/Amd 3:2015

Remove from “Table 5 — Syntax of SAOCspecificConfig()”:  
<http://jtc1.sistech.org/standards/sist/6f594619-58b8-4ebc-bff6-64d4016f1752/iso-iec-23003-2-2010-amd-3-2015>

if ( bsNumDmxChannels == 1 ) {		
<b>bsTttDualMode;</b>	<b>1</b>	<b>uimsbf</b>
if (bsTttDualMode) {		
<b>bsTttBandsLow;</b>	<b>5</b>	<b>uimsbf</b>
bsTttBandsHigh = numBands;		
} else {		Note 1
bsTttBandsLow = numBands;		
}		
}		

**Disabling post downmix gain information transport***Remove from "Table 5 — Syntax of SAOCspecificConfig()":*

<b>bsPdgFlag;</b>	<b>1</b>	<b>uimsbf</b>
-------------------	----------	---------------

*Remove from "Table 20 — Syntax of SAOCFrame()":*

```
if ( bsPdgFlag == 1 ) {
    for (i=0; i<bsNumDmxChannels + 1; i++) {
        idxPDG[i] = EcDataSaoc(PDG, i, numBands);
    }
}
```

Note 1

**Modification on downmix information transport***Replace in "Table 20 — Syntax of SAOCFrame()":*

```
idxDMG = EcDataSaoc(DMG, 0, bsNumObjects+1);
if ( bsNumDmxChannels == 1 ) {
    idxDCLD = EcDataSaoc(DCLD, 0, bsNumObjects+1);
}
```

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```
for (i=0; i<bsNumDmxChannels + 1; i++) {
    idxDMG[i] = EcDataSaoc(DMG, 0, bsNumObjects+1);  

    https://standards.iteh.ai/catalog/standards/sist/6f594619-58b8-4ebc-bff6-  
64d4016f752/iso-ice-23003-2-2010-AMD-3-2015
}
```

**Modification range control setting transport***Add in "Table 5 — Syntax of SAOCspecificConfig()" the following text:*

<b>bsOneIOC;</b>	<b>1</b>	<b>uimsbf</b>
<b>bsDeLimitFlag;</b>	<b>1</b>	<b>uimsbf</b>
if ( bsDeLimitFlag == 1 ) {		
<b>bsDeLimitFgo;</b>	<b>4</b>	<b>uimsbf</b>
<b>bsDeLimitBgo;</b>	<b>4</b>	<b>uimsbf</b>
} else {		
bsDeLimitFgo = 0;		
bsDeLimitBgo = 0;		
}		