
**Information technology — MPEG video
technologies —**

**Part 4:
Video tool library**

**AMENDMENT 1: Graphics tool library (GTL)
for the reconfigurable multimedia coding
(RMC) framework**

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ISO/IEC 23002-4:2014/Amd.1:2014
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*Technologies de l'information — Technologies vidéo MPEG —
Partie 4: Bibliothèque d'outils vidéo*

*AMENDEMENT 1: Bibliothèque d'outils graphiques (GTL) pour le cadre
de codage multimédia reconfigurable (RMC)*

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<https://standards.iteh.ai/catalog/standards/sist/a719ff17-ea08-4db2-9952-43b780230e8f/iso-iec-23002-4-2014-amd-1-2014>

Information technology — MPEG video technologies —

Part 4: Video Tool Library

AMENDMENT 1: Graphics tool library (GTL) for the reconfigurable multimedia coding (RMC) framework

In 4.1 "FU Interfaces", before Table 1, add the following text:

- In several FU diagrams, the ports are named with a trailing “_i” for the input port type and with a trailing “_o” for the output port type.
 - Some FU diagrams contains as well the Finite State Machine diagram. The following conventions apply: INPUT - the action of reading a token or a set of tokens from the input port, OUTPUT - the action of writing the token or a set of tokens to an output port.
 - "Parameter" is set at network configuration stage (cannot be changed during the process) and it is characteristic for each FU
 - Token RANGE: describes the mathematical interval for the token value
- Examples:
- Token RANGE: { 0, 1 } — binary value
- Token RANGE: [0 .. N], $value \in [0, N]$ real values, closed interval
- All the FUs require the data to be in little-endian format.

In 4.2 "FU IDs", complete Table 2 with the following lines:

Note: update the FU table..

ID	FU Name
107	Algo_Parser_SC3DMC
108	Algo_InverseQuantization1D
109	Algo_InverseQuantizationND
110	Algo_InversePrediction1D
111	Algo_InversePredictionND
112	Algo_ED_AD_StaticBit
113	Algo_ED_AD_AdaptiveBit
114	Algo_ED_VLD
115	Algo_ED_BitPrecision
116	Algo_ED_AD
117	Algo_ED_AD_EG
118	Algo_ContextModeling

119	Algo_ContextModeling_SVA_nType
120	Algo_ContextModeling_SVA_Indexes
121	Algo_ContextModeling_SVA_Vertex_Attribute
122	Algo_ED_4bitsD
123	Algo_ED_FixedLength
124	Algo_LookUpTable1D
125	Algo_DecodeConnectivity_SVA
126	Algo_DecodeConnectivity_TFAN
127	Algo_ExtractMask_SC3DMC
128	Algo_ExtractFaceDirection_SVA
129	Algo_simpleMath_2op
130	Algo_Connectivity_InversePrediction_SVA
131	Mgnt_Replicate_1_2
132	Mgnt_Replicate_1_4
133	Mgnt_Replicate_1_8
134	Mgnt_MUX_2_1
135	Mgnt_MUX_4_1
136	Mgnt_MUX_8_1
137	Mgnt_DEMUX_1_2
138	Mgnt_DEMUX_1_4
139	Mgnt_DEMUX_1_8
140	Mgnt_ExtractSegment
141	Mgnt_ProviderValue
142	Mgnt_RepeatSegment
143	Mgnt_ExtractBytes
144	Mgnt_ExtractBits
145	Mgnt_Provider1D
146	Mgnt_Provider2D

4.3 "Token Pool":

Add the following rows at the end of the table.

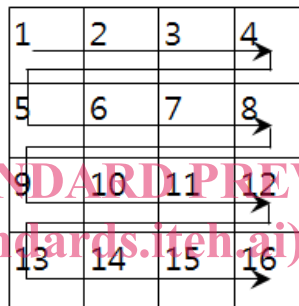
ID & Name	Description
55 BOOLEAN	Token which value is 0 or 1.
56 SIGN	Token which value is 0 or 1.
57 FLAG	Token which value is 0 or 1.
58 UINT_2	Unsigned integer on 2 bits.
59 UINT_4	Unsigned integer on 4 bits.
60 UINT_8	Unsigned integer on 8 bits.
61 UINT_16	Unsigned integer on 16 bits.
62 UINT_32	Unsigned integer on 32 bits.

63 UINT_64	Unsigned integer on 64 bits.
64 INT_8	Integer on 8 bits.
65 INT_16	Integer on 16 bits.
66 INT_32	Integer on 32 bits.
67 INT_64	Integer on 64 bits.
68 FLOAT_32	Float on 32 bits.

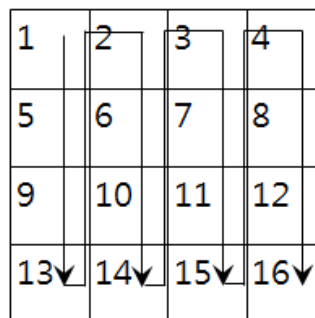
Add 4.4 "Array data order":

4.4 Array data order

- Row based:
 - The data is processed or sent in a sequential order, row by row
- Example:



- Column based
 - The data is processed or sent in a sequential order, column by column
- Example:



Add 4.5 "Input ports":

4.5 Input ports (reset_i, init_i, start_i)

An FU does not have an outside synchronization signal or synchronization mechanism. These ports are used for the purpose of changing the values of the local variables to default or initialization values.

Add 4.6 "FU block diagram notations":

4.6 FU block diagram notations

The notation [EMBED] defines a part of the main FSM schematic that is described as a separate schematic (for complexity reasons). The [EMBED] schematic is an integrated part of the main FSM schematic

The notation [MODULE] defines a part of the main FSM schematic that is defined as a separate FU. The module schematic is integrated in the main schematic with the entire FU logic, except the “START” FSM state. The INPUT/OUTPUT states do not read or write values from the ports, they refer to local variables relative to the FU that embeds the other schematic.

Add 4.7 "Conventions":

4.7 Conventions

The significance of the “sign” port values is:

Value	Significance
0	negative
1	positive

The significance of the “flag” values is:

Value	Significance
0	false
1	true

Add 5.2 "General Processing FUs":

5.2 General Processing FUs
5.2.1 Algo_InverseQuantization1D

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FU Name	Algo_InverseQuantization1D																							
Description																								
	<table border="1"> <thead> <tr> <th>Port Name</th> <th>Direction</th> <th>Token TYPE</th> </tr> </thead> <tbody> <tr> <td>dataIn_i</td> <td>I</td> <td>INT8, INT16, INT32, INT64</td> </tr> <tr> <td>qp_i</td> <td>I</td> <td>UINT_32</td> </tr> <tr> <td>quantMin_i</td> <td>I</td> <td>FLOAT</td> </tr> <tr> <td>quantRange_i</td> <td>I</td> <td>FLOAT</td> </tr> <tr> <td>segmentSize_i</td> <td>I</td> <td>UINT8, UINT16, UINT32, UINT64</td> </tr> <tr> <td>quantizationMode_i</td> <td>I</td> <td>UINT_2</td> </tr> <tr> <td>dataOut_o</td> <td>O</td> <td>FLOAT</td> </tr> </tbody> </table>	Port Name	Direction	Token TYPE	dataIn_i	I	INT8, INT16, INT32, INT64	qp_i	I	UINT_32	quantMin_i	I	FLOAT	quantRange_i	I	FLOAT	segmentSize_i	I	UINT8, UINT16, UINT32, UINT64	quantizationMode_i	I	UINT_2	dataOut_o	O
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quantRange_i	I	FLOAT																						
segmentSize_i	I	UINT8, UINT16, UINT32, UINT64																						
quantizationMode_i	I	UINT_2																						
dataOut_o	O	FLOAT																						
	<p>Inverse Quantization Process: START: INPUT_SEGMENT_PARAM: INPUT: quantizationMode segmentSize SegmentSizeCounter = 0 IF quantizationMode = 0 INPUT: qp quantMin quantRange IF (quantRange > 0.0) delta = ((1 << qp) - 1) / quantRange</p>																							

	<pre> ELSE delta = 1.0 IF quantizationMode = 1 INPUT: qp INPUT_DATA_IN: INPUT: dataIn IF quantizationMode = 0 PROCESS: dataOut = quantMin + (dataIn / delta) IF quantizationMode = 1 PROCESS: dataOut = dataIn / qp OUTPUT dataOut SegmentSizeCounter ++ IF SegmentSizeCounter < segmentSize GOTO INPUT_DATA_IN ELSE GOTO INPUT_SEGMENT_PARAM </pre> <p>The following table contains the quantization types index used in the Inverse Quantization 1D FU:</p> <table border="1"> <thead> <tr> <th>Name of quantization mode</th> <th>Value</th> </tr> </thead> <tbody> <tr> <td>Uniform Quantization</td> <td>0</td> </tr> <tr> <td>Uniform Texture Quantization</td> <td>1</td> </tr> </tbody> </table> <p>The "Inverse Quantization" is an algorithm (step by step procedures) that allows a set of data to be represented with a limited set of values that are associated with its nearest representative. For a number of "segmentSize" of input data (dataIn), it uses the same set of quantMin, quantRange and quantValue to produce a set of output data (dataOut) of size "segmentSize"</p>	Name of quantization mode	Value	Uniform Quantization	0	Uniform Texture Quantization	1
Name of quantization mode	Value						
Uniform Quantization	0						
Uniform Texture Quantization	1						
<p>ISO Standards using the FU</p>	<p>ISO/IEC 14496-16:2011 https://standards.iso.org/iso/iec/23002-4:2014/Amd.1:2014</p>						
<p>Profiles@levels supported</p>	<p>https://standards.ieh.ch/catalog/standards/sist/a719ff17-7ea08-4db2-9952-43b780230e8f/iso-iec-23002-4-2014-amd-1-2014</p>						

5.2.2 Algo_InverseQuantizationND

<p>FU Name</p>	<p>Algo_InverseQuantizationND</p>																								
<p>Description</p>	<div style="display: flex; align-items: flex-start;"> <div style="margin-right: 20px;"> </div> <table border="1" style="margin-right: 20px;"> <thead> <tr> <th>Port Name</th> <th>Direction (I/O)</th> <th>Token TYPE</th> </tr> </thead> <tbody> <tr> <td>dataIn_i</td> <td>I</td> <td>INT8, INT16, INT32, INT64</td> </tr> <tr> <td>qp_i</td> <td>I</td> <td>UINT_32</td> </tr> <tr> <td>quantMin_i</td> <td>I</td> <td>FLOAT</td> </tr> <tr> <td>quantRange_i</td> <td>I</td> <td>FLOAT</td> </tr> <tr> <td>segmentSize_i</td> <td>I</td> <td>UINT8, UINT16, UINT32, UINT64</td> </tr> <tr> <td>quantizationMode_i</td> <td>I</td> <td>UINT_2</td> </tr> <tr> <td>dataOut_o</td> <td>O</td> <td>FLOAT</td> </tr> </tbody> </table> </div>	Port Name	Direction (I/O)	Token TYPE	dataIn_i	I	INT8, INT16, INT32, INT64	qp_i	I	UINT_32	quantMin_i	I	FLOAT	quantRange_i	I	FLOAT	segmentSize_i	I	UINT8, UINT16, UINT32, UINT64	quantizationMode_i	I	UINT_2	dataOut_o	O	FLOAT
Port Name	Direction (I/O)	Token TYPE																							
dataIn_i	I	INT8, INT16, INT32, INT64																							
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quantMin_i	I	FLOAT																							
quantRange_i	I	FLOAT																							
segmentSize_i	I	UINT8, UINT16, UINT32, UINT64																							
quantizationMode_i	I	UINT_2																							
dataOut_o	O	FLOAT																							

Inverse Quantization Process:

$$dimQ = \begin{cases} dimD, & \text{if homogeneous } Q = 0 \\ 1, & \text{if homogeneous } Q = 1 \end{cases}$$

```

START:
INPUT_SEGMENT_PARAM
INPUT:
    quantizationMode
    segmentSize
SegmentSizeCounter = 0
IF quantizationMode = 0
    INPUT:
        qp
        quantMin [ dimQ ]
        quantRange [ dimQ ]
    WHILE dimQ_counter < dimQ
        IF ( quantRange [ dimQ_counter ] > 0.0 )
            delta [ dimQ_counter ] = ( ( 1 << qp ) - 1 ) / quantRange [ dimQ_counter ]
        ELSE
            delta [ dimQ_counter ] = 1.0;
        dimQ_counter++
IF quantizationMode = 1
    INPUT:
        Qp [ dimQ ]
IF quantizationMode = 2
    INPUT:
        qp
    PROCESS:
        Subdivision = (qp - 3) / 2

INPUT_DATA_IN:
INPUT:
    dataIn [ dimQ ]
IF quantizationMode = 0
    PROCESS:
        dataOut = quantMin [ segmentSizeCounter % dimD ] + (dataIn / delta [ segmentSizeCounter % dimD])
IF quantizationMode = 1
    PROCESS: EMBED Code 2 Normal
IF quantizationMode = 2
    PROCESS:
        dataOut = dataIn / qp [ segmentSizeCounter % dimD ]
OUTPUT
    dataOut
SegmentSizeCounter ++
IF SegmentSizeCounter < segmentSize
    GOTO INPUT_DATA_IN
ELSE
    GOTO INPUT_SEGMENT_PARAM

EMBED: Code 2 Normal

Mask = ( 1 << ( 2 * subdivision ) ) - 1
tricode = data & mask;

// Find y coordinate by solving 2nd degree equation
factor = 1 << subdivision
y = factor - sqrt ( (factor2) - tricode )
tricode = tricode + ( y * ( y - (2 * factor) ) )
x = tricode / 2
upsideDown = tricode % 2

// Calculate coordinates for all vertices in triangle
v1x = x + upsideDown
v1y = y + upsideDown
v2x = x + 1
v2y = y
v3x = x
v3y = y + 1

// Calculate coordinates of barycenter

```

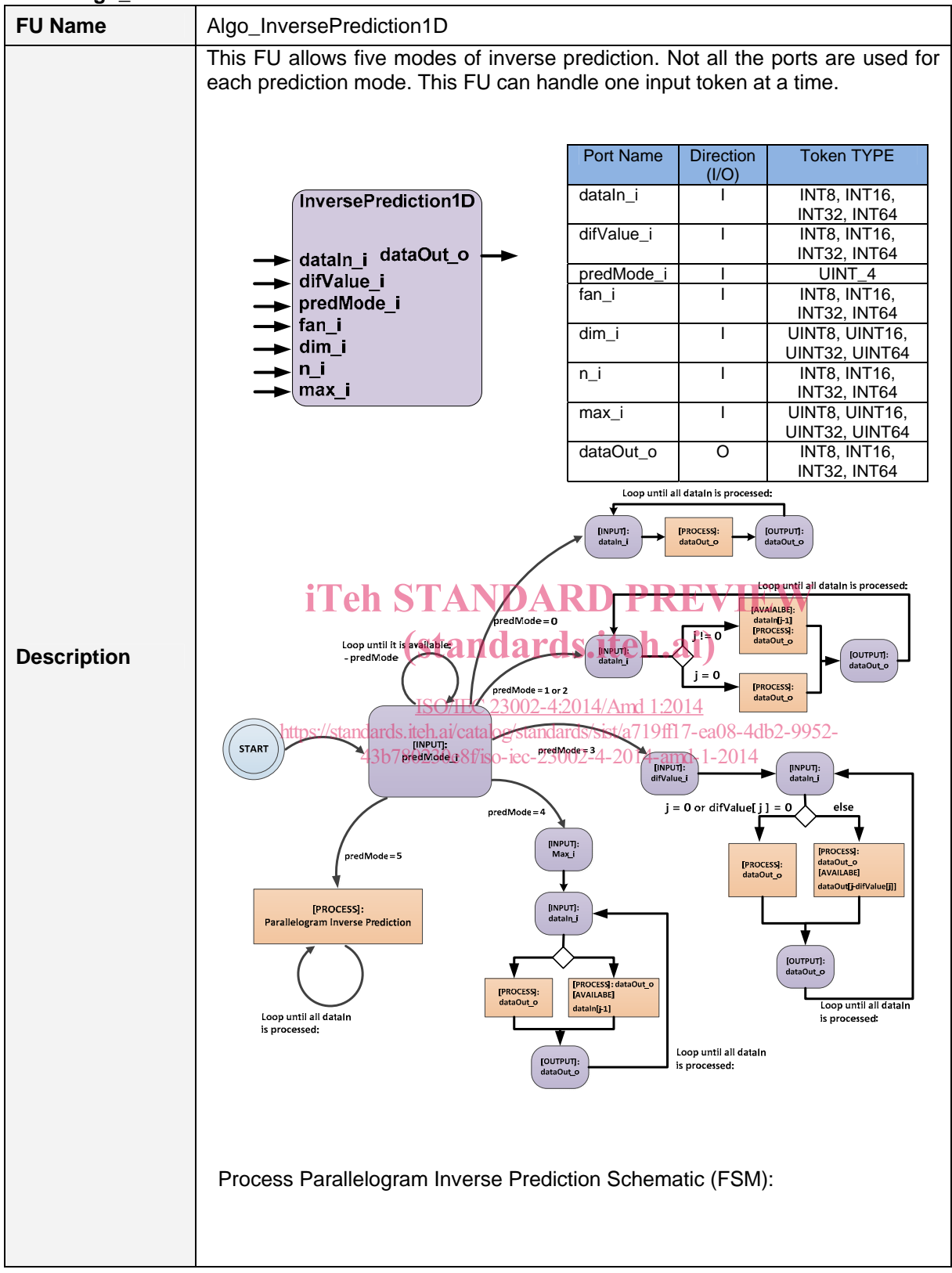
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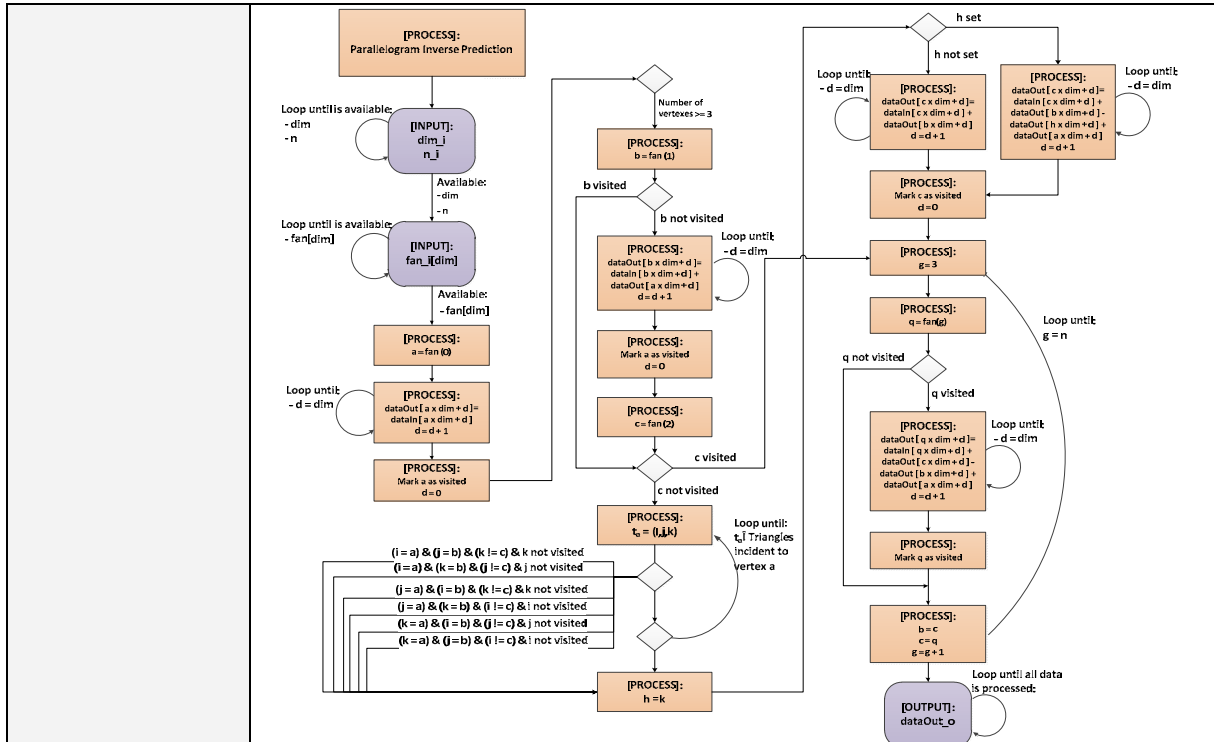
<https://standards.iteh.ai/catalog/standards/sist/1b780230e8f140408402199521b780230e8f/iso-iec-23002-4-2014-amd-1-2014>

	<pre> invMaxCoord = 1 / factor normal [0] = (v1x + v2x + v3x) * invMaxCoord normal [1] = (v1y + v2y + v3y) * invMaxCoord normal [2] = 3 - normal [0] - normal [1] // Flip component signs if necessary octantCode = (data >> 2 * subdivision) & 0x7 if (octantCode & 0x4) normal [0] = (-1) * normal [0] if (octantCode & 0x2) normal [1] = (-1) * normal [1] if (octantCode & 0x1) normal [2] = (-1) * normal [2] invNorm:= 1 / sqrt ((normal[0])^2 + (normal [1])^2 + (normal [2])^2); //Write the 3 output values normal [0] = normal [0] * invNorm normal [1] = normal [1] * invNorm normal [2] = normal [2] * invNorm dataOut = normal </pre> <p>The following table contains the quantization types index used in the Inverse Quantization ND FU:</p> <table border="1" data-bbox="427 792 951 922"> <thead> <tr> <th>Name of quantization mode</th> <th>Value</th> </tr> </thead> <tbody> <tr> <td>Uniform Quantization</td> <td>0</td> </tr> <tr> <td>Normal Quantization</td> <td>1</td> </tr> <tr> <td>Uniform Texture Quantization</td> <td>2</td> </tr> </tbody> </table> <p>The "Inverse Quantization" is an algorithm (step by step procedures) that allows a set of data to be represented with a limited set of values that are associated with its nearest representative. (standards.iteh.ai)</p> <p>For a number of "segmentSize" x "dimD" of input data (dataIn), it uses the same set of quantMin, quantRange and quantValue of size "dimD" to produce a set of output data (dataOut) of size segmentSize x "dimD".</p> <p>For each set of size "dimD" of input data (dataIn) it uses the corresponding value of quantMin, quantRange and quantValue.</p>	Name of quantization mode	Value	Uniform Quantization	0	Normal Quantization	1	Uniform Texture Quantization	2
Name of quantization mode	Value								
Uniform Quantization	0								
Normal Quantization	1								
Uniform Texture Quantization	2								
<p>ISO Standards using the FU</p>	<p>ISO/IEC 14496-16:2011</p>								
<p>Profiles@levels supported</p>									
<p>Parameter</p>									
<p>Name</p>	<p>Description</p>	<p>Type / Range</p>							
<p>dimD</p>	<p>Describes the number of tokens of type dataIn_i that are consumed at each firing. This parameter is set at the network configuration level.</p>	<p>Type: Integer Range: [1 .. 2⁵]</p>							
<p>homogeneousQ</p>	<p>Describes the number of tokens of type quantRange_i, quantMin_i and quantValue_i that are necessary for the inverse quantization process. This parameter is set at the network configuration level. The number of tokens is equal to dimD if this parameter is 0 and the number of tokens is equal to 1 if this parameter is 1</p>	<p>Type: Boolean Range: {0,1}</p>							

5.2.3 Algo_InversePrediction1D



Description



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Switch (predMode)

{

Case 0: NP – No Prediction

$dataOut[j] = dataIn[j], \forall j \in \{0..N-1\}$

Case 1: Diff – Differential Prediction

$$dataOut[j] = \begin{cases} DataIn[j], & \forall j = 0 \\ DataIn[j] + DataOut[j-1], & \forall j \in \{1..N-1\} \end{cases}$$

Case 2: XOR – based prediction

$$dataOut[j] = \begin{cases} dataIn[j], & \forall j = 0 \\ dataIn[j] \otimes dataOut[j-1], & \forall j \in \{1..N-1\} \end{cases}$$

Case 3: Adaptive Prediction

$$dataOut[j] = \begin{cases} dataIn[j], & \text{if } difValue[j] = 0 \text{ or } j = 0 \\ dataIn[j] + dataOut[j - difValue[j]], & \text{otherwise} \end{cases}$$

Case 4: Circular Differential Prediction

$$dataOut[j] = \begin{cases} dataIn[j], & \forall j = 0 \\ d, & \text{if } dataIn[j] < dataIn[j-1], \text{ where} \\ -d, & \text{otherwise} \end{cases}$$

$$d = \begin{cases} dataIn[j-1] + M_d - dataIn[j], & \text{if } dataIn[j] > dataIn[j-1] \\ dataIn[j] + M_d - dataIn[j-1], & \text{otherwise} \end{cases}$$

Case 5: Parallelogram Inverse Prediction

```

a = fan ( 0 )
if a not visited
    d = 0
    WHILE d < dim
        dataOut [ a x dim + d ] = dataIn [ a x dim + d ]
        d = d + 1
    Mark a as visited
If number of vertexes > 3
    b = fan ( 1 )
    if b not visited
        d = 0
        WHILE d < dim
            dataOut [ b x dim + d ] = dataIn [ b x dim + d ] + dataOut [ a x dim + d ]
            d = d + 1
        Mark b as visited
    c = fan ( 2 )
    if c not visited
        init h, ta
        WHILE ta = (i,j,k) ∈ Triangles incident to vertex a
            If ( i=a & j=b & k≠c and k not visited ) h = k, break
            If ( i=a & k=b & j≠c and j not visited ) h = k, break
            If ( j=a & i=b & k≠c and k not visited ) h = k, break
            If ( j=a & k=b & i≠c and i not visited ) h = k, break
            If ( k=a & i=b & j≠c and j not visited ) h = k, break
            If ( k=a & b=b & i≠c and i not visited ) h = k, break
        If h not set
            d = 0
            WHILE d < dim
                dataOut [ c x dim + d ] = dataIn [ c x dim + d ] + dataOut [ b x dim + d ]
                d = d + 1
            else
                d = 0
                WHILE d < dim
                    dataOut [ c x dim + d ] =
                        dataIn [ c x dim + d ] + dataOut [ b x dim + d ]
                        - dataOut [ h x dim + d ] + dataOut [ a x dim + d ]
                    d = d + 1
                Mark c as visited
            g = 3, d = 0
            WHILE g < fanSize
                q = fan ( g )
                if q not visited
                    WHILE d < dim
                        dataOut [ q x dim + d ] =
                            dataIn [ q x dim + d ] + dataOut [ c x dim + d ]
                            - dataOut [ b x dim + d ] + dataOut [ a x dim + d ]
                        d = d + 1
                    b = c
                    c = q

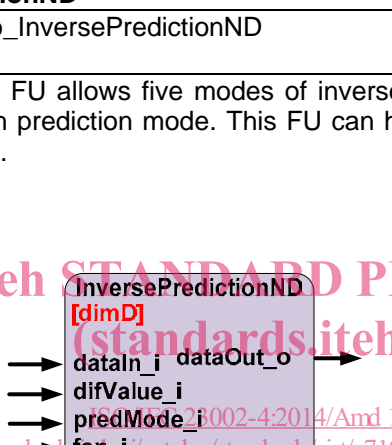
```

The following table contains the prediction types index used in the Inverse Prediction 1D FU:

Name of prediction mode	Value
No Prediction	0
Differential Prediction	1
XOR based prediction	2

	Adaptive Differential Prediction	3
	Circular Differential Prediction	4
	Parallelogram Inverse Prediction	5
	The detailed description of the inverse parallelogram prediction is described in Annex F.	
ISO Standards using the FU	ISO/IEC 14496-16:2011	
Profiles@levels supported		
Parameter		
Name	Description	Range

5.2.4 Algo_InversePredictionND

FU Name	Algo_InversePredictionND																											
Description	<p>This FU allows five modes of inverse prediction. Not all the ports are used for each prediction mode. This FU can handle a number of dimD input tokens at a time.</p> <div style="display: flex; align-items: center;">  <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>Port Name</th> <th>Direction (I/O)</th> <th>Token RANGE</th> </tr> </thead> <tbody> <tr> <td>dataIn_i</td> <td>I</td> <td>INT8, INT16, INT32, INT64</td> </tr> <tr> <td>difValue_i</td> <td>I</td> <td>INT8, INT16, INT32, INT64</td> </tr> <tr> <td>predMode_i</td> <td>I</td> <td>UINT_4</td> </tr> <tr> <td>fan_i</td> <td>I</td> <td>INT8, INT16, INT32, INT64</td> </tr> <tr> <td>dim_i</td> <td>I</td> <td>UINT8, UINT16, UINT32, UINT64</td> </tr> <tr> <td>n_i</td> <td>I</td> <td>INT8, INT16, INT32, INT64</td> </tr> <tr> <td>max_i</td> <td>I</td> <td>UINT8, UINT16, UINT32, UINT64</td> </tr> <tr> <td>dataOut_o</td> <td>O</td> <td>INT8, INT16, INT32, INT64</td> </tr> </tbody> </table> </div> <p>Process Schematic (FSM):</p>	Port Name	Direction (I/O)	Token RANGE	dataIn_i	I	INT8, INT16, INT32, INT64	difValue_i	I	INT8, INT16, INT32, INT64	predMode_i	I	UINT_4	fan_i	I	INT8, INT16, INT32, INT64	dim_i	I	UINT8, UINT16, UINT32, UINT64	n_i	I	INT8, INT16, INT32, INT64	max_i	I	UINT8, UINT16, UINT32, UINT64	dataOut_o	O	INT8, INT16, INT32, INT64
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