
Aktivne optične komponente in naprave - Standardi za ohišja in vmesnike - 21. del: Vodilo za načrtovanje električnega vmesnika za PIC-ohišja, ki uporabljajo silicijev fini raster mreže krogličnih priključkov (S-FBGA) in silicijev fini raster mreže priključkov v ravnini (S-FLGA) (IEC 62148-21:2019)

Fibre optic active components and devices - Package and interface standards - Part 21: Design guide of electrical interface of PIC packages using Silicon Fine-pitch Ball Grid Array (S-FBGA) and Silicon Fine-pitch Land Grid Array (S-FLGA) (IEC 62148-21:2019)

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Aktive Lichtwellenleiterbauelemente und Geräte - Gehäuse- und Schnittstellennormen - Teil 21: Konstruktionsleitfaden für elektrische Schnittstellen von PIC-Gehäusen mit Si-Feinraster-Ball-Grid-Array (S-FBGA) und Si-Feinraster-Land-Grid-Array (S-FLGA) (IEC 62148-21:2019)

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Composants et dispositifs actifs fibroniques - Normes de boîtier et d'interface - Partie 21: Guide de conception de l'interface électrique des boîtiers PIC utilisant des boîtiers matriciels à billes et à pas fins en silicium (S-FBGA) et des boîtiers matriciels à zone de contact plate et à pas fins en silicium (S-FLGA) (IEC 62148-21:2019)

Ta slovenski standard je istoveten z: EN IEC 62148-21:2019

ICS:

33.180.20	Povezovalne naprave za optična vlakna	Fibre optic interconnecting devices
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EUROPEAN STANDARD

EN IEC 62148-21

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Fibre optic active components and devices - Package and interface standards - Part 21: Design guide of electrical interface of PIC packages using silicon fine-pitch ball grid array (S-FBGA) and silicon fine-pitch land grid array (S-FLGA) (IEC 62148-21:2019)

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European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Rue de la Science 23, B-1040 Brussels

EN IEC 62148-21:2019 (E)**European foreword**

The text of document 86C/1571/FDIS, future edition 1 of IEC 62148-21, prepared by SC 86C "Fibre optic systems and active devices" of IEC/TC 86 "Fibre optics" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN IEC 62148-21:2019.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2020-01-15
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2022-04-15

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In the official version, for Bibliography, the following notes have to be added for the standards indicated:

IEC 60191-6-22 NOTE Harmonized as EN 60191-6-22

IEC 62148-1 NOTE Harmonized as EN IEC 62148-1

IEC 62148-19¹ NOTE Harmonized as EN IEC 62148-19²

¹ Under preparation. Stage at the time of publication: IEC/TFDIS 62148-19:2018.

² Under preparation. Stage at the time of publication: FprEN IEC 62148-19:2019.

Annex ZA (normative)

Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

<u>Publication</u>	<u>Year</u>	<u>Title</u>	<u>EN/HD</u>	<u>Year</u>
IEC 60050-731	-	International Electrotechnical Vocabulary - Chapter 731: Optical fibre communication	-	-
IEC/TR 61931	-	Fibre optic - Terminology	-	-

[SIST EN IEC 62148-21:2019](https://standards.iteh.ai/catalog/standards/sist/2a0ea283-f086-4912-bd4e-38357f1b43fc/sist-en-iec-62148-21-2019)
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Part 21: Design guide of electrical interface of PIC packages using silicon fine-
pitch ball grid array (S-FBGA) and silicon fine-pitch land grid array (S-FLGA)**

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Partie 21: Guide de conception de l'interface électrique des boîtiers PIC utilisant
des boîtiers matriciels à billes et à pas fins en silicium (S-FBGA) et des boîtiers
matriciels à zone de contact plate et à pas fins en silicium (S-FLGA)**

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**FIBRE OPTIC ACTIVE COMPONENTS AND DEVICES –
PACKAGE AND INTERFACE STANDARDS –**

**Part 21: Design guide of electrical interface of PIC
packages using silicon fine-pitch ball grid array (S-FBGA)
and silicon fine-pitch land grid array (S-FLGA)**

FOREWORD

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International Standard IEC 62148-21 has been prepared by subcommittee 86C: Fibre optic systems and active devices, of IEC technical committee 86: Fibre optics.

The text of this International Standard is based on the following documents:

FDIS	Report on voting
86C/1571/FDIS	86C/1577/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.