

ETSI TS 125 212 V17.0.0 (2022-05)



iTeh STANDARD
Universal Mobile Telecommunications System (UMTS);
Multiplexing and channel coding (FDD)
(3GPP TS 25.212 version 17.0.0 Release 17)
(standards.iteh.ai)

ETSI TS 125 212 V17.0.0 (2022-05)
<https://standards.iteh.ai/catalog/standards/sist/45ee5fd3-019f-4271-8325-70bbddac8321/etsi-ts-125-212-v17-0-0-2022-05>



Reference

RTS/TSGR-0025212vh00

Keywords

UMTS

ETSI

650 Route des Lucioles
 F-06921 Sophia Antipolis Cedex - FRANCE

Tel.: +33 4 92 94 42 00 Fax: +33 4 93 65 47 16

Siret N° 348 623 562 00017 - APE 7112B
 Association à but non lucratif enregistrée à la
 Sous-Préfecture de Grasse (06) N° w061004871

Important notice

The present document can be downloaded from:
<http://www.etsi.org/standards-search>

The present document may be made available in electronic versions and/or in print. The content of any electronic and/or print versions of the present document shall not be modified without the prior written authorization of ETSI. In case of any existing or perceived difference in contents between such versions and/or in print, the prevailing version of an ETSI deliverable is the one made publicly available in PDF format at www.etsi.org/deliver.

Users of the present document should be aware that the document may be subject to revision or change of status.

Information on the current status of this and other ETSI documents is available at
<https://portal.etsi.org/TB/ETSIDeliverableStatus.aspx>

If you find errors in the present document, please send your comment to one of the following services:

<https://portal.etsi.org/People/CommitteeSupportStaff.aspx>

If you find a security vulnerability in the present document, please report it through our
 Coordinated Vulnerability Disclosure Program:

<https://www.etsi.org/standards/coordinated-vulnerability-disclosure>

Notice of disclaimer & limitation of liability

The information provided in the present deliverable is directed solely to professionals who have the appropriate degree of experience to understand and interpret its content in accordance with generally accepted engineering or other professional standard and applicable regulations.

No recommendation as to products and services or vendors is made or should be implied.

No representation or warranty is made that this deliverable is technically accurate or sufficient or conforms to any law and/or governmental rule and/or regulation and further, no representation or warranty is made of merchantability or fitness for any particular purpose or against infringement of intellectual property rights.

In no event shall ETSI be held liable for loss of profits or any other incidental or consequential damages.

Any software contained in this deliverable is provided "AS IS" with no warranties, express or implied, including but not limited to, the warranties of merchantability, fitness for a particular purpose and non-infringement of intellectual property rights and ETSI shall not be held liable in any event for any damages whatsoever (including, without limitation, damages for loss of profits, business interruption, loss of information, or any other pecuniary loss) arising out of or related to the use or inability to use the software.

Copyright Notification

No part may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm except as authorized by written permission of ETSI.

The content of the PDF version shall not be modified without the written authorization of ETSI.

The copyright and the foregoing restriction extend to reproduction in all media.

Intellectual Property Rights

Essential patents

IPRs essential or potentially essential to normative deliverables may have been declared to ETSI. The declarations pertaining to these essential IPRs, if any, are publicly available for **ETSI members and non-members**, and can be found in ETSI SR 000 314: *"Intellectual Property Rights (IPRs); Essential, or potentially Essential, IPRs notified to ETSI in respect of ETSI standards"*, which is available from the ETSI Secretariat. Latest updates are available on the ETSI Web server (<https://ipr.etsi.org/>).

Pursuant to the ETSI Directives including the ETSI IPR Policy, no investigation regarding the essentiality of IPRs, including IPR searches, has been carried out by ETSI. No guarantee can be given as to the existence of other IPRs not referenced in ETSI SR 000 314 (or the updates on the ETSI Web server) which are, or may be, or may become, essential to the present document.

Trademarks

The present document may include trademarks and/or tradenames which are asserted and/or registered by their owners. ETSI claims no ownership of these except for any which are indicated as being the property of ETSI, and conveys no right to use or reproduce any trademark and/or tradename. Mention of those trademarks in the present document does not constitute an endorsement by ETSI of products, services or organizations associated with those trademarks.

DECT™, PLUGTESTS™, UMTS™ and the ETSI logo are trademarks of ETSI registered for the benefit of its Members. **3GPP™** and **LTE™** are trademarks of ETSI registered for the benefit of its Members and of the 3GPP Organizational Partners. **oneM2M™** logo is a trademark of ETSI registered for the benefit of its Members and of the oneM2M Partners. **GSM®** and the GSM logo are trademarks registered and owned by the GSM Association.

Legal Notice

(standards.iteh.ai)

This Technical Specification (TS) has been produced by ETSI 3rd Generation Partnership Project (3GPP).

[ETSI TS 125 212 V17.0.0 \(2022-05\)](#)

The present document may refer to technical specifications or reports using their 3GPP identities. These shall be interpreted as being references to the corresponding ETSI deliverables.

[3GPP technical specification and report identities](#)

[0191-4271-8323-7055ddac8521/etsi-ts-125-212-v17-0-](#)

[0-2022-05](#)

The cross reference between 3GPP and ETSI identities can be found under <http://webapp.etsi.org/key/queryform.asp>.

Modal verbs terminology

In the present document "**shall**", "**shall not**", "**should**", "**should not**", "**may**", "**need not**", "**will**", "**will not**", "**can**" and "**cannot**" are to be interpreted as described in clause 3.2 of the [ETSI Drafting Rules](#) (Verbal forms for the expression of provisions).

"**must**" and "**must not**" are **NOT** allowed in ETSI deliverables except when used in direct citation.

Contents

Intellectual Property Rights	2
Legal Notice	2
Modal verbs terminology.....	2
Foreword.....	9
1 Scope	10
2 References	10
3 Definitions, symbols and abbreviations	10
3.1 Definitions.....	10
3.2 Symbols.....	12
3.3 Abbreviations	13
4 Multiplexing, channel coding and interleaving.....	14
4.1 General	14
4.2 General coding/multiplexing of TrCHs.....	14
4.2.0 Transport channel concatenation	19
4.2.1 CRC attachment.....	19
4.2.1.1 CRC Calculation	19
4.2.1.2 Relation between input and output of the CRC attachment block	20
4.2.2 Transport block concatenation and code block segmentation.....	20
4.2.2.1 Concatenation of transport blocks.....	20
4.2.2.2 Code block segmentation	20
4.2.3 Channel coding	21
4.2.3.1 Convolutional coding.....	22
4.2.3.2 Turbo coding	23
4.2.3.2.1 Turbo coder	23
4.2.3.2.2 Trellis termination for Turbo coder	23
4.2.3.2.3 Turbo code internal interleaver.....	24
4.2.3.3 Concatenation of encoded blocks.....	27
4.2.3.4 Radio frame size equalisation.....	27
4.2.5 1 st interleaving	28
4.2.5.1 Void.....	28
4.2.5.2 1 st interleaver operation.....	28
4.2.5.3 Relation between input and output of 1 st interleaving in uplink.....	29
4.2.5.4 Relation between input and output of 1 st interleaving in downlink	29
4.2.6 Radio frame segmentation	29
4.2.6.1 Relation between input and output of the radio frame segmentation block in uplink	30
4.2.6.2 Relation between input and output of the radio frame segmentation block in downlink	30
4.2.7 Rate matching	30
4.2.7.1 Determination of rate matching parameters in uplink	32
4.2.7.1.1 Determination of SF and number of PhCHs needed.....	32
4.2.7.2 Determination of rate matching parameters in downlink	35
4.2.7.2.1 Determination of rate matching parameters for fixed positions of TrCHs	35
4.2.7.2.2 Determination of rate matching parameters for pseudo-flexible positions of TrCHs	37
4.2.7.2.3 Determination of rate matching parameters for flexible positions of TrCHs	37
4.2.7.3 Bit separation and collection in uplink.....	39
4.2.7.3.1 Bit separation.....	41
4.2.7.3.2 Bit collection	41
4.2.7.4 Bit separation and collection in downlink.....	42
4.2.7.4.1 Bit separation.....	43
4.2.7.4.2 Bit collection	43
4.2.7.5 Rate matching pattern determination	44
4.2.8 TrCH multiplexing.....	45
4.2.9 Insertion of discontinuous transmission (DTX) indication bits	45
4.2.9.1 1 st insertion of DTX indication bits.....	45
4.2.9.2 2 nd insertion of DTX indication bits	46

4.2.10	Physical channel segmentation	47
4.2.10.1	Relation between input and output of the physical segmentation block in uplink	47
4.2.10.2	Relation between input and output of the physical segmentation block in downlink	47
4.2.11	2^{nd} interleaving.....	47
4.2.11.1	2^{nd} interleaving for Secondary CCPCH with 16QAM	48
4.2.12	Physical channel mapping	49
4.2.12.1	Uplink	49
4.2.12.1.1	UL_DPCH_10ms_Mode is not configured by higher layers, or, no compressed-mode transmission gap overlaps with the first radio frame in the 20ms CI.....	49
4.2.12.1.2	UL_DPCH_10ms_Mode is configured by higher layers, and, a compressed-mode transmission gap overlaps with the first radio frame in 20ms CI	49
4.2.12.2	Downlink.....	50
4.2.13	Restrictions on different types of CCTrCHs.....	50
4.2.13.1	Uplink Dedicated channel (DCH)	50
4.2.13.2	Random Access Channel (RACH)	51
4.2.13.3	Void.....	51
4.2.13.4	Downlink Dedicated Channel (DCH)	51
4.2.13.5	Void.....	51
4.2.13.6	Broadcast channel (BCH)	51
4.2.13.7	Forward access and paging channels (FACH and PCH)	51
4.2.13.8	High Speed Downlink Shared Channel (HS-DSCH) associated with a DCH.....	51
4.2.13.9	Enhanced Dedicated Channel (E-DCH).....	52
4.2.14	Multiplexing of different transport channels into one CCTrCH, and mapping of one CCTrCH onto physical channels	52
4.2.14.1	Allowed CCTrCH combinations for one UE	53
4.2.14.1.1	Allowed CCTrCH combinations on the uplink	53
4.2.14.1.2	Allowed CCTrCH combinations on the downlink	53
4.3	Transport format detection	53
4.3.1	Blind transport format detection	54
4.3.1A	Single transport format detection.....	54
4.3.2	Transport format detection based on TFCI.....	54
4.3.3	Coding of Transport-Format-Combination Indicator (TFCI)	55
4.3.4	Void	56
4.3.5	Mapping of TFCI words	56
4.3.5.1	Mapping of TFCI word in normal mode in downlink, and in uplink when uplink DPCCH slot format is not 5	56
4.3.5.1A	Mapping of TFCI word in normal mode in uplink when uplink DPCCH slot format is 5	56
4.3.5.1.1	Mapping of TFCI bits for Secondary CCPCH with 16QAM	56
4.3.5.2	Mapping of TFCI word in compressed mode	57
4.3.5.2.1	Uplink compressed mode	57
4.3.5.2.2	Downlink compressed mode	57
4.3A	Mapping of DL FET ACK/NACK bits	58
4.4	Compressed mode	59
4.4.1	Frame structure in the uplink	59
4.4.2	Frame structure types in the downlink	59
4.4.2A	Frame structure in the downlink for F-DPCH	60
4.4.2B	Frame structure in the downlink for F-TPICH.....	60
4.4.3	Transmission time reduction method	60
4.4.3.1	Void.....	60
4.4.3.2	Compressed mode by reducing the spreading factor by 2	60
4.4.3.3	Compressed mode by higher layer scheduling	60
4.4.4	Transmission gap position	61
4.4.5	Transmission gap position for E-DCH	62
4.4.5.1	E-DPDCH Transmission Gap Position during Initial Transmissions.....	62
4.4.5.2	E-DPDCH Transmission Gap Position during Retransmissions	63
4.4.5.3	E-DPCCH Transmission Gap Position.....	63
4.5	Coding for HS-DSCH	63
4.5.1	CRC attachment for HS-DSCH	64
4.5.1.1	CRC attachment method 1 for HS-DSCH	64
4.5.1.2	CRC attachment method 2 for HS-DSCH.....	65
4.5.1A	Bit scrambling for HS-DSCH	65
4.5.2	Code block segmentation for HS-DSCH	65

4.5.3	Channel coding for HS-DSCH.....	66
4.5.4	Hybrid ARQ for HS-DSCH	66
4.5.4.1	HARQ bit separation.....	66
4.5.4.2	HARQ First Rate Matching Stage.....	66
4.5.4.3	HARQ Second Rate Matching Stage	67
4.5.4.4	HARQ bit collection	68
4.5.5	Physical channel segmentation for HS-DSCH.....	68
4.5.6	Interleaving for HS-DSCH	69
4.5.7	Constellation re-arrangement for 16 QAM and 64QAM	69
4.5.8	Physical channel mapping for HS-DSCH.....	70
4.6	Coding for HS-SCCH type 1	70
4.6.1	Overview	70
4.6.2	HS-SCCH information field mapping	72
4.6.2.1	Redundancy and constellation version coding	72
4.6.2.2	Modulation scheme mapping	72
4.6.2.3	Channelization code-set mapping	72
4.6.2.4	UE identity mapping	73
4.6.2.5	HARQ process identifier mapping	73
4.6.2.6	Transport block size index mapping	73
4.6.3	Multiplexing of HS-SCCH information.....	73
4.6.4	CRC attachment for HS-SCCH	73
4.6.5	Channel coding for HS-SCCH.....	74
4.6.6	Rate matching for HS-SCCH.....	74
4.6.7	UE specific masking for HS-SCCH.....	74
4.6.8	Physical channel mapping for HS-SCCH.....	74
4.6A	Coding for HS-SCCH type 2	74
4.6A.1	Overview	74
4.6A.2	HS-SCCH Type 2 information field mapping	76
4.6A.2.1	The first transmission	76
4.6A.2.2	The second and the third transmissions	76
4.6A.2.2.1	Special Information mapping.....	76
4.6A.2.2.1.1	Transport-block size information mapping	76
4.6A.2.2.1.2	Pointer to the previous transmission mapping	77
4.6A.2.2.1.3	Second or third transmission mapping	77
4.6A.2.2.2	Redundancy and Constellation Version mapping	77
4.6A.2.2.3	Modulation scheme mapping	77
4.6A.2.2.4	Channelization code-set mapping	77
4.6A.2.2.5	UE identity mapping	77
4.6A.3	Multiplexing of HS-SCCH Type 2 information.....	77
4.6A.4	CRC attachment for HS-SCCH Type 2	77
4.6A.5	Channel coding for HS-SCCH Type 2.....	77
4.6A.6	Rate matching for HS-SCCH Type 2.....	77
4.6A.7	UE specific masking for HS-SCCH Type 2.....	78
4.6A.8	Physical channel mapping for HS-SCCH Type 2	78
4.6B	Coding for HS-SCCH type 3	78
4.6B.1	Overview	78
4.6B.2	HS-SCCH type 3 information field mapping	79
4.6B.2.1	Redundancy and constellation version coding	79
4.6B.2.2	Modulation scheme and number of transport blocks mapping.....	80
4.6B.2.3	Channelization code-set mapping	80
4.6B.2.4	UE identity mapping	81
4.6B.2.5	HARQ process identifier mapping	81
4.6B.2.6	Transport block size index mapping	81
4.6B.2.7	Precoding Weight Information mapping	82
4.6B.3	Multiplexing of HS-SCCH type 3 information.....	82
4.6B.4	CRC attachment for HS-SCCH type 3.....	83
4.6B.5	Channel coding for HS-SCCH type 3.....	83
4.6B.6	Rate matching for HS-SCCH type 3	83
4.6B.7	UE specific masking for HS-SCCH type 3	83
4.6B.8	Physical channel mapping for HS-SCCH type 3	84
4.6C	Coding for HS-SCCH orders.....	84
4.6C.1	Overview	84

4.6C.2	HS-SCCH Order information field mapping in the CELL_DCH state	84
4.6C.2.1	Order type mapping.....	84
4.6C.2.2	Order mapping	84
4.6C.2.2.1	Orders for activation and deactivation of DTX, DRX and HS-SCCH-less operation and for HS-DSCH serving cell change	84
4.6C.2.2.2	Orders for activation and deactivation of Secondary serving HS-DSCH cells and Secondary uplink frequency.....	85
4.6C.2.2.3	Orders for Switching between Uplink Closed Loop Transmit Diversity Activation states	92
4.6C.2.2.4	Orders for activating and de-activating demodulation common pilots (D-CPICH) when the UE is configured in MIMO mode with four transmit antennas	93
4.6C.2.2.5	Orders for switching the E-DCH TTI, and for start/stop a simplified HS-SCCH type 1 transmission interval.....	93
4.6C.2.3	UE identity mapping	94
4.6C.3	HS-SCCH order information field mapping in the CELL_FACH and CELL_PCH states.....	94
4.6C.3.1	Order type mapping.....	94
4.6C.3.2	Order mapping	94
4.6C.3.2.1	Orders for network triggered HS-DPCCH transmission	94
4.6C.3.2.2	Orders for HS-DSCH reception during CELL_FACH HS-SCCH DRX operation.....	95
4.6D	Coding for HS-SCCH type 4.....	95
4.6D.1	Overview	95
4.6D.2	HS-SCCH type 4 information field mapping.....	96
4.6D.2.1	Redundancy and constellation version coding	96
4.6D.2.2	Modulation scheme and number of transport blocks mapping.....	96
4.6D.2.3	Channelization code-set mapping	98
4.6D.2.4	UE identity mapping	98
4.6D.2.5	HARQ process identifier mapping	98
4.6D.2.6	Transport block size index mapping	98
4.6D.2.7	Precoding Weight Information mapping.....	99
4.6D.3	Multiplexing of HS-SCCH type 4 information.....	100
4.6D.4	CRC attachment for HS-SCCH type 4.....	101
4.6D.5	Channel coding for HS-SCCH type 4.....	102
4.6D.6	Rate matching for HS-SCCH type 4	102
4.6D.7	UE specific masking for HS-SCCH type 4	102
4.6D.8	Physical channel mapping for HS-SCCH type 4	102
4.7	Coding for HS-DPCCH.....	102
4.7.1	Overview	102
4.7.2	Channel coding for HS-DPCCH when the UE is not configured in MIMO mode and not configured in MIMO mode with four transmit antennas in the serving HS-DSCH cell and Secondary_Cell_Enabled is 0 or 1 and Secondary_Cell_Active is 0	108
4.7.2.1	Channel coding for HS-DPCCH HARQ-ACK	108
4.7.2.2	Channel coding for HS-DPCCH channel quality indication	108
4.7.3	Channel coding for HS-DPCCH when the UE is configured in MIMO mode in the serving HS-DSCH cell and Secondary_Cell_Enabled is 0	109
4.7.3.1	Channel coding for HS-DPCCH HARQ-ACK	109
4.7.3.2	Channel coding for HS-DPCCH composite precoding control indication and channel quality indication.....	110
4.7.3.2.1	Bit mapping of Type A channel quality indication.....	110
4.7.3.2.2	Bit mapping of Type B channel quality indication.....	110
4.7.3.2.3	Bit mapping of precoding control indication.....	110
4.7.3.2.4	Composite precoding control indication and channel quality indication bits	110
4.7.3.2.5	Block encoding of composite precoding control indication and channel quality indication bits...111	111
4.7.3A	Channel coding for HS-DPCCH when the UE is not configured in MIMO mode in any cell and Secondary_Cell_Enabled is 1 and Secondary_Cell_Active is 1	112
4.7.3A.1	Channel coding for the composite HS-DPCCH HARQ-ACK	112
4.7.3A.2	Channel coding for HS-DPCCH composite channel quality indication.....	113
4.7.3A.2.1	Composite channel quality indication bits.....	113
4.7.3A.2.2	Block encoding of composite channel quality indication bits	114
4.7.3B	Channel coding for HS-DPCCH when Secondary_Cell_Enabled is at least 3 or when the UE is configured in MIMO mode in at least one cell and Secondary_Cell_Enabled is greater than 0	114
4.7.3B.1	Channel coding for the composite HS-DPCCH HARQ-ACK	114
4.7.3B.2	Channel coding for HS-DPCCH composite precoding control indication and channel quality indication.....	116

4.7.3C	Channel coding for HS-DPCCH when the UE is not configured in MIMO mode in any cell and Secondary_Cell_Enabled is 2	116
4.7.3C.1	Channel coding for the composite HS-DPCCH HARQ-ACK	116
4.7.3C.2	Channel coding for HS-DPCCH channel quality indication	117
4.7.3D	Channel coding for HS-DPCCH when the UE is configured in Multiflow mode	117
4.7.3D.1	Channel coding for the composite HS-DPCCH HARQ-ACK	117
4.7.3D.2	Channel coding for HS-DPCCH channel quality indication	118
4.7.3E	Channel coding for HS-DPCCH when the UE is configured in MIMO mode with 4 transmit antennas	118
4.7.3E.1	Channel coding for HS-DPCCH when the UE is configured in MIMO mode with four transmit antennas and Secondary_Cell_Enabled is 0	118
4.7.3E.1.1	Channel coding for HS-DPCCH HARQ-ACK	118
4.7.3E.1.2	Channel coding for HS-DPCCH composite number of transport blocks preferred, precoding control indication and channel quality indication	120
4.7.3E.2	Channel coding for HS-DPCCH when the UE is configured in MIMO mode with four transmit antennas in any cell and Secondary_Cell_Enabled is 1	122
4.7.3E.2.1	Channel coding for the composite HS-DPCCH HARQ-ACK	122
4.7.3E.2.2	Channel coding for HS-DPCCH composite number of transport blocks preferred, precoding control indication and channel quality indication	123
4.7.3E.3	Channel coding for HS-DPCCH when the UE is configured in MIMO mode with four transmit antennas in any cell and Secondary_Cell_Enabled is 2	123
4.7.3E.3.1	Channel coding for the composite HS-DPCCH HARQ-ACK	123
4.7.3E.3.2	Channel coding for HS-DPCCH composite number of transport blocks preferred, precoding control indication and channel quality indication	123
4.7.3E.4	Channel coding for HS-DPCCH when the UE is configured in MIMO mode with four transmit antennas in any cell and Secondary_Cell_Enabled is 3	123
4.7.3E.4.1	Channel coding for the composite HS-DPCCH HARQ-ACK	124
4.7.3E.4.2	Channel coding for HS-DPCCH composite number of transport blocks preferred, precoding control indication and channel quality indication	124
4.7.4	Physical channel mapping for HS-DPCGH	124
4.7.4.1	Physical Channel mapping for HS-DPCCH HARQ-ACK	124
4.7.4.2	Physical Channel mapping for HS-DPCCH PCI/CQI	127
4.7.4.3	Physical Channel mapping for HS-DPCCH HARQ-ACK and PCI/CQI when the UE is configured in Multiflow mode	133
4.7.4.3.1	Physical Channel mapping for HS-DPCCH HARQ-ACK and PCI/CQI when the UE is configured with one serving and one assisting serving HS-DSCH cell	133
4.7.4.3.2	Physical Channel mapping for HS-DPCCH HARQ-ACK and PCI/CQI when the UE is configured with two serving and one assisting serving or one serving and two assisting serving HS-DSCH cells	134
4.7.4.3.3	Physical Channel mapping for HS-DPCCH HARQ-ACK and PCI/CQI when the UE is configured with two serving and two assisting serving HS-DSCH cells	135
4.7.4.3.4	Physical Channel mapping for HS-DPCCH HARQ-ACK and PCI/CQI when the UE is configured with three serving and one assisting serving HS-DSCH cells or one serving and three assisting serving HS-DSCH cells	136
4.7.4.4	Physical Channel mapping for HS-DPCCH when the UE is configured in MIMO mode with four transmit antennas in at least one cell	137
4.7.4.4.1	Physical Channel mapping for HS-DPCCH HARQ-ACK	137
4.7.4.4.2	Physical Channel mapping for HS-DPCCH NTBP/PCI/CQI	138
4.8	Coding for E-DCH	141
4.8.1	CRC attachment for E-DCH	142
4.8.2	Code block segmentation for E-DCH	142
4.8.3	Channel coding for E-DCH	143
4.8.4	Physical layer HARQ functionality and rate matching for E-DCH	143
4.8.4.1	Determination of SF, modulation scheme and number of E-DPDCH PhCHs needed	143
4.8.4.1A	Determination of SF, modulation scheme and number of S-E-DPDCH PhCHs needed	145
4.8.4.2	HARQ bit separation	146
4.8.4.3	HARQ Rate Matching Stage	146
4.8.4.4	HARQ bit collection	146
4.8.5	Physical channel segmentation for E-DCH	147
4.8.6	Interleaving for E-DCH	147
4.8.7	Physical channel mapping for E-DCH	148
4.9	Coding for E-DPCCH	148

4.9.1	Overview	148
4.9.2	E-DPCCH information field mapping	149
4.9.2.1	Information field mapping of E-TFCI.....	149
4.9.2.2	Information field mapping of retransmission sequence number	149
4.9.2.3	Information field mapping of the "Happy" bit	149
4.9.3	Multiplexing of E-DPCCH information	149
4.9.4	Channel coding for E-DPCCH.....	150
4.9.5	Physical channel mapping for E-DPCCH.....	150
4.9A	Coding for S-E-DPCCH	150
4.9A.1	Overview	150
4.9A.2	S-E-DPCCH information field mapping.....	150
4.9A.2.1	Information field mapping of E-TFCI.....	150
4.9A.2.2	Information field mapping of retransmission sequence number	150
4.9A.2.3	Information field mapping of the Spare bit	150
4.9A.3	Multiplexing of S-E-DPCCH information	150
4.9A.4	Channel coding for S-E-DPCCH.....	150
4.9A.5	Physical channel mapping for S-E-DPCCH	151
4.10	Coding for E-AGCH	151
4.10.1	Overview	151
4.10.1A	E-AGCH information field mapping	152
4.10.1A.1	Information field mapping of the Absolute Grant Value	152
4.10.1A.2	Information field mapping of the Absolute Grant Scope	154
4.10.1B	Multiplexing of E-AGCH information	154
4.10.2	CRC attachment for E-AGCH	155
4.10.3	Channel coding for E-AGCH.....	155
4.10.4	Rate matching for E-AGCH.....	155
4.10.5	Physical channel mapping for E-AGCH	155
4.10A	Coding for E-ROCH.....	155
4.10A.1	Overview	155
4.10A.2	E-ROCH information field mapping.....	156
4.10A.2.1	Information field mapping of the S-ETFC Offset.....	156
4.10A.2.2	Information field mapping of the Rank Indication.....	157
4.10A.3	Multiplexing of E-ROCH information.....	157
4.10A.4	CRC attachment for E-ROCH	157
4.10A.5	Channel coding for E-ROCH.....	158
4.10A.6	Rate matching for E-ROCH.....	158
4.10A.7	Physical channel mapping for E-ROCH	158
4.11	Mapping for E-RGCH Relative Grant.....	158
4.11.1	Overview	158
4.11.2	Relative Grant mapping.....	158
4.12	Mapping for E-HICH ACK/NACK.....	158
4.12.1	Overview	158
4.12.2	ACK/NACK mapping.....	158
Annex A (informative):	Blind transport format detection.....	160
A.1	Blind transport format detection using fixed positions	160
A.1.1	Blind transport format detection using received power ratio.....	160
A.1.2	Blind transport format detection using CRC	160
A.2	Blind transport format detection using pseudo-flexible positions.....	162
Annex B (informative):	Compressed mode idle lengths.....	163
B.1	Idle lengths for DL, UL and DL+UL compressed mode for DPCH	163
Annex C (informative):	Change history	165
	History	171

Foreword

This Technical Specification (TS) has been produced by the 3rd Generation Partnership Project (3GPP).

The contents of the present document are subject to continuing work within the TSG and may change following formal TSG approval. Should the TSG modify the contents of the present document, it will be re-released by the TSG with an identifying change of release date and an increase in version number as follows:

Version x.y.z

where:

- x the first digit:
 - 1 presented to TSG for information;
 - 2 presented to TSG for approval;
 - 3 or greater indicates TSG approved document under change control.
- y the second digit is incremented for all changes of substance, i.e. technical enhancements, corrections, updates, etc.
- z the third digit is incremented when editorial only changes have been incorporated in the document.

iTeh STANDARD PREVIEW (standards.iteh.ai)

[ETSI TS 125 212 V17.0.0 \(2022-05\)](#)

<https://standards.iteh.ai/catalog/standards/sist/45ee5fd3-019f-4271-8325-70bbddac8321/etsi-ts-125-212-v17-0-0-2022-05>

1 Scope

The present document describes the characteristics of the Layer 1 multiplexing and channel coding in the FDD mode of UTRA.

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
- For a specific reference, subsequent revisions do not apply.
- For a non-specific reference, the latest version applies.

- [1] 3GPP TS 25.201: "Physical layer - General Description".
- [2] 3GPP TS 25.211: "Physical channels and mapping of transport channels onto physical channels (FDD)".
- [3] 3GPP TS 25.213: "Spreading and modulation (FDD)".
- [4] 3GPP TS 25.214: "Physical layer procedures (FDD)".
- [5] 3GPP TS 25.215: "Physical layer - Measurements (FDD)".
- [6] 3GPP TS 25.221: "Physical channels and mapping of transport channels onto physical channels (TDD)".
- [7] 3GPP TS 25.222: "Multiplexing and channel coding (TDD)".
- [8] 3GPP TS 25.223: "Spreading and modulation (TDD)".
- [9] 3GPP TS 25.224: "Physical layer procedures (TDD)".
- [10] 3GPP TS 25.225: "Physical layer – Measurements (TDD)".
- [11] 3GPP TS 25.302: "Services Provided by the Physical Layer".
- [12] 3GPP TS 25.402: "Synchronization in UTRAN, Stage 2".
- [13] 3GPP TS 25.331: "Radio Resource Control (RRC); Protocol Specification".
- [14] ITU-T Recommendation X.691 (12/97) "Information technology - ASN.1 encoding rules: Specification of Packed Encoding Rules (PER)"
- [15] 3GPP TS 25.306: "UE Radio Access capabilities".
- [16] 3GPP TS 25.321: "Medium Access Control (MAC) protocol specification".

3 Definitions, symbols and abbreviations

3.1 Definitions

For the purposes of the present document, the following terms and definitions apply:

Assisting secondary serving HS-DSCH Cell(s): In addition to the serving HS-DSCH cell, a cell in the secondary downlink frequency, where the UE is configured to simultaneously monitor a HS-SCCH set and receive HS-DSCH if it is scheduled in that cell. There can be up to two assisting secondary serving HS-DSCH cells.

Assisting serving HS-DSCH Cell: In addition to the serving HS-DSCH cell, a cell in the same frequency, where the UE is configured to simultaneously monitor a HS-SCCH set and receive HS-DSCH if it is scheduled in that cell.

Cell group: A group of (one, two, or three) Multiflow mode cells that have the same CPICH timing. The cells that belong to a cell group are indicated by higher layers.

DL FET: DL FET refers to early termination of DL DPCH transmission upon receiving an acknowledgement message. In this context, a DL FET ACK/NACK message represents an acknowledge message sent on UL DPCCH for DL FET.

DL_DCH_FET_Config: Higher layers signal this configuration parameter to indicate enhanced DCH physical layer configuration. The possible values are 0 and 1. The value 0 indicates Mode 0 configuration where DL transport channels concatenation and DL FET ACK/NACK signalling on UL are not configured. The value 1 indicates Mode 1 where DL transport channel concatenation and DL FET ACK/NACK signalling on UL are configured.

MIMO mode: This term refers to the downlink MIMO configuration with two transmit antennas

MIMO mode with four transmit antennas: This term refers to the downlink MIMO configuration with four transmit antennas

Multiflow mode: The UE is configured in Multiflow mode when it is configured with assisting serving HS-DSCH cell.

Primary uplink frequency: If a single uplink frequency is configured for the UE, then it is the primary uplink frequency. In case more than one uplink frequency is configured for the UE, then the primary uplink frequency is the frequency on which the E-DCH corresponding to the serving E-DCH cell associated with the serving HS-DSCH cell is transmitted. The association between a pair of uplink and downlink frequencies is indicated by higher layers.

Secondary uplink frequency: A secondary uplink frequency is a frequency on which an E-DCH corresponding to a serving E-DCH cell associated with a secondary serving HS-DSCH cell is transmitted. The association between a pair of uplink and downlink frequencies is indicated by higher layers.

TG: Transmission Gap is consecutive empty slots that have been obtained with a transmission time reduction method. The transmission gap can be contained in one or two consecutive radio frames.

<https://standards.iteh.ai/catalog/standards/sist/45ee5fd3-0222-05>

TGL: Transmission Gap Length is the number of consecutive empty slots that have been obtained with a transmission time reduction method. $0 \leq TGL \leq 14$. The CFNs of the radio frames containing the first empty slot of the transmission gaps, the CFNs of the radio frames containing the last empty slot, the respective positions N_{first} and N_{last} within these frames of the first and last empty slots of the transmission gaps, and the transmission gap lengths can be calculated with the compressed mode parameters described in [5].

TrCH number: The transport channel number identifies a TrCH in the context of L1. The L3 transport channel identity (TrCH ID) maps onto the L1 transport channel number. The mapping between the transport channel number and the TrCH ID is as follows: TrCH 1 corresponds to the TrCH with the lowest TrCH ID, TrCH 2 corresponds to the TrCH with the next lowest TrCH ID and so on.

UL DPCH 10ms Mode: When configured by higher layers for the TTI to be transmitted [16], UL DPCH follows physical channel procedures specific to this mode. UL DPCH 10ms Mode can only happen when DL_DCH_FET_Config is configured.

UL 20ms Compression Interval (CI): A time interval of 20ms duration aligned to a 20ms TTI defined for UL DPCH physical layer procedures when DL_DCH_FET_Config is configured by higher layers.

1st secondary serving HS-DSCH cell: If the UE is configured with two uplink frequencies, the 1st secondary serving HS-DSCH cell is the secondary serving HS-DSCH cell that is associated with the secondary uplink frequency. If the UE is configured with a single uplink frequency, the 1st secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

2nd secondary serving HS-DSCH cell: If the UE is configured with more than two serving HS-DSCH cells, the 2nd secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

3rd secondary serving HS-DSCH cell: If the UE is configured with more than three serving HS-DSCH cells, the 3rd secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

4th secondary serving HS-DSCH cell: If the UE is configured with more than four serving HS-DSCH cells, the 4th secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

5th secondary serving HS-DSCH cell: If the UE is configured with more than five serving HS-DSCH cells, the 5th secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

6th secondary serving HS-DSCH cell: If the UE is configured with more than six serving HS-DSCH cells, the 6th secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

7th secondary serving HS-DSCH cell: If the UE is configured with eight serving HS-DSCH cells, the 7th secondary serving HS-DSCH cell is a secondary serving HS-DSCH cell whose index is indicated by higher layers.

3.2 Symbols

For the purposes of the present document, the following symbols apply:

$\lceil x \rceil$	round towards ∞ , i.e. integer such that $x \leq \lceil x \rceil < x+1$
$\lfloor x \rfloor$	round towards $-\infty$, i.e. integer such that $x-1 < \lfloor x \rfloor \leq x$
$ x $	absolute value of x
$\text{sgn}(x)$	signum function, i.e. $\text{sgn}(x) = \begin{cases} 1; & x \geq 0 \\ -1; & x < 0 \end{cases}$
N_{first}	The first slot in the TG, located in the first compressed radio frame if the TG spans two frames.
N_{last}	The last slot in the TG, located in the second compressed radio frame if the TG spans two frames.
N_{tr}	Number of transmitted slots in a radio frame.

Unless otherwise is explicitly stated when the symbol is used, the meaning of the following symbols is:

A	List of transport channels numbers i_1, i_2, \dots, i_B , corresponding to downlink transport channels to be concatenated when UL_DCH_FET_Config = 1.
i	TrCH number
j	TFC number
k	Bit number
l	TF number
m	Transport block number
n_i	Radio frame number of TrCH i .
p	PhCH number
r	Code block number
I	Number of TrCHs in a CCTrCH.
C_i	Number of code blocks in one TTI of TrCH i .
F_i	Number of radio frames in one TTI of TrCH i , except for uplink when UL_DPCH_10ms_Mode is configured. In uplink, when UL_DPCH_10ms_Mode is configured, F_i is the number of radio frames in the transmission time interval of TrCH i divided by 2.
M_i	Number of transport blocks in one TTI of TrCH i .
$N_{\text{data},j}$	Number of data bits that are available for the CCTrCH in a radio frame with TFC j .
$N_{\text{data},j}^{\text{cm}}$	Number of data bits that are available for the CCTrCH in a compressed radio frame with TFC j .
P	Number of PhCHs used for one CCTrCH.
PL	Puncturing Limit for the uplink. Signalled from higher layers
RM_i	Rate Matching attribute for TrCH i . Signalled from higher layers, or determined as specified in clause 4.2.0.

Temporary variables, i.e. variables used in several (sub)clauses with different meaning.

x, X
y, Y
z, Z

3.3 Abbreviations

For the purposes of the present document, the following abbreviations apply:

ARQ	Automatic Repeat Request
BCH	Broadcast Channel
BER	Bit Error Rate
BLER	Block Error Rate
BS	Base Station
CCPCH	Common Control Physical Channel
CCTrCH	Coded Composite Transport Channel
CFN	Connection Frame Number
CLTD	Closed Loop Transmit Diversity
CRC	Cyclic Redundancy Check
DCH	Dedicated Channel
DL	Downlink (Forward link)
DPCCH	Dedicated Physical Control Channel
DPCCH2	Dedicated Physical Control Channel 2
DPCH	Dedicated Physical Channel
DPDCH	Dedicated Physical Data Channel
DS-CDMA	Direct-Sequence Code Division Multiple Access
DTX	Discontinuous Transmission
FACH	Forward Access Channel
E-AGCH	E-DCH Absolute Grant Channel
E-DCH	Enhanced Dedicated Channel
E-DPCCH	E-DCH Dedicated Physical Control Channel
E-DPDCH	E-DCH Dedicated Physical Data Channel
E-HICH	E-DCH Hybrid ARQ Indicator Channel
E-RGCH	E-DCH Relative Grant Channel
E-RNTI	E-DCH Radio Network Temporary Identifier
E-ROCH	E-DCH Rank and Offset Channel
FDD	Frequency Division Duplex
F-DPCH	Fractional Dedicated Physical Channel
F-TPICH	Fractional Transmitted Precoding Indicator Channel
FER	Frame Error Rate
GF	Galois Field
HARQ	Hybrid Automatic Repeat reQuest
HS-DPCCH	Dedicated Physical Control Channel (uplink) for HS-DSCH
HS-DPCCH ₂	Secondary Dedicated Physical Control Channel (uplink) for HS-DSCH, when Secondary_Cell_Enabled is greater than 3
HS-DSCH	High Speed Downlink Shared Channel
HS-PDSCH	High Speed Physical Downlink Shared Channel
HS-SCCH	Shared Control Channel for HS-DSCH
MAC	Medium Access Control
MBSFN	MBMS over a Single Frequency Network
Mcps	Mega Chip Per Second
MIMO	Multiple Input Multiple Output
MS	Mobile Station
OVSF	Orthogonal Variable Spreading Factor (codes)
PCCC	Parallel Concatenated Convolutional Code
PCH	Paging Channel
PhCH	Physical Channel
PRACH	Physical Random Access Channel
RACH	Random Access Channel
RSC	Recursive Systematic Convolutional Coder
RV	Redundancy Version
RX	Receive
SCH	Synchronization Channel
SF	Spreading Factor
SFN	System Frame Number
SIR	Signal-to-Interference Ratio
SNR	Signal to Noise Ratio

S-DPCCH	Secondary Dedicated Physical Control Channel
S-E-DPCCH	Secondary Dedicated Physical Control Channel for E-DCH
S-E-DPDCH	Secondary Dedicated Physical Data Channel for E-DCH
S-E-RNTI	Secondary E-RNTI
TF	Transport Format
TFC	Transport Format Combination
TFCI	Transport Format Combination Indicator
TPC	Transmit Power Control
TPI	Transmitted Precoding Indicator
TrCH	Transport Channel
TTI	Transmission Time Interval
TX	Transmit
UL	Uplink (Reverse link)

4 Multiplexing, channel coding and interleaving

4.1 General

Data stream from/to MAC and higher layers (Transport block / Transport block set) is encoded/decoded to offer transport services over the radio transmission link. Channel coding scheme is a combination of error detection, error correcting, rate matching, interleaving and transport channels mapping onto/splitting from physical channels.

4.2 General coding/multiplexing of TrCHs

This section only applies to the transport channels: DCH, RACH, BCH, FACH and PCH. Other transport channels which do not use the general method are described separately below.

**iTeh STANDARD
PREVIEW
(standards.iteh.ai)**

Data arrives to the coding/multiplexing unit in form of transport block sets once every transmission time interval. The transmission time interval is transport-channel specific from the set {10 ms, 20 ms, 40 ms, 80 ms}, where 80 ms TTI for DCH shall not be used unless SF=512. [ETSI TS 125 212 V17.0.0 \(2022-05\)](#)

The following coding/multiplexing steps can be identified:

- add CRC to each transport block (see subclause 4.2.1);
- transport block concatenation and code block segmentation (see subclause 4.2.2);
- channel coding (see subclause 4.2.3);
- radio frame equalisation (see subclause 4.2.4);
- rate matching (see subclause 4.2.7);
- insertion of discontinuous transmission (DTX) indication bits (see subclause 4.2.9);
- interleaving (two steps, see subclauses 4.2.5 and 4.2.11);
- radio frame segmentation (see subclause 4.2.6);
- multiplexing of transport channels (see subclause 4.2.8);
- physical channel segmentation (see subclause 4.2.10);
- mapping to physical channels (see subclause 4.2.12).

The coding/multiplexing steps for uplink and downlink are shown in figure 1 and figure 2 respectively.