

ETSI TS 136 306 V17.3.0 (2023-01)



**LTE;
Evolved Universal Terrestrial Radio Access (E-UTRA);
User Equipment (UE) radio access capabilities
(3GPP TS 36.306 version 17.3.0 Release 17)**

[ETSI TS 136 306 V17.3.0 \(2023-01\)](https://standards.iteh.ai/catalog/standards/sist/517b2e64-78b4-472e-b90c-bb8a5b7d2409/etsi-ts-136-306-v17-3-0-2023-01)

<https://standards.iteh.ai/catalog/standards/sist/517b2e64-78b4-472e-b90c-bb8a5b7d2409/etsi-ts-136-306-v17-3-0-2023-01>



ReferenceRTS/TSGR-0236306vh30

KeywordsLTE

ETSI

650 Route des Lucioles
F-06921 Sophia Antipolis Cedex - FRANCE

Tel.: +33 4 92 94 42 00 Fax: +33 4 93 65 47 16

Siret N° 348 623 562 00017 - APE 7112B
Association à but non lucratif enregistrée à la
Sous-Préfecture de Grasse (06) N° w061004871

Important notice

The present document can be downloaded from:

<http://www.etsi.org/standards-search>

The present document may be made available in electronic versions and/or in print. The content of any electronic and/or print versions of the present document shall not be modified without the prior written authorization of ETSI. In case of any existing or perceived difference in contents between such versions and/or in print, the prevailing version of an ETSI deliverable is the one made publicly available in PDF format at www.etsi.org/deliver.

Users of the present document should be aware that the document may be subject to revision or change of status.

Information on the current status of this and other ETSI documents is available at

<https://portal.etsi.org/TB/ETSIDeliverableStatus.aspx>

If you find errors in the present document, please send your comment to one of the following services:

<https://portal.etsi.org/People/CommitteeSupportStaff.aspx>

If you find a security vulnerability in the present document, please report it through our

Coordinated Vulnerability Disclosure Program:

<https://www.etsi.org/standards/coordinated-vulnerability-disclosure>

Notice of disclaimer & limitation of liability

The information provided in the present deliverable is directed solely to professionals who have the appropriate degree of experience to understand and interpret its content in accordance with generally accepted engineering or other professional standard and applicable regulations.

No recommendation as to products and services or vendors is made or should be implied.

No representation or warranty is made that this deliverable is technically accurate or sufficient or conforms to any law and/or governmental rule and/or regulation and further, no representation or warranty is made of merchantability or fitness for any particular purpose or against infringement of intellectual property rights.

In no event shall ETSI be held liable for loss of profits or any other incidental or consequential damages.

Any software contained in this deliverable is provided "AS IS" with no warranties, express or implied, including but not limited to, the warranties of merchantability, fitness for a particular purpose and non-infringement of intellectual property rights and ETSI shall not be held liable in any event for any damages whatsoever (including, without limitation, damages for loss of profits, business interruption, loss of information, or any other pecuniary loss) arising out of or related to the use of or inability to use the software.

Copyright Notification

No part may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm except as authorized by written permission of ETSI.

The content of the PDF version shall not be modified without the written authorization of ETSI.

The copyright and the foregoing restriction extend to reproduction in all media.

© ETSI 2023.
All rights reserved.

Intellectual Property Rights

Essential patents

IPRs essential or potentially essential to normative deliverables may have been declared to ETSI. The declarations pertaining to these essential IPRs, if any, are publicly available for **ETSI members and non-members**, and can be found in ETSI SR 000 314: *"Intellectual Property Rights (IPRs); Essential, or potentially Essential, IPRs notified to ETSI in respect of ETSI standards"*, which is available from the ETSI Secretariat. Latest updates are available on the ETSI Web server (<https://ipr.etsi.org/>).

Pursuant to the ETSI Directives including the ETSI IPR Policy, no investigation regarding the essentiality of IPRs, including IPR searches, has been carried out by ETSI. No guarantee can be given as to the existence of other IPRs not referenced in ETSI SR 000 314 (or the updates on the ETSI Web server) which are, or may be, or may become, essential to the present document.

Trademarks

The present document may include trademarks and/or tradenames which are asserted and/or registered by their owners. ETSI claims no ownership of these except for any which are indicated as being the property of ETSI, and conveys no right to use or reproduce any trademark and/or tradename. Mention of those trademarks in the present document does not constitute an endorsement by ETSI of products, services or organizations associated with those trademarks.

DECT™, **PLUGTESTS™**, **UMTS™** and the ETSI logo are trademarks of ETSI registered for the benefit of its Members. **3GPP™** and **LTE™** are trademarks of ETSI registered for the benefit of its Members and of the 3GPP Organizational Partners. **oneM2M™** logo is a trademark of ETSI registered for the benefit of its Members and of the oneM2M Partners. **GSM®** and the GSM logo are trademarks registered and owned by the GSM Association.

Legal Notice

This Technical Specification (TS) has been produced by ETSI 3rd Generation Partnership Project (3GPP).

The present document may refer to technical specifications or reports using their 3GPP identities. These shall be interpreted as being references to the corresponding ETSI deliverables.

The cross reference between 3GPP and ETSI identities can be found under <http://webapp.etsi.org/key/queryform.asp>.

Modal verbs terminology

In the present document **"shall"**, **"shall not"**, **"should"**, **"should not"**, **"may"**, **"need not"**, **"will"**, **"will not"**, **"can"** and **"cannot"** are to be interpreted as described in clause 3.2 of the [ETSI Drafting Rules](#) (Verbal forms for the expression of provisions).

"must" and **"must not"** are **NOT** allowed in ETSI deliverables except when used in direct citation.

Contents

Intellectual Property Rights	2
Legal Notice	2
Modal verbs terminology.....	2
Foreword.....	19
1 Scope	20
2 References	20
3 Definitions, symbols and abbreviations	22
3.1 Definitions	22
3.2 Symbols.....	22
3.3 Abbreviations	22
4 UE radio access capability parameters.....	24
4.1 <i>ue-Category</i>	27
4.1A <i>ue-CategoryDL</i> and <i>ue-CategoryUL</i>	30
4.1B <i>ue-CategorySL-C-RX</i> , <i>ue-CategorySL-C-TX</i> and <i>ue-CategorySL-D</i>	51
4.1C <i>ue-Category-NB</i>	52
4.2 Parameters set by the field <i>ue-Category</i> and <i>ue-CategoryDL</i> / <i>ue-CategoryUL</i>	53
4.2.1 Transport channel parameters in downlink.....	53
4.2.1.1 Maximum number of DL-SCH transport block bits received within a TTI	53
4.2.1.2 Maximum number of bits of a DL-SCH transport block received within a TTI	54
4.2.1.3 Total number of DL-SCH soft channel bits	54
4.2.1.4 Maximum number of bits of a MCH transport block received within a TTI	54
4.2.2 Transport channel parameters in uplink.....	54
4.2.2.1 Maximum number of bits of an UL-SCH transport block transmitted within a TTI.....	54
4.2.2.2 Maximum number of UL-SCH transport block bits transmitted within a TTI.....	54
4.2.3 Physical channel parameters in downlink (DL).....	54
4.2.3.1 Maximum number of supported layers for spatial multiplexing in DL	54
4.2.4 Physical channel parameters in uplink (UL).....	54
4.2.4.1 Support for 64QAM in UL.....	54
4.2.5 Total layer 2 buffer size	54
4.2.6 Half-duplex FDD operation type	55
4.2.7 RF parameters	55
4.2.7.1 Maximum UE channel bandwidth.....	55
4.2A Parameters set by <i>ue-CategorySL-C</i> / <i>ue-CategorySL-D</i>	55
4.2A.1 Transport channel parameters in sidelink (SL)	55
4.2A.1.1 Maximum number of SL-SCH transport block bits received within a TTI.....	55
4.2A.1.2 Maximum number of bits of a SL-SCH transport block received within a TTI.....	55
4.2A.1.3 Maximum number of SL-DCH transport block bits received within a TTI	55
4.2A.1.4 Maximum number of bits of a SL-DCH transport block received within a TTI	55
4.2A.1.5 Maximum number of bits of a SL-SCH transport block transmitted within a TTI	55
4.2A.1.6 Maximum number of SL-SCH transport block bits transmitted within a TTI	55
4.2A.1.7 Maximum number of bits of a SL-DCH transport block transmitted within a TTI.....	55
4.2A.1.8 Maximum number of SL-DCH transport block bits transmitted within a TTI.....	56
4.2A.2 Physical channel parameters in sidelink (SL)	56
4.2A.2.1 Maximum number of supported layers for spatial multiplexing in SL-C	56
4.2A.2.2 Maximum number of supported layers for spatial multiplexing in SL-D	56
4.3 Parameters independent of the field <i>ue-Category</i> and <i>ue-CategoryDL</i> / <i>ue-CategoryUL</i>	56
4.3.1 PDCP Parameters.....	56
4.3.1.1 <i>supportedROHC-Profiles</i>	56
4.3.1.1A <i>supportedROHC-Profiles-r13</i>	56
4.3.1.2 <i>maxNumberROHC-ContextSessions</i>	57
4.3.1.2A <i>maxNumberROHC-ContextSessions-r13</i>	57
4.3.1.3 <i>pdcp-SN-Extension</i>	57
4.3.1.4 <i>supportRohcContextContinue</i>	57
4.3.1.5 <i>pdcp-SN-Extension-18bits-r13</i>	57

4.3.1.6	<i>supportedUplinkOnlyROHC-Profiles</i>	57
4.3.1.7	<i>supportedUDC-r15</i>	57
4.3.1.8	<i>supportedStandardDic-r15</i>	57
4.3.1.9	<i>supportedOperatorDic-r15</i>	58
4.3.1.10	<i>pdcp-Duplication-r15</i>	58
4.3.1.11	<i>pdcp-VersionChangeWithoutHO-r16</i>	58
4.3.1.12	<i>ehc-r16</i>	58
4.3.1.13	<i>maxNumberEHC-Contexts-r16</i>	58
4.3.1.14	<i>continueEHC-Context-r16</i>	58
4.3.1.15	<i>jointEHC-ROHC-Config-r16</i>	58
4.3.1A	NR PDCP Parameters	58
4.3.2	RLC parameters	59
4.3.2.1	<i>Void</i>	59
4.3.2.2	<i>extended-RLC-LI-Field-r12</i>	59
4.3.2.3	<i>extendedRLC-SN-SO-Field-r13</i>	59
4.3.2.4	<i>extendedPollByte-r14</i>	59
4.3.2.5	<i>rlc-UM-r15</i>	59
4.3.2.6	<i>rlc-AM-Ooo-Delivery-r15</i>	59
4.3.2.7	<i>rlc-UM-Ooo-Delivery-r15</i>	59
4.3.2.8	<i>flexibleUM-AM-Combinations-r15</i>	59
4.3.3	<i>Void</i>	59
4.3.4	Physical layer parameters	59
4.3.4.1	<i>ue-TxAntennaSelectionSupported</i>	59
4.3.4.2	<i>ue-SpecificRefSigsSupported</i>	59
4.3.4.3	<i>Void</i>	59
4.3.4.4	<i>enhancedDualLayerFDD</i>	59
4.3.4.5	<i>enhancedDualLayerTDD</i>	60
4.3.4.6	<i>supportedMIMO-CapabilityUL-r10</i>	60
4.3.4.7	<i>supportedMIMO-CapabilityDL-r10</i>	60
4.3.4.8	<i>two-AntennaPortsForPUCCH-r10</i>	60
4.3.4.9	<i>tm9-With-8Tx-FDD-r10</i>	60
4.3.4.10	<i>pmi-Disabling-r10</i>	60
4.3.4.11	<i>crossCarrierScheduling-r10</i>	60
4.3.4.12	<i>simultaneousPUCCH-PUSCH-r10</i>	60
4.3.4.13	<i>multiClusterPUSCH-WithinCC-r10</i>	61
4.3.4.14	<i>nonContiguousUL-RA-WithinCC-Info-r10</i>	61
4.3.4.15	<i>crs-InterfHandl-r11</i>	61
4.3.4.16	<i>Void</i>	61
4.3.4.17	<i>Void</i>	61
4.3.4.18	<i>ePDCCH-r11</i>	61
4.3.4.19	<i>multiACK-CSI-Reporting-r11</i>	61
4.3.4.20	<i>ss-CCH-InterfHandl-r11</i>	61
4.3.4.21	<i>tdd-SpecialSubframe-r11</i>	61
4.3.4.21A	<i>tdd-SpecialSubframe-r14</i>	61
4.3.4.21B	<i>ssp10-TDD-Only-r14</i>	61
4.3.4.22	<i>txDiv-PUCCH1b-ChSelect-r11</i>	62
4.3.4.23	<i>ul-CoMP-r11</i>	62
4.3.4.24	<i>tm5-FDD</i>	62
4.3.4.25	<i>tm5-TDD</i>	62
4.3.4.26	<i>interBandTDD-CA-WithDifferentConfig-r11</i>	62
4.3.4.27	<i>e-HARQ-Pattern-FDD-r12</i>	62
4.3.4.28	<i>tdd-FDD-CA-PCellDuplex-r12</i>	62
4.3.4.29	<i>csi-SubframeSet-r12</i>	62
4.3.4.30	<i>phy-TDD-ReConfig-FDD-PCell-r12</i>	62
4.3.4.31	<i>phy-TDD-ReConfig-TDD-PCell-r12</i>	62
4.3.4.32	<i>pusch-SRS-PowerControl-SubframeSet-r12</i>	63
4.3.4.33	<i>enhanced-4TxCodebook-r12</i>	63
4.3.4.34	<i>pusch-FeedbackMode-r12</i>	63
4.3.4.35	<i>naics-Capability-List-r12</i>	63
4.3.4.36	<i>noResourceRestrictionForTTIBundling-r12</i>	63
4.3.4.37	<i>Void</i>	63
4.3.4.38	<i>discoverySignalsInDeactSCell-r12</i>	63

4.3.4.39	<i>ul-64QAM-r12</i>	63
4.3.4.40	<i>supportedMIMO-CapabilityDL-r12</i>	63
4.3.4.41	<i>alternativeTBS-Indices-r12</i>	63
4.3.4.42	<i>codebook-HARQ-ACK-r13</i>	63
4.3.4.43	<i>fdd-HARQ-TimingTDD-r13</i>	64
4.3.4.44	<i>maxNumberUpdatedCSI-Proc-r13</i>	64
4.3.4.45	<i>pucch-Format4-r13</i>	64
4.3.4.46	<i>pucch-Format5-r13</i>	64
4.3.4.47	<i>pucch-SCell-r13</i>	64
4.3.4.48	<i>supportedBlindDecoding-r13</i>	64
4.3.4.48.1	<i>maxNumberDecoding-r13</i>	64
4.3.4.48.2	<i>pdccch-CandidateReductions-r13</i>	64
4.3.4.48.3	<i>skipMonitoringDCI-Format0-1A-r13</i>	64
4.3.4.49	<i>crs-InterfMitigationTM10-r13</i>	64
4.3.4.49a	<i>crs-InterfMitigationTM1toTM9-r13</i>	65
4.3.4.50	<i>pdsch-CollisionHandling-r13</i>	65
4.3.4.51	<i>aperiodicCSI-Reporting-r13</i>	65
4.3.4.52	<i>crossCarrierScheduling-B5C-r13</i>	65
4.3.4.53	<i>spatialBundling-HARQ-ACK-r13</i>	65
4.3.4.54	<i>uci-PUSCH-Ext-r13</i>	65
4.3.4.55	<i>multiTone-r13</i>	65
4.3.4.56	<i>multiCarrier-r13</i>	66
4.3.4.57	<i>cch-InterfMitigation-RefRecTypeA-r13</i>	66
4.3.4.58	<i>cch-InterfMitigation-RefRecTypeB-r13</i>	66
4.3.4.59	<i>cch-InterfMitigation-MaxNumCCs-r13</i>	66
4.3.4.60	<i>tdd-TTI-Bundling-r14</i>	66
4.3.4.61	<i>dmrs-LessUpPTS-r14</i>	66
4.3.4.62	<i>twoHARQ-Processes-r14</i>	66
4.3.4.63	<i>ce-PUSCH-NB-MaxTBS-r14</i>	66
4.3.4.64	<i>ce-PDSCH-PUSCH-MaxBandwidth-r14</i>	66
4.3.4.65	<i>ce-HARQ-AckBundling-r14</i>	67
4.3.4.66	<i>ce-PDSCH-TenProcesses-r14</i>	67
4.3.4.67	<i>ce-RetuningSymbols-r14</i>	67
4.3.4.68	<i>ce-PDSCH-PUSCH-Enhancement-r14</i>	67
4.3.4.69	<i>ce-SchedulingEnhancement-r14</i>	67
4.3.4.70	<i>ce-SRS-Enhancement-r14</i>	67
4.3.4.70A	<i>ce-SRS-EnhancementWithoutComb4-r14</i>	67
4.3.4.71	<i>ce-PUCCH-Enhancement-r14</i>	67
4.3.4.72	<i>ce-ClosedLoopTxAntennaSelection-r14</i>	67
4.3.4.73	<i>ul-256QAM-r14</i>	68
4.3.4.73A	<i>ul-256QAM-r15</i>	68
4.3.4.74	<i>alternativeTBS-Index-r14</i>	68
4.3.4.75	<i>multiCarrier-NPRACH-r14</i>	68
4.3.4.76	<i>multiCarrierPaging-r14</i>	68
4.3.4.77	<i>ul-256QAM-perCC-InfoList-r14</i>	68
4.3.4.78	<i>unicast-fembmsMixedSCell-r14</i>	68
4.3.4.79	<i>emptyUnicastRegion-r14</i>	68
4.3.4.80	<i>interferenceRandomisation-r14</i>	68
4.3.4.81	<i>must-CapabilityPerBand-r14</i>	68
4.3.4.81.1	<i>must-TM234-UpTo2Tx-r14</i>	68
4.3.4.81.2	<i>must-TM89-UpToOneInterferingLayer-r14</i>	69
4.3.4.81.3	<i>must-TM10-UpToOneInterferingLayer-r14</i>	69
4.3.4.81.4	<i>must-TM89-UpToThreeInterferingLayers-r14</i>	69
4.3.4.81.5	<i>must-TM10-UpToThreeInterferingLayers-r14</i>	69
4.3.4.82	<i>crs-LessDwPTS-r14</i>	69
4.3.4.83	<i>dl-1024QAM-Slot-r15</i>	69
4.3.4.84	<i>dl-1024QAM-SubslotTA-1-r15</i>	69
4.3.4.85	<i>dl-1024QAM-SubslotTA-2-r15</i>	69
4.3.4.86	<i>dmrs-PositionPattern-r15</i>	69
4.3.4.87	<i>dmrs-RepetitionSubslotPDSCH-r15</i>	69
4.3.4.88	<i>dmrs-SharingSubslotPDSCH-r15</i>	69
4.3.4.89	<i>epdcch-SPT-differentCells-r15</i>	69

4.3.4.90	<i>epdcch-STTI-differentCells-r15</i>	69
4.3.4.91	<i>maxLayersSlotOrSubslotPUSCH-r15</i>	70
4.3.4.92	<i>maxNumberUpdatedCSI-Proc-SPT-r15</i>	70
4.3.4.93	<i>Void</i>	70
4.3.4.94	<i>numberOfBlindDecodesUSS-r15</i>	70
4.3.4.95	<i>pdsch-SlotSubslotPDSCH-Decoding-r15</i>	70
4.3.4.96	<i>simultaneousTx-differentTx-duration-r15</i>	70
4.3.4.97	<i>slotPDSCH-TxDiv-TM8-r15</i>	70
4.3.4.98	<i>slotPDSCH-TxDiv-TM9and10-r15</i>	70
4.3.4.99	<i>spdcch-differentRS-types-r15</i>	70
4.3.4.100	<i>spt-Parameters-r15</i>	70
4.3.4.101	<i>sps-CyclicShift-r15</i>	70
4.3.4.102	<i>subslotPDSCH-TxDiv-TM9and10-r15</i>	70
4.3.4.103	<i>sTTI-SupportedCombinations-r15</i>	71
4.3.4.104	<i>Void</i>	71
4.3.4.105	<i>sTTI-SPT-BandParameters-r15</i>	71
4.3.4.106	<i>sTTI-SupportedCSI-Proc-r15</i>	71
4.3.4.107	<i>txDiv-SPUCCH-r15</i>	71
4.3.4.108	<i>ul-256QAM-Slot-r15</i>	71
4.3.4.109	<i>ul-256QAM-Subslot-r15</i>	71
4.3.4.110	<i>ue-TxAntennaSelection-SRS-1T4R-r15</i>	71
4.3.4.111	<i>ue-TxAntennaSelection-SRS-2T4R-2Pairs-r15</i>	71
4.3.4.112	<i>ue-TxAntennaSelection-SRS-2T4R-3Pairs-r15</i>	71
4.3.4.113	<i>wakeUpSignal-r15</i>	72
4.3.4.114	<i>wakeUpSignalMinGap-eDRX-r15</i>	72
4.3.4.115	<i>mixedOperationMode-r15</i>	72
4.3.4.116	<i>void</i>	72
4.3.4.117	<i>sr-WithHARQ-ACK-r15</i>	72
4.3.4.118	<i>sr-WithoutHARQ-ACK-r15</i>	72
4.3.4.119	<i>nprach-Format2-r15</i>	72
4.3.4.120	<i>ce-UL-HARQ-ACK-Feedback-r15</i>	72
4.3.4.121	<i>ce-PDSCH-FlexibleStartPRB-CE-ModeA-r15</i>	72
4.3.4.122	<i>ce-PDSCH-FlexibleStartPRB-CE-ModeB-r15</i>	72
4.3.4.123	<i>ce-PUSCH-FlexibleStartPRB-CE-ModeA-r15</i>	72
4.3.4.124	<i>ce-PUSCH-FlexibleStartPRB-CE-ModeB-r15</i>	73
4.3.4.125	<i>ce-CRS-IntfMitig-r15</i>	73
4.3.4.126	<i>ce-PDSCH-64QAM-r15</i>	73
4.3.4.127	<i>ce-CQI-AlternativeTable-r15</i>	73
4.3.4.128	<i>ce-PUSCH-SubPRB-Allocation-r15</i>	73
4.3.4.129	<i>wakeUpSignal-TDD-r15</i>	73
4.3.4.130	<i>wakeUpSignalMinGap-eDRX-TDD-r15</i>	73
4.3.4.131	<i>shortCqi-ForSCellActivation-r15</i>	73
4.3.4.132	<i>crs-IntfMitig-r15</i>	73
4.3.4.133	<i>srs-UpPTS-6sym-r14</i>	73
4.3.4.134	<i>multiCarrierPagingTDD-r15</i>	74
4.3.4.135	<i>altMCS-Table-r15</i>	74
4.3.4.136	<i>ul-PowerControlEnhancements-r15</i>	74
4.3.4.137	<i>additionalTransmissionSIB1-r15</i>	74
4.3.4.138	<i>aperiodicCsi-ReportingSTTI-r15</i>	74
4.3.4.139	<i>dmrs-BasedSPDCCH-MBSFN-r15</i>	74
4.3.4.140	<i>dmrs-BasedSPDCCH-nonMBSFN-r15</i>	74
4.3.4.141	<i>maxNumberUpdatedCSI-Proc-STTI-Comb77-r15</i>	74
4.3.4.142	<i>maxNumberUpdatedCSI-Proc-STTI-Comb27-r15</i>	74
4.3.4.143	<i>maxNumberUpdatedCSI-Proc-STTI-Comb22-Set1-r15</i>	74
4.3.4.144	<i>maxNumberUpdatedCSI-Proc-STTI-Comb22-Set2-r15</i>	74
4.3.4.145	<i>powerUCI-SlotPUSCH-r15</i>	75
4.3.4.146	<i>powerUCI-SubslotPUSCH-r15</i>	75
4.3.4.147	<i>spdcch-Reuse-r15</i>	75
4.3.4.148	<i>sps-STTI-r15</i>	75
4.3.4.149	<i>sTTI-FD-MIMO-Coexistence-r15</i>	75
4.3.4.150	<i>sTTI-SPT-Supported-r15</i>	75
4.3.4.151	<i>tm8-slotPDSCH-r15</i>	75

4.3.4.152	<i>tm9-slotSubslot-r15</i>	75
4.3.4.153	<i>tm9-slotSubslotMBSFN-r15</i>	75
4.3.4.154	<i>tm10-slotSubslot-r15</i>	75
4.3.4.155	<i>tm10-slotSubslotMBSFN-r15</i>	75
4.3.4.156	<i>ul-AsyncHarqSharingDiff-TTI-Lengths-r15</i>	76
4.3.4.157	<i>semiStaticCFI-r15</i>	76
4.3.4.158	<i>semiStaticCFI-Pattern-r15</i>	76
4.3.4.159	<i>pdsch-RepSubframe-r15</i>	76
4.3.4.160	<i>pdsch-RepSlot-r15</i>	76
4.3.4.161	<i>pdsch-RepSubslot-r15</i>	76
4.3.4.162	<i>pusch-SPS-SubframeRepPCell-r15</i>	76
4.3.4.163	<i>pusch-SPS-SubframeRepPSCell-r15</i>	76
4.3.4.164	<i>pusch-SPS-SubframeRepSCell-r15</i>	76
4.3.4.165	<i>pusch-SPS-SlotRepPCell-r15</i>	76
4.3.4.166	<i>pusch-SPS-SlotRepPSCell-r15</i>	76
4.3.4.167	<i>pusch-SPS-SlotRepSCell-r15</i>	77
4.3.4.168	<i>pusch-SPS-SubslotRepPCell-r15</i>	77
4.3.4.169	<i>pusch-SPS-SubslotRepPSCell-r15</i>	77
4.3.4.170	<i>pusch-SPS-SubslotRepSCell-r15</i>	77
4.3.4.171	<i>pusch-SPS-MaxConfigSubframe-r15</i>	77
4.3.4.172	<i>pusch-SPS-MultiConfigSubframe-r15</i>	77
4.3.4.173	<i>pusch-SPS-MaxConfigSlot-r15</i>	77
4.3.4.174	<i>pusch-SPS-MultiConfigSlot-r15</i>	77
4.3.4.175	<i>pusch-SPS-MaxConfigSubslot-r15</i>	77
4.3.4.176	<i>pusch-SPS-MultiConfigSubslot-r15</i>	77
4.3.4.177	<i>npusch-3dot75kHz-SCS-TDD-r15</i>	78
4.3.4.178	<i>crs-IM-TM1-toTM9-OneRX-Port</i>	78
4.3.4.179	<i>cch-IM-RefRecTypeA-OneRX-Port</i>	78
4.3.4.180	<i>dmrs-OverheadReduction-r15</i>	78
4.3.4.181	<i>srs-DCI7-TriggeringFS2-r15</i>	78
4.3.4.182	<i>npusch-MultiTB-r16</i>	78
4.3.4.183	<i>npdsch-MultiTB-r16</i>	78
4.3.4.184	<i>pusch-MultiTB-CE-ModeA-r16</i>	78
4.3.4.185	<i>pdsch-MultiTB-CE-ModeA-r16</i>	79
4.3.4.186	<i>pusch-MultiTB-CE-ModeB-r16</i>	79
4.3.4.187	<i>pdsch-MultiTB-CE-ModeB-r16</i>	79
4.3.4.188	<i>ce-CSI-RS-Feedback-r16</i>	79
4.3.4.188a	<i>ce-CSI-RS-FeedbackCodebookRestriction-r16</i>	79
4.3.4.189	<i>mpdcch-InLteControlRegionCE-ModeA-r16</i>	79
4.3.4.189a	<i>mpdcch-InLteControlRegionCE-ModeB-r16</i>	79
4.3.4.189b	<i>pdsch-InLteControlRegionCE-ModeA-r16</i>	79
4.3.4.189c	<i>pdsch-InLteControlRegionCE-ModeB-r16</i>	79
4.3.4.190	<i>crs-ChEstMPDCCH-CE-ModeA-r16</i>	80
4.3.4.190a	<i>crs-ChEstMPDCCH-CE-ModeB-r16</i>	80
4.3.4.190b	<i>crs-ChEstMPDCCH-CSI-r16</i>	80
4.3.4.190c	<i>crs-ChEstMPDCCH-ReciprocityTDD-r16</i>	80
4.3.4.191	<i>widebandPRG-Slot-r16, widebandPRG-Subslot-r16, widebandPRG-Subframe-r16</i>	80
4.3.4.192	<i>npusch-MultiTB-Interleaving-r16</i>	80
4.3.4.193	<i>npdsch-MultiTB-Interleaving-r16</i>	80
4.3.4.194	<i>multiTB-HARQ-AckBundling-r16</i>	80
4.3.4.195	<i>groupWakeUpSignal-r16</i>	80
4.3.4.196	<i>groupWakeUpSignalAlternation-r16</i>	81
4.3.4.197	<i>subframeResourceResvUL-r16</i>	81
4.3.4.198	<i>subframeResourceResvDL-r16</i>	81
4.3.4.199	<i>slotSymbolResourceResvUL-r16</i>	81
4.3.4.200	<i>slotSymbolResourceResvDL-r16</i>	81
4.3.4.201	<i>groupWakeUpSignalTDD-r16</i>	81
4.3.4.202	<i>groupWakeUpSignalAlternationTDD-r16</i>	81
4.3.4.203	<i>subframeResourceResvUL-CE-ModeA-r16</i>	81
4.3.4.204	<i>subframeResourceResvUL-CE-ModeB-r16</i>	81
4.3.4.205	<i>subframeResourceResvDL-CE-ModeA-r16</i>	82
4.3.4.206	<i>subframeResourceResvDL-CE-ModeB-r16</i>	82

4.3.4.207	slotSymbolResourceResvUL-CE-ModeA-r16.....	82
4.3.4.208	slotSymbolResourceResvUL-CE-ModeB-r16.....	82
4.3.4.209	slotSymbolResourceResvDL-CE-ModeA-r16.....	82
4.3.4.210	slotSymbolResourceResvDL-CE-ModeB-r16.....	82
4.3.4.211	subcarrierPuncturingCE-ModeA-r16.....	82
4.3.4.212	subcarrierPuncturingCE-ModeB-r16.....	82
4.3.4.213	ce-MultiTB-Interleaving-r16.....	82
4.3.4.214	ce-MultiTB-HARQ-AckBundling-r16.....	82
4.3.4.215	ce-MultiTB-SubPRB-r16.....	83
4.3.4.216	ce-MultiTB-EarlyTermination-r16.....	83
4.3.4.217	ce-MultiTB-64QAM-r16.....	83
4.3.4.218	ce-MultiTB-FrequencyHopping-r16.....	83
4.3.4.219	Void.....	83
4.3.4.220	virtualCellID-BasicSRS-r16.....	83
4.3.4.221	addSRS-r16.....	83
4.3.4.221.1	addSRS-1T2R-r16.....	83
4.3.4.221.2	addSRS-1T4R-r16.....	83
4.3.4.221.3	addSRS-2T4R-2Pairs-r16.....	83
4.3.4.221.4	addSRS-2T4R-3Pairs-r16.....	84
4.3.4.221.5	addSRS-AntennaSwitching-r16.....	84
4.3.4.221.6	addSRS-CarrierSwitching-r16.....	84
4.3.4.221.7	addSRS-FrequencyHopping-r16.....	84
4.3.4.221.8	virtualCellID-AddSRS-r16.....	84
4.3.4.222	npdsch-16QAM-r17.....	84
4.3.4.223	npusch-16QAM-r17.....	84
4.3.4.224	ce-PDSCH-MaxTBS-r17.....	84
4.3.4.225	ce-PDSCH-14HARQProcesses-r17.....	85
4.3.4.226	ce-PDSCH-14HARQProcesses-Alt2-r17.....	85
4.3.4.227	csi-SubframeSet2ForDormantSCell-r17.....	85
4.3.5	RF parameters.....	85
4.3.5.1	supportedBandListEUTRA.....	85
4.3.5.1.1	ue-PowerClass-N-r13, ue-PowerClass-5-r13.....	85
4.3.5.1.2	intraFreq-CE-NeedForGaps-r13.....	85
4.3.5.1.3	ue-CA-PowerClass-N.....	85
4.3.5.1A	supportedBandList-r13.....	85
4.3.5.1A.1	powerClassNB-20dBm-r13.....	85
4.3.5.1A.2	powerClassNB-14dBm-r14.....	85
4.3.5.2	supportedBandCombination.....	86
4.3.5.2.1	supportedBandCombinationReduced-r13.....	87
4.3.5.3	multipleTimingAdvance.....	87
4.3.5.4	simultaneousRx-Tx.....	87
4.3.5.5	supportedCSI-Proc-r11.....	87
4.3.5.6	freqBandRetrieval-r11.....	87
4.3.5.7	dl-256QAM-r12.....	87
4.3.5.8	supportedNAICS-2CRS-AP-r12.....	87
4.3.5.9	dc-Support-r12.....	88
4.3.5.9.1	asynchronous-r12.....	88
4.3.5.9.2	supportedCellGrouping-r12.....	88
4.3.5.10	modifiedMPR-Behavior-r10.....	88
4.3.5.11	freqBandPriorityAdjustment-r12.....	88
4.3.5.12	commSupportedBandsPerBC-r12.....	88
4.3.5.13	supportedCSI-Proc-r12.....	88
4.3.5.14	fourLayerTM3-TM4-r10.....	88
4.3.5.15	fourLayerTM3-TM4-perCC-r12.....	88
4.3.5.16	multiNS-Pmax-r10.....	88
4.3.5.16A	multiNS-Pmax-r13.....	89
4.3.5.17	differentFallbackSupported-r13.....	89
4.3.5.18	maximumCCsRetrieval-r13.....	89
4.3.5.19	skipFallbackCombinations-r13.....	89
4.3.5.20	Void.....	89
4.3.5.21	reducedIntNonContComb-r13.....	89
4.3.5.22	additionalRx-Tx-PerformanceReq-r13.....	89

4.3.5.23	<i>maxLayersMIMO-Indication-r12</i>	89
4.3.5.24	<i>rf-RetuningTimeDL-r14</i>	89
4.3.5.25	<i>rf-RetuningTimeUL-r14</i>	90
4.3.5.26	<i>diffFallbackCombReport-r14</i>	90
4.3.5.27	<i>v2x-SupportedTxBandCombListPerBC-r14, v2x-SupportedRxBandCombListPerBC-r14</i>	90
4.3.5.28	<i>txAntennaSwitchDL-r13</i>	90
4.3.5.29	<i>txAntennaSwitchUL-r13</i>	90
4.3.5.30	<i>supportedMIMO-CapabilityDL-r15</i>	90
4.3.5.31	<i>dl-1024QAM-r15</i>	90
4.3.5.32	<i>srs-MaxSimultaneousCCs-r14</i>	91
4.3.5.33	<i>powerClass-14dBm-r15</i>	91
4.3.5.34	<i>supportedMIMO-CapabilityDL-MRDC-r15</i>	91
4.3.5.35	<i>srs-FlexibleTiming-r14</i>	91
4.3.5.36	<i>srs-HARQ-ReferenceConfig-r14</i>	91
4.3.5.37	<i>fourLayerTM3-TM4-r15</i>	91
4.3.5.38	<i>supportedCSI-Proc-r15</i>	91
4.3.5.39	<i>intraFreqAsyncDAPS-r16</i>	91
4.3.5.40	<i>intraFreqDAPS-r16</i>	92
4.3.5.41	<i>Void</i>	92
4.3.5.42	<i>interFreqAsyncDAPS-r16</i>	92
4.3.5.43	<i>interFreqDAPS-r16</i>	92
4.3.5.44	<i>interFreqMultiUL-TransmissionDAPS-r16</i>	92
4.3.5.45	<i>intraFreqTwoTAGs-DAPS-r16</i>	92
4.3.5.46	<i>v2x-SupportedTxBandCombListPerBC-v1630, v2x-SupportedRxBandCombListPerBC-v1630</i>	92
4.3.5.47	<i>scalingFactorTxSidelink-r16, scalingFactorRxSidelink-r16</i>	92
4.3.5.48	<i>interBandPowerSharingSyncDAPS-r16</i>	92
4.3.5.49	<i>interBandPowerSharingAsyncDAPS-r16</i>	93
4.3.6	Measurement parameters	93
4.3.6.1	<i>interFreqNeedForGaps and interRAT-NeedForGaps</i>	93
4.3.6.2	<i>rsrqMeasWideband</i>	93
4.3.6.3	<i>timerT312-r12</i>	93
4.3.6.4	<i>alternativeTimeToTrigger-r12</i>	93
4.3.6.5	<i>benefitsFromInterruption-r11</i>	93
4.3.6.6	<i>incMonEUTRA-r12</i>	93
4.3.6.7	<i>incMonUTRA-r12</i>	93
4.3.6.8	<i>extendedMaxMeasId-r12</i>	93
4.3.6.9	<i>crs-DiscoverySignalsMeas-r12</i>	94
4.3.6.10	<i>csi-RS-DiscoverySignalsMeas-r12</i>	94
4.3.6.11	<i>extendedRSRQ-LowerRange-r12</i>	94
4.3.6.12	<i>rsrq-OnAllSymbols-r12</i>	94
4.3.6.13	<i>rs-SINR-Meas-r13</i>	94
4.3.6.14	<i>allowedCellList-r13</i>	94
4.3.6.15	<i>extendedFreqPriorities-r13</i>	94
4.3.6.16	<i>extendedMaxObjectId-r13</i>	94
4.3.6.17	<i>ul-PDCP-Delay-r13</i>	94
4.3.6.18	<i>Void</i>	95
4.3.6.19	<i>rssI-AndChannelOccupancyReporting-r13</i>	95
4.3.6.20	<i>multiBandInfoReport-r13</i>	95
4.3.6.21	<i>Void</i>	95
4.3.6.22	<i>Void</i>	95
4.3.6.23	<i>ceMeasurements-r14</i>	95
4.3.6.24	<i>ncsg-r14</i>	95
4.3.6.25	<i>perServingCellMeasurementGap-r14</i>	95
4.3.6.26	<i>shortMeasurementGap-r14</i>	95
4.3.6.27	<i>nonUniformGap-r14</i>	95
4.3.6.28	<i>rlm-ReportSupport-r14</i>	95
4.3.6.29	<i>Void</i>	95
4.3.6.30	<i>qoe-MeasReport-r15</i>	95
4.3.6.31	<i>ca-IdleModeMeasurements-r15</i>	96
4.3.6.32	<i>ca-IdleModeValidityArea-r15</i>	96
4.3.6.33	<i>qoe-MTSI-MeasReport-r15</i>	96
4.3.6.34	<i>multipleCellsMeasExtension-r15</i>	96

4.3.6.35	<i>heightMeas-r15</i>	96
4.3.6.36	<i>measGapPatterns-r15</i>	96
4.3.6.37	<i>dl-ChannelQualityReporting-r16</i>	96
4.3.6.37a	<i>ce-DL-ChannelQualityReporting-r16</i>	96
4.3.6.38	<i>interRAT-NeedForGapsNR-r16</i>	96
4.3.6.39	<i>ce-MeasRSS-Dedicated-r16</i>	96
4.3.6.39a	<i>ce-MeasRSS-DedicatedSameRBs-r16</i>	97
4.3.6.40	<i>eutra-IdleInactiveMeasurements-r16</i>	97
4.3.6.41	<i>nr-IdleInactiveMeasFR1-r16</i>	97
4.3.6.42	<i>nr-IdleInactiveMeasFR2-r16</i>	97
4.3.6.43	<i>idleInactiveValidityAreaList-r16</i>	97
4.3.6.44	<i>measGapPatterns-NRonly-r16</i>	97
4.3.6.45	<i>measGapPatterns-NRonly-ENDC-r16</i>	97
4.3.6.46	<i>nr-IdleInactiveBeamMeasFR1-r16</i>	97
4.3.6.47	<i>nr-IdleInactiveBeamMeasFR2-r16</i>	98
4.3.6.48	<i>nr-RSSI-ChannelOccupancyReporting-r17</i>	98
4.3.6.49	<i>connModeMeasIntraFreq-r17</i>	98
4.3.6.50	<i>connModeMeasInterFreq-r17</i>	98
4.3.7	Inter-RAT parameters	98
4.3.7.1	<i>utraFDD</i>	98
4.3.7.2	<i>supportedBandListUTRA-FDD</i>	98
4.3.7.3	<i>utraTDD128</i>	98
4.3.7.4	<i>supportedBandListUTRA-TDD128</i>	98
4.3.7.5	<i>utraTDD384</i>	98
4.3.7.6	<i>supportedBandListUTRA-TDD384</i>	99
4.3.7.7	<i>utraTDD768</i>	99
4.3.7.8	<i>supportedBandListUTRA-TDD768</i>	99
4.3.7.9	<i>geran</i>	99
4.3.7.10	<i>supportedBandListGERAN</i>	99
4.3.7.11	<i>interRAT-PS-HO-ToGERAN</i>	99
4.3.7.12	<i>cdma2000-HRPD</i>	99
4.3.7.13	<i>supportedBandListHRPD</i>	99
4.3.7.14	<i>tx-ConfigHRPD</i>	99
4.3.7.15	<i>rx-ConfigHRPD</i>	99
4.3.7.16	<i>cdma2000-1xRTT</i>	99
4.3.7.17	<i>supportedBandList1XRTT</i>	99
4.3.7.18	<i>tx-Config1XRTT</i>	100
4.3.7.19	<i>rx-Config1XRTT</i>	100
4.3.7.20	<i>e-CSFB-1XRTT</i>	100
4.3.7.21	<i>e-CSFB-ConcPS-Mob1XRTT</i>	100
4.3.7.22	<i>e-RedirectionUTRA</i>	100
4.3.7.23	<i>e-RedirectionGERAN</i>	100
4.3.7.24	<i>dtm</i>	100
4.3.7.25	<i>e-CSFB-dual-1XRTT</i>	100
4.3.7.26	<i>e-RedirectionUTRA-TDD</i>	100
4.3.7.27	<i>cdma2000-NW-Sharing-r11</i>	100
4.3.7.28	<i>mfbi-UTRA</i>	100
4.3.7.29	<i>supportedBandListWLAN</i>	101
4.3.8	General parameters	101
4.3.8.1	<i>accessStratumRelease</i>	101
4.3.8.1A	<i>accessStratumRelease-r13</i>	101
4.3.8.2	<i>deviceType</i>	101
4.3.8.3	<i>Void</i>	101
4.3.8.4	<i>Void</i>	101
4.3.8.5	<i>multipleDRB-r13</i>	101
4.3.8.6	<i>Void</i>	101
4.3.8.7	<i>earlyData-UP-r15</i>	101
4.3.8.8	<i>void</i>	101
4.3.8.9	<i>extendedNumberOfDRBs-r15</i>	101
4.3.8.10	<i>reducedCP-Latency-r15</i>	101
4.3.8.11	<i>earlySecurityReactivation-r16</i>	101
4.3.8.12	<i>Void</i>	102

4.3.8.13	Void.....	102
4.3.8.14	<i>dl-DedicatedMessageSegmentation-r16</i>	102
4.3.8.15	<i>altFreqPriority-r16</i>	102
4.3.8.16	<i>coverageBasedPaging-r17</i>	102
4.3.9	Void	102
4.3.10	CSG Proximity Indication parameters	102
4.3.10.1	<i>intraFreqProximityIndication</i>	102
4.3.10.2	<i>interFreqProximityIndication</i>	102
4.3.10.3	<i>utran-ProximityIndication</i>	102
4.3.11	Neighbour cell SI acquisition parameters	102
4.3.11.1	<i>intraFreqSI-AcquisitionForHO</i>	102
4.3.11.2	<i>interFreqSI-AcquisitionForHO</i>	102
4.3.11.3	<i>utran-SI-AcquisitionForHO</i>	102
4.3.11.4	<i>reportCGI-NR-EN-DC-r15</i>	103
4.3.11.5	<i>reportCGI-NR-NoEN-DC-r15</i>	103
4.3.11.6	<i>eutra-CGI-Reporting-ENDC</i>	103
4.3.11.7	<i>utra-GERAN-CGI-Reporting-ENDC</i>	103
4.3.11.8	<i>eutra-SI-AcquisitionForHO-ENDC-r16</i>	103
4.3.11.9	<i>nr-AutonomousGaps-ENDC-FR1-r16</i>	104
4.3.11.10	<i>nr-AutonomousGaps-ENDC-FR2-r16</i>	104
4.3.11.11	<i>nr-AutonomousGaps-FR1-r16</i>	104
4.3.11.12	<i>nr-AutonomousGaps-FR2-r16</i>	104
4.3.11.13	<i>eutra-CGI-Reporting-NEDC-r15</i>	104
4.3.11.14	<i>gNB-ID-Length-Reporting-NR-EN-DC-r17</i>	104
4.3.11.15	<i>gNB-ID-Length-Reporting-NR-NoEN-DC-r17</i>	104
4.3.12	SON parameters	104
4.3.12.1	<i>rach-Report</i>	104
4.3.12.2	<i>anr-Report-r16</i>	105
4.3.12.3	<i>rach-Report-r16</i>	105
4.3.13	UE-based network performance measurement parameters	105
4.3.13.1	<i>loggedMeasurementsIdle</i>	105
4.3.13.2	<i>standaloneGNSS-Location</i>	105
4.3.13.3	Void.....	105
4.3.13.4	<i>loggedMBSFNMeasurements-r12</i>	105
4.3.13.5	<i>locationReport-r14</i>	105
4.3.13.6	<i>loggedMeasBT-r15</i>	105
4.3.13.7	<i>loggedMeasWLAN-r15</i>	105
4.3.13.8	<i>immMeasBT-r15</i>	105
4.3.13.9	<i>immMeasWLAN-r15</i>	105
4.3.13.10	<i>ul-PDCP-AvgDelay-r16</i>	105
4.3.13.11	<i>loggedMeasIdleEventL1-r17</i>	106
4.3.13.12	<i>loggedMeasIdleEventOutOfCoverage-r17</i>	106
4.3.13.13	<i>loggedMeasUncomBarPre-r17</i>	106
4.3.13.14	<i>immMeasUncomBarPre-r17</i>	106
4.3.14	IMS Voice parameters	106
4.3.14.1	<i>voiceOver-PS-HS-UTRA-FDD</i>	106
4.3.14.2	<i>voiceOver-PS-HS-UTRA-TDD128</i>	106
4.3.14.3	<i>srvcc-FromUTRA-FDD-ToGERAN</i>	106
4.3.14.4	<i>srvcc-FromUTRA-FDD-ToUTRA-FDD</i>	106
4.3.14.5	<i>srvcc-FromUTRA-TDD128-ToGERAN</i>	106
4.3.14.6	<i>srvcc-FromUTRA-TDD128-ToUTRA-TDD128</i>	106
4.3.15	Other parameters.....	107
4.3.15.1	Void.....	107
4.3.15.2	<i>inDeviceCoexInd-r11</i>	107
4.3.15.3	<i>powerPrefInd-r11</i>	107
4.3.15.4	<i>ue-Rx-TxTimeDiffMeasurements-r11</i>	107
4.3.15.5	Void.....	107
4.3.15.6	Void.....	107
4.3.15.7	Void.....	107
4.3.15.8	<i>inDeviceCoexInd-UL-CA-r11</i>	107
4.3.15.9	<i>bwPrefInd-r14</i>	107
4.3.15.10	<i>inDeviceCoexInd-HardwareSharingInd-r13</i>	107

4.3.15.11	<i>overheatingInd-r14</i>	107
4.3.15.12	<i>assistInfoBitForLC-r15</i>	107
4.3.15.13	<i>timeReferenceProvision-r15</i>	107
4.3.15.14	<i>flightPathPlan-r15</i>	108
4.3.15.15	<i>inDeviceCoexInd-ENDC-r15</i>	108
4.3.15.16	<i>nonCSG-SI-Reporting-r14</i>	108
4.3.15.17	<i>resumeWithStoredMCG-SCells-r16</i>	108
4.3.15.18	<i>resumeWithMCG-SCellConfig-r16</i>	108
4.3.15.19	<i>resumeWithStoredSCG-r16</i>	108
4.3.15.20	<i>resumeWithSCG-Config-r16</i>	108
4.3.15.21	<i>mcgRLF-RecoveryViaSCG-r16</i>	108
4.3.15.22	<i>overheatingIndForSCG-r16</i>	108
4.3.15.23	<i>mpsPriorityIndication-r16</i>	108
4.3.15.24	<i>ul-RRC-Segmentation-r16</i>	108
4.3.16	Positioning parameters	109
4.3.16.1	<i>otdoa-UE-assisted</i>	109
4.3.16.2	<i>interFreqRSTDmeasurement</i>	109
4.3.17	MBMS parameters	109
4.3.17.1	<i>mbms-SCell-r11</i>	109
4.3.17.2	<i>mbms-NonServingCell-r11</i>	109
4.3.17.3	<i>mbms-AsyncDC-r12</i>	109
4.3.17.4	<i>fembmsMixedCell-r14</i>	109
4.3.17.5	<i>fembmsDedicatedCell-r14</i>	109
4.3.17.6	<i>subcarrierSpacingMBMS-khz1dot25-r14, subcarrierSpacingMBMS-khz7dot5-r14</i>	109
4.3.17.6a	<i>subcarrierSpacingMBMS-khz0dot37-r16, subcarrierSpacingMBMS-khz2dot5-r16</i>	110
4.3.17.7	<i>mbms-MaxBW-r14</i>	110
4.3.17.8	<i>mbms-ScalingFactor1dot25-r14, mbms-ScalingFactor7dot5-r14</i>	110
4.3.17.9	<i>mbms-ScalingFactor0dot37-r16, mbms-ScalingFactor2dot5-r16</i>	110
4.3.17.10	<i>timeSeparationSlot2-r16, timeSeparationSlot4-r16</i>	110
4.3.17.11	<i>pmch-Bandwidth-n40-r17, pmch-Bandwidth-n35-r17, pmch-Bandwidth-n30-r17</i>	110
4.3.18	RAN-assisted WLAN interworking parameters	110
4.3.18.1	<i>wlan-IW-RAN-Rules-r12</i>	110
4.3.18.2	<i>wlan-IW-ANDSF-Policies-r12</i>	111
4.3.18.3	<i>rclwi-r13</i>	111
4.3.19	MAC parameters	111
4.3.19.1	<i>longDRX-Command-r12</i>	111
4.3.19.2	<i>logicalChannelSR-ProhibitTimer-r12</i>	111
4.3.19.3	<i>extendedMAC-LengthField-r13</i>	111
4.3.19.4	<i>extendedLongDRX-r13</i>	111
4.3.19.5	<i>shortSPS-IntervalFDD-r14</i>	111
4.3.19.6	<i>shortSPS-IntervalTDD-r14</i>	111
4.3.19.7	<i>skipUplinkDynamic-r14</i>	111
4.3.19.8	<i>skipUplinkSPS-r14</i>	111
4.3.19.9	<i>dataInactMon-r14</i>	111
4.3.19.10	<i>rai-Support-r14</i>	112
4.3.19.11	<i>multipleUplinkSPS-r14</i>	112
4.3.19.12	<i>min-Proc-TimelineSubslot-r15</i>	112
4.3.19.13	<i>skipSubframeProcessing-r15</i>	112
4.3.19.14	<i>earlyContentionResolution-r14</i>	112
4.3.19.15	<i>sr-SPS-BSR-r15</i>	112
4.3.19.16	<i>dormantSCellState-r15</i>	112
4.3.19.17	<i>directSCellActivation-r15</i>	112
4.3.19.18	<i>directSCellHibernation-r15</i>	113
4.3.19.19	<i>sps-ServingCell-r15</i>	113
4.3.19.20	<i>extendedLCID-Duplication-r15</i>	113
4.3.19.21	<i>eLCID-Support-r15</i>	113
4.3.19.22	<i>rai-SupportEnh-r16</i>	113
4.3.19.23	<i>directMCG-SCellActivationResume-r16</i>	113
4.3.19.24	<i>directSCG-SCellActivationResume-r16</i>	113
4.3.20	Dual Connectivity parameters	113
4.3.20.1	<i>drb-TypeSplit-r12</i>	113
4.3.20.2	<i>drb-TypeSCG-r12</i>	114

4.3.20.3	<i>pdcp-TransferSplitUL-r13</i>	114
4.3.20.4	<i>ue-SSTD-Meas-r13</i>	114
4.3.21	Sidelink parameters.....	114
4.3.21.1	<i>commSupportedBands-r12</i>	114
4.3.21.2	<i>commSimultaneousTx-r12</i>	114
4.3.21.3	<i>discSupportedBands-r12</i>	114
4.3.21.4	<i>discScheduledResourceAlloc-r12</i>	114
4.3.21.5	<i>disc-UE-SelectedResourceAlloc-r12</i>	114
4.3.21.6	<i>disc-SLSS-r12</i>	114
4.3.21.7	<i>discSupportedProc-r12</i>	114
4.3.21.8	<i>commMultipleTx-r13</i>	115
4.3.21.9	<i>discInterFreqTx-r13</i>	115
4.3.21.10	<i>discPeriodicSLSS-r13</i>	115
4.3.21.11	<i>discSysInfoReporting-r13</i>	115
4.3.21.12	<i>zoneBasedPoolSelection-r14</i>	115
4.3.21.13	<i>v2x-HighReception-r14</i>	115
4.3.21.14	<i>v2x-eNB-Scheduled-r14</i>	115
4.3.21.15	<i>ue-AutonomousWithFullSensing-r14</i>	115
4.3.21.16	<i>ue-AutonomousWithPartialSensing-r14</i>	115
4.3.21.17	<i>slss-TxRx-r14</i>	115
4.3.21.18	<i>sl-CongestionControl-r14</i>	116
4.3.21.19	<i>v2x-TxWithShortResvInterval-r14</i>	116
4.3.21.20	<i>v2x-numberTxRxTiming-r14</i>	116
4.3.21.21	<i>v2x-nonAdjacentPSCCH-PSSCH-r14</i>	116
4.3.21.22	<i>v2x-HighPower-r14</i>	116
4.3.21.23	<i>v2x-SupportedBandCombinationList-r14</i>	116
4.3.21.24	<i>slss-SupportedTxFreq-r15</i>	116
4.3.21.25	<i>sl-64QAM-Tx-r15</i>	116
4.3.21.26	<i>sl-TxDiversity-r15</i>	116
4.3.21.27	<i>v2x-EnhancedHighReception-r15</i>	116
4.3.21.28	<i>sl-64QAM-Rx-r15</i>	116
4.3.21.29	<i>sl-RateMatchingTBSScaling-r15</i>	117
4.3.21.30	<i>sl-LowT2min-r15</i>	117
4.3.21.31	<i>v2x-SensingReportingMode3-r15</i>	117
4.3.21.32	<i>v2x-SupportedBandCombinationListEUTRA-NR-r16</i>	117
4.3.21.33	Void.....	117
4.3.21.34	<i>tx-Sidelink-r16, rx-Sidelink-r16</i>	117
4.3.22	SC-PTM parameters.....	117
4.3.22.1	<i>scptm-ParallelReception-r13</i>	117
4.3.22.2	Void.....	117
4.3.22.3	<i>scptm-SCell-r13</i>	117
4.3.22.4	<i>scptm-NonServingCell-r13</i>	117
4.3.22.5	<i>scptm-AsyncDC-r13</i>	118
4.3.23	LAA parameters.....	118
4.3.23.1	<i>downlinkLAA-r13</i>	118
4.3.23.2	<i>crossCarrierSchedulingLAA-DL-r13</i>	118
4.3.23.3	<i>csi-RS-DRS-RRM-MeasurementsLAA-r13</i>	118
4.3.23.4	<i>endingDwPTS-r13</i>	118
4.3.23.5	<i>secondSlotStartingPosition-r13</i>	118
4.3.23.6	<i>tm9-LAA-r13</i>	118
4.3.23.7	<i>tm10-LAA-r13</i>	118
4.3.23.8	<i>uplinkLAA-r14</i>	118
4.3.23.9	<i>crossCarrierSchedulingLAA-UL-r14</i>	119
4.3.23.10	<i>twoStepSchedulingTimingInfo-r14</i>	119
4.3.23.11	<i>uss-BlindDecodingAdjustment-r14</i>	119
4.3.23.12	<i>uss-BlindDecodingReduction-r14</i>	119
4.3.23.13	<i>outOfSequenceGrantHandling-r14</i>	119
4.3.23.14	<i>aul-r15</i>	119
4.3.23.15	<i>laa-PUSCH-Mode1-r15</i>	119
4.3.23.16	<i>laa-PUSCH-Mode2-r15</i>	119
4.3.23.17	<i>laa-PUSCH-Mode3-r15</i>	119
4.3.24	LWIP parameters.....	119

4.3.24.1	<i>lwip-r13</i>	119
4.3.24.2	<i>lwip-Aggregation-UL-r14</i>	120
4.3.24.3	<i>lwip-Aggregation-DL-r14</i>	120
4.3.25	LWA parameters.....	120
4.3.25.1	<i>lwa-r13</i>	120
4.3.25.2	<i>lwa-SplitBearer-r13</i>	120
4.3.25.3	<i>lwa-BufferSize-r13</i>	120
4.3.25.4	<i>wlan-MAC-Address-r13</i>	120
4.3.25.5	<i>lwa-HO-WithoutWT-Change-r14</i>	120
4.3.25.6	<i>lwa-UL-r14</i>	120
4.3.25.7	<i>Void</i>	120
4.3.25.8	<i>wlan-SupportedDataRate-r14</i>	120
4.3.25.9	<i>lwa-RLC-UM-r14</i>	120
4.3.26	<i>Void</i>	121
4.3.26.1	<i>Void</i>	121
4.3.27	Inter-RAT parameters WLAN.....	121
4.3.27.1	<i>supportedBandListWLAN-r13</i>	121
4.3.28	EBF FD-MIMO parameters.....	121
4.3.28.1	<i>beamformed-r13</i>	121
4.3.28.2	<i>channelMeasRestriction-r13</i>	121
4.3.28.3	<i>csi-RS-EnhancementsTDD-r13</i>	121
4.3.28.4	<i>dmrs-Enhancements-r13</i>	121
4.3.28.5	<i>interferenceMeasRestriction-r13</i>	121
4.3.28.6	<i>nonPrecoded-r13</i>	121
4.3.28.7	<i>srs-Enhancements-r13</i>	122
4.3.28.8	<i>srs-EnhancementsTDD-r13</i>	122
4.3.28.9	<i>csi-ReportingAdvanced-r14, csi-ReportingAdvancedMaxPorts-r14</i>	122
4.3.28.10	<i>mimo-CBSR-AdvancedCSI-r15</i>	122
4.3.28.11	<i>csi-ReportingNP-r14</i>	122
4.3.28.12	<i>relWeightTwoLayers-r13, relWeightFourLayers-r13, relWeightEightLayers-r13</i>	122
4.3.28.13	<i>totalWeightedLayers-r13</i>	122
4.3.28.14	<i>zp-CSI-RS-AperiodicInfo-r14</i>	123
4.3.28.15	<i>ul-dmrs-Enhancements-r14</i>	123
4.3.28.16	<i>densityReductionNP-r14, densityReductionBF-r14</i>	123
4.3.28.17	<i>hybridCSI-r14</i>	123
4.3.28.18	<i>semiOL-r14</i>	123
4.3.28.19	<i>nzp-CSI-RS-AperiodicInfo-r14</i>	123
4.3.28.20	<i>nzp-CSI-RS-PeriodicInfo-r14</i>	123
4.3.29	CE parameters.....	124
4.3.29.1	<i>ce-ModeA-r13</i>	124
4.3.29.2	<i>ce-ModeB-r13</i>	124
4.3.29.3	<i>intraFreqA3-CE-ModeA-r13</i>	124
4.3.29.4	<i>intraFreqA3-CE-ModeB-r13</i>	124
4.3.29.5	<i>intraFreqHO-CE-ModeA-r13</i>	124
4.3.29.6	<i>intraFreqHO-CE-ModeB-r13</i>	124
4.3.29.7	<i>ue-CE-NeedULGaps-r13</i>	124
4.3.29.8	<i>unicastFrequencyHopping-r13</i>	124
4.3.29.9	<i>ce-SwitchWithoutHO-r14</i>	124
4.3.29.10	<i>tm9-CE-ModeA-r13</i>	125
4.3.29.11	<i>tm9-CE-ModeB-r13</i>	125
4.3.29.12	<i>tm6-CE-ModeA-r13</i>	125
4.3.29.13	<i>etws-CMAS-RxInConnCE-ModeA-r16</i>	125
4.3.29.14	<i>etws-CMAS-RxInConnCE-ModeB-r16</i>	125
4.3.30	Mobility enhancement parameters.....	125
4.3.30.1	<i>makeBeforeBreak-r14</i>	125
4.3.30.2	<i>rach-Less-r14</i>	125
4.3.30.3	<i>cho-r16</i>	125
4.3.30.4	<i>cho-Failure-r16</i>	125
4.3.30.5	<i>cho-FDD-TDD-r16</i>	125
4.3.30.6	<i>cho-TwoTriggerEvents-r16</i>	126
4.3.31	<i>Void</i>	126
4.3.31.1	<i>Void</i>	126