

SLOVENSKI STANDARD SIST EN IEC 61760-1:2022

01-junij-2022

Nadomešča:

SIST EN 61760-1:2006

Tehnologija površinske montaže - 1. del: Standardna metoda za specifikacijo komponent za površinsko montažo (SMDs)

Surface mounting technology - Part 1: Standard method for the specification of surface mounting components (SMDs)

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Oberflächenmontagetechnik - Teil 1: Genormtes Verfahren zur Spezifizierung oberflächenmontierbarer Bauelemente (SMDs)

(standards.iteh.ai)
Technique du montage en surface - Partie 1: Méthode de normalisation pour la spécification des composants montés en surface (CMS)

https://standards.iteh.ai/catalog/standards/sist/7895702a-

Ta slovenski standard je-istoveten-2:a41blEN1IEC:61760-1:2020-1-

2022

ICS:

31.190 Sestavljeni elektronski

elementi

Electronic component

assemblies

SIST EN IEC 61760-1:2022

en

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<u>SIST EN IEC 61760-1:2022</u> https://standards.iteh.ai/catalog/standards/sist/7895702ad56a-4da7-89d6-fea41bb121e7/sist-en-iec-61760-1-2022

EUROPEAN STANDARD NORME EUROPÉENNE EUROPÄISCHE NORM

EN IEC 61760-1

September 2020

ICS 31.240

Supersedes EN 61760-1:2006 and all of its amendments and corrigenda (if any)

English Version

Surface mounting technology - Part 1: Standard method for the specification of surface mounting components (SMDs) (IEC 61760-1:2020)

Technique du montage en surface - Partie 1: Méthode normalisée pour la spécification des composants montés en surface (CMS) (IEC 61760-1:2020) Oberflächenmontagetechnik - Teil 1: Genormtes Verfahren zur Spezifizierung oberflächenmontierbarer Bauelemente (SMDs) (IEC 61760-1:2020)

This European Standard was approved by CENELEC on 2020-08-18. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

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2022



European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Rue de la Science 23, B-1040 Brussels

EN IEC 61760-1:2020 (E)

European foreword

The text of document 91/1648/FDIS, future edition 3 of IEC 61760-1, prepared by IEC/TC 91 "Electronics assembly technology" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN IEC 61760-1:2020.

The following dates are fixed:

- latest date by which the document has to be implemented at national (dop) 2021-05-18 level by publication of an identical national standard or by endorsement
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2023-08-18

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In the official version, for Bibliography, the following notes have to be added for the standards indicated: https://standards.iteh.ai/catalog/standards/sist/7895702a-

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IEC 60062	NOTE	Harmonized as EN 60062
IEC 60068-1	NOTE	Harmonized as EN 60068-1
IEC 60068-2-20	NOTE	Harmonized as EN 60068-2-20
IEC 60068-2-69	NOTE	Harmonized as EN 60068-2-69
IEC 60191-6-19	NOTE	Harmonized as EN 60191-6-19
IEC 60352-5	NOTE	Harmonized as EN 60352-5
IEC 60749 (series)	NOTE	Harmonized as EN 60749 (series)
IEC 61188-5-1	NOTE	Harmonized as EN 61188-5-1
IEC 61189-5-504	NOTE	Harmonized as EN IEC 61189-5-504
IEC 62474	NOTE	Harmonized as EN IEC 62474

EN IEC 61760-1:2020 (E)

Annex ZA

(normative)

Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

	<u>-</u>			
<u>Publication</u>	<u>Year</u>	<u>Title</u>	EN/HD	<u>Year</u>
IEC 60068	-	Environmental testing	-	-
IEC 60068-2-2	-	Environmental testing - Part 2-2. Tests - Test B: Dry heat	EN 60068-2-2	-
IEC 60068-2-21	-	Environmental testing - Part 2-21: Tests - Test U: Robustness of terminations and integral mounting devices	-	-
IEC 60068-2-45	1980 https: d56	Basic environmental testing procedures - Part 2-48: Tests - Test XA and guidance: //immersion in cleaning solvents dards/sist/789 6a-4da7-89d6-fea41bb121e7/sist-en-iec-617	EN 60068-2-45 95702a- 60-1-	1992
+ A1	1993	2022	+ A1	1993
IEC 60068-2-58	-	Environmental testing - Part 2-58: Tests - Test Td: Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)	EN 60068-2-58	-
IEC 60191-6	-	Mechanical standardization of semiconductor devices - Part 6: General rules for the preparation of outline drawings of surface mounted semiconductor device packages	EN 60191-6	-
IEC 60194-2	-	Printed boards design, manufacture and assembly - Vocabulary - Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies	-	-
IEC 60286-3	-	Packaging of components for automatic handling - Part 3: Packaging of surface mount components on continuous tapes	EN IEC 60286-3	-

EN IEC 61760-1:2020 (E)

<u>Publication</u>	<u>Year</u>	<u>Title</u>	EN/HD	<u>Year</u>
IEC 60286-4	-	Packaging of components for automatic handling - Part 4: Stick magazines for electronic components encapsulated in packages of different forms	EN 60286-4	-
IEC 60286-5	-	Packaging of components for automatic handling - Part 5: Matrix trays	EN IEC 60286-5	-
IEC 60286-6	-	Packaging of components for automatic handling - Part 6: Bulk case packaging for surface mounting components	EN 60286-6	-
IEC 60749-20	2008	Semiconductor devices - Mechanical and climatic test methods - Part 20: Resistance of plastic encapsulated SMDs to the combined effect of moisture and soldering heat	EN 60749-20	2009
IEC 61188-6-4	-	Printed boards and printed board assemblies - Design and use - Part 6-4: Land pattern design - Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design	EN IEC 61188-6-4	-
IEC 61340-5-1	-	Electrostatics - Part 5-1: Protection of electronic devices from electrostatic phenomena - General requirements	EN 61340-5-1	-
IEC 61340-5-3	https://d56	classification for packaging intended for	EN 61340-5-3 95702a- 760-1-	-
IEC 61760-2	-	Surface mounting technology - Part 2: Transportation and storage conditions of surface mounting devices (SMD) - Application guide	-	-
IEC 61760-4	-	Surface mounting technology - Part 4: Classification, packaging, labelling and handling of moisture sensitive devices	EN 61760-4	-
IEC 62090	-	Product package labels for electronic components using bar code and twodimensional symbologies	EN 62090	-
IPC/JEDEC J-STD-020E	-	Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices	-	-
IPC/JEDEC J-STD-033B	-	Handling, Packing, Shipping and Use of Moisture/Reflow Sensitive Surface Mount Devices	-	-



IEC 61760-1

Edition 3.0 2020-07

INTERNATIONAL STANDARD

NORME INTERNATIONALE



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Surface mounting technology PREVIEW
Part 1: Standard method for the specification of surface mounting components
(SMDs) (standards.iteh.ai)

Technique du montage en surface – Partie 1: Méthode normalisée pour la spécification des composants montés en surface (CMS)

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

COMMISSION ELECTROTECHNIQUE INTERNATIONALE

ICS 31.240 ISBN 978-2-8322-8588-6

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

SURFACE MOUNTING TECHNOLOGY -

Part 1: Standard method for the specification of surface mounting components (SMDs)

FOREWORD

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International Standard IEC 61760-1 has been prepared by IEC technical committee 91: Surface mounting technology.

This third edition cancels and replaces the second edition published in 2006. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

a) inclusion of additional mounting methods: conductive glue bonding, sintering and solderless interconnection.

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The text of this International Standard is based on the following documents:

FDIS	Report on voting
91/1648/FDIS	91/1653/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 61760 series, published under the general title *Surface mounting technology*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

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INTRODUCTION

Specifications for electronic components have in the past been formulated for each component family. The regulations for environmental tests have been selected from IEC 60068 and other IEC and ISO publications. The intention for this procedure was that all components, once installed in a piece of equipment, had to satisfy certain criteria.

The introduction and increasing use of different mounting processes on one assembly make it necessary to extend the existing requirements to include those arising from processing during assembly.

Nevertheless, there existed no harmonized standard that prescribes the content of a component specification before the publication of IEC 61760-1. It is the purpose of this document to define the general requirements for component specifications derived from the assembly processes. This is done in three steps.

In the first step, general requirements for component specifications and component design related to the handling and placement of the component on the substrate are given (Clause 4). In the second step, the requirements related to assembly processes are given (Clause 5). In the third step, additional requirements resulting from specific mounting methods are given (Clauses 6 to 9).

Mixed technology boards, i.e. boards containing through-hole components and SMDs, require additional consideration with respect to the through-hole components. These may be subject to the same requirements as the SMDs. Persons responsible for drafting specifications for "non-surface mounting components" wishing to include a statement on their ability to withstand surface mounting conditions should use the classifications and tests set out in the present document.

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