

SLOVENSKI STANDARD oSIST prEN IEC 61189-2-803:2022

01-januar-2022

Preskusne metode za električne materiale, tiskana vezja in druge povezovalne strukture in sestave - 2-803. del: Metode za preskušanje raztezanja po osi Z tankih podložnih materialov

Test methods for electrical materials, printed board and other interconnection structures and assemblies - Part 2-803: Test methods for Z-Axis Expansion of base materials and printed board

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Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles - Partie 2-803: Méthodes d'essai pour la dilatation suivant l'axe Z des matériaux de base et des cartes imprimées

Ta slovenski standard je istoveten z: prEN IEC 61189-2-803:2021

ICS:

31.180 Tiskana vezja (TIV) in tiskane Printed circuits and boards plošče

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91/1760/CDV

COMMITTEE DRAFT FOR VOTE (CDV)

	PROJECT NUMBER:					
	IEC 61189-2-803 ED1					
	DATE OF CIRCULATION:	CLOSING DATE FOR VOTING:				
	2021-11-05	2022-01-28				
	SUPERSEDES DOCUMENTS: 91/1545/CD, 91/1612A/CC					

IEC TC 91 : ELECTRONICS ASSEMBLY TECHNOLOGY				
SECRETARIAT:	SECRETARY:			
Japan	Mr Masahide Okamoto			
OF INTEREST TO THE FOLLOWING COMMITTEES:	PROPOSED HORIZONTAL STANDARD:			
	Other TC/SCs are requested to indicate their interest, if any, in this CDV to the secretary.			
FUNCTIONS CONCERNED:	QUALITY ASSURANCE			
Submitted for CENELEC PARALLEL VOTING	NOT SUBMITTED FOR CENELEC PARALLEL VOTING			
Attention IEC-CENELEC parallel voting OSIST prEN IEC 6	1189-2-803:2022			
The attention of IEC National Scommittees at members and CENELEC, is drawn to the fact that this Committee Draft for Vote (CDV) is submitteed for parallel voting.	rds/sist/fa6b7bd5-3594-4c9b-9133- - iec-61189-2-803-2022			
The CENELEC members are invited to vote through the CENELEC online voting system.				

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Recipients of this document are invited to submit, with their comments, notification of any relevant patent rights of which they are aware and to provide supporting documentation.

TITLE:

Test methods for electrical materials, printed board and other interconnection structures and assemblies – Part 2-803: Test methods for Z-Axis Expansion of base materials and printed board

PROPOSED STABILITY DATE: 2027

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22	Part 2-803: Test Methods for Z-Axis Expansion of Base Materials and Printed Board						
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24			FORE	WORD			
25 26 27 28 29 30 31 32 33	1)	The International Electrotechnical electrotechnical committees (IEC questions concerning standardiza IEC publishes International Standa and Guides (hereafter referred to National Committee interested in t and non-governmental organization the International Organization for the two organizations.	Commission (IEC) is a wo National Committees). The tion in the electrical and eards, Technical Specificati as "IEC Publication(s)"). The subject dealt with may ons liaising with the IEC al Standardization (ISO) in a	orldwide organization for some object of IEC is to proper electronic fields. To this electronic fields. To this electronical Reports, F ons, Technical Reports, F Their preparation is entrus participate in this preparation so participate in this preparation accordance with condition	tandardization comprising all national mote international co-operation on all and and in addition to other activities, vublicly Available Specifications (PAS) sted to technical committees; any IEC tory work. International, governmental paration. IEC collaborates closely with ns determined by agreement between		
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56 57	International Standard IEC 61189-2-803 has been prepared by subcommittee WG10 of IEC technical committee TC91						
58	3 The text of this International Standard is based on the following documents:						
			FDIS	Report on voting			

59 Full information on the voting for the approval of this International Standard can be found in the report 60 on voting indicated in the above table.

XX/XX/RVD

XX/XX/FDIS

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The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- 66 withdrawn,
- replaced by a revised edition, or
- amended.
- 69

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TEST METHODS FOR ELECTRICAL MATERIALS, PRINTED BOARD AND OTHER INTERCONNECTION STRUCTURES AND ASSEMBLIES –

78 Part 2-803: Test methods for Z-Axis Expansion of base materials and printed board

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80 **1 Scope**

This International Standard specifies a test method to determine the Z-Axis Expansion of base materials and printed boards using a thermomechanical analyser (TMA).

84 **2 Normative References**

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

- IEC 60194-1, Printed board design, manufacture and assembly Vocabulary Part 1: Common usage
 in printed board and electronic assembly technologies
- 90 IPC-TM-650 No. 2.4.24, Glass Transition temperature and Z-Axis Thermal Expansion by TMA
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92 3 Terms and definitions

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For the purposes of this document, the terms and definitions given in IEC 60194-1 apply.

- ISO and IEC maintain terminological databases for use in standardization at the following addresses:
- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

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88651922e69d/osist-pren-iec-61189-2-803-2022

98 4 Preparation of Test Specimens

99 Unless otherwise specified, a minimum of two specimens shall be tested. These specimens shall be 100 taken from random locations of the material to be evaluated.

102 The test specimens shall be verified to be free of particles.

104 5 Test Specimens

105 Test specimens shall be unclad laminate material or a printed circuit board. Multilayer printed boards 106 may be tested but no internal conductors are to be present in the specimen.

- All Cu shall be etched from the test specimens using standard industry methods.
- The specimen shall be taken at a distance ≥ 25 mm from the edge of the material / circuit board being evaluated. The dimensions of the specimens shall be approximately 6.35mm x 6.35 mm and have a minimum thickness of 0.51mm.
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The specimen needs to lie flat on the test surface, so all edges of the specimen shall be sanded, or equivalent, to make them smooth and free of burrs. Care should be taken that this process does not induce mechanical stresses or heat the specimen.

The specimen thickness shall be measured and recorded, to allow for the percentage of thermal expansion to be determined at the completion of the test.