

## SLOVENSKI STANDARD SIST EN 61000-4-30:2015/A1:2021

01-junij-2021

Elektromagnetna združljivost (EMC) - 4-30. del: Preskusne in merilne tehnike - Metode merjenja kakovosti napetosti - Dopolnilo A1

Amendment 1: Electromagnetic compatibility (EMC) - Part 4-30: Testing and measurement techniques - Power quality measurement methods

### iTeh STANDARD PREVIEW

Partie 4-30: Techniques d'essai et de mesure Méthodes de mesure de la qualité de l'alimentation

SIST EN 61000-4-30:2015/A1:2021

Ta slovenski standard je istoveten z log/stan EN 61000-4-30:2015/A1:2021 20da08808dc9/sist-en-61000-4-30-2015-a1-2021

ICS:

33.100.01 Elektromagnetna združljivost Electromagnetic compatibility

na splošno in general

SIST EN 61000-4-30:2015/A1:2021 en

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<u>SIST EN 61000-4-30:2015/A1:2021</u> https://standards.iteh.ai/catalog/standards/sist/75f8d366-9fcf-4605-8d97-20da08808dc9/sist-en-61000-4-30-2015-a1-2021

**EUROPEAN STANDARD** NORME EUROPÉENNE

**EUROPÄISCHE NORM** 

EN 61000-4-30:2015/A1

April 2021

ICS 33.100.99

### **English Version**

Electromagnetic compatibility (EMC) - Part 4-30: Testing and measurement techniques - Power quality measurement methods (IEC 61000-4-30:2015/A1:2021)

Compatibilité électromagnétique (CEM) - Partie 4-30: Techniques d'essai et de mesure - Méthodes de mesure de la qualité de l'alimentation (IEC 61000-4-30:2015/A1:2021)

Elektromagnetische Verträglichkeit (EMV) - Teil 4-30: Prüfund Messverfahren - Verfahren zur Messung der Spannungsqualität (IEC 61000-4-30:2015/A1:2021)

This amendment A1 modifies the European Standard EN 61000-4-30:2015; it was approved by CENELEC on 2021-04-08. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this amendment the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CENELEC member.

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European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Rue de la Science 23, B-1040 Brussels

EN 61000-4-30:2015/A1:2021 (E)

### **European foreword**

The text of document 77A/1080/CDV, future IEC 61000-4-30/A1, prepared by SC 77A "EMC - Low frequency phenomena" of IEC/TC 77 "Electromagnetic compatibility" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 61000-4-30:2015/A1:2021.

The following dates are fixed:

- latest date by which the document has to be implemented at national (dop) 2022-01-08 level by publication of an identical national standard or by endorsement
- latest date by which the national standards conflicting with the document have to be withdrawn

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CENELEC shall not be held responsible for identifying any or all such patent rights.

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The text of the International Standard IEC 61000-4-30:2015/A1:2021 was approved by CENELEC as a European Standard without any modification. A R D P R F V IF W

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# INTERNATIONAL STANDARD

## NORME INTERNATIONALE



AMENDMENT 1
AMENDEMENT 1

Electromagnetic compatibility (EMC) ARD PREVIEW

Part 4-30: Testing and measurement techniques a Power quality measurement methods

SIST EN 61000-4-30:2015/A1:2021

Compatibilité électromagnétique (CEM) auts/sist/75f8d366-9fcf-4605-8d97-

Partie 4-30: Techniques d'essai et de mesure - Méthodes de mesure de la qualité de l'alimentation

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### **FOREWORD**

This amendment has been prepared by subcommittee 77A:EMC – Low frequency phenomena, of IEC technical committee 77:Electromagnetic compatibility.

The text of this amendment is based on the following documents:

CDV	Report on voting		
77A/1080/CDV	77A/1092/RVC		

Full information on the voting for the approval of this amendment can be found in the report on voting indicated in the above table.

The committee has decided that the contents of this amendment and the base publication will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

- reconfirmed,
- withdrawn,
- · replaced by a revised edition, or
- amended.

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IMPORTANT – The 'colour inside' logo on the cover page of this publication indicates that it contains colours which are considered to be useful for the correct understanding of its contents. Users should therefore print this document using a colour printer.

#### 5.7.2 Measurement uncertainty and measuring range

Replace, in the second dashed item (Class S),

"Same as Class A."

with the following:

"Same as Class A, except for the uncertainty, which shall be less than  $\pm 0.3$  % for  $u_2$  (and for  $u_0$  if it is evaluated)."

#### 5.11.2 RVC event detection

Replace, in the first dash (Class A), in the fifth paragraph, the third bullet point with the following:

• If every one of the previous  $100/120\ U_{\rm rms(1/2)}$  values, including the new value, is within the RVC threshold (including the hysteresis, if applied) of the newly calculated arithmetic mean, then the 'voltage-is-steady-state' logic signal for that channel is set to true; otherwise, it is set to false.

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Replace, in the first dash (Class A), in the fifth paragraph, the fifth, sixth and seventh bullet points with the following:

- An RVC event on a given voltage channel begins when the 'voltage-is-steady-state' logical signal of that channel changes from true to false.
- When an RVC event begins on a given voltage channel, the RVC hysteresis is applied to the RVC threshold of that channel, and changes to the voltage-is-steady-state logic of that voltage channel are disabled for 100/120 half cycles.
- An RVC event ends on a given voltage channel when the 'voltage-is-steady-state' logic signal of that channel changes from false to true. When the RVC event ends, the RVC hysteresis of that channel is removed from the RVC threshold. The time stamp of the end of the RVC event for that channel is 100/120 half cycles prior to the logic signal changing from false to true.

Figure 6 – RVC event: example of a change in r.m.s voltage that results in an RVC event

Replace Figure 6 with the following new figure:

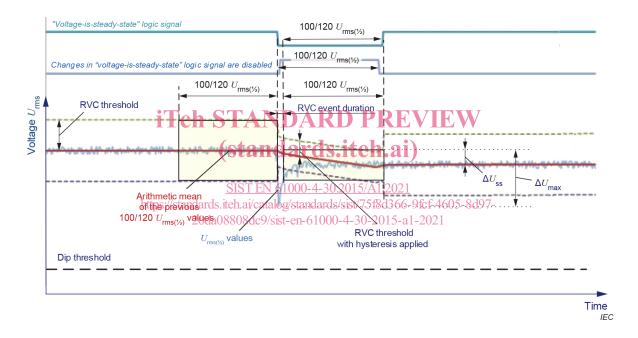


Table 1 – Summary of requirements (see subclauses for actual requirements)

### Replace the row for 5.13:

5.13 Current	А	See 5.13.2. Crest factor of 3 is required	±1 %	10 % FS to 150 % FS	N/A	N/R
	S	N/R	N/R	N/R	N/A	N/R

### with the following new row:

5.13 Current	Α	See 5.13.2. Crest factor of 3	±1 %	10 % FS to 100 % FS	N/A	N/R
		is required				