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## Information technology — High efficiency coding and media delivery in heterogeneous environments —

### Part 2: High efficiency video coding

iTeh STANDARD REVIEW 10 still picture  
(standards.iteh.ai)

*Technologies de l'information — Codage à haute efficacité et livraison des médias dans des environnements hétérogènes —*  
ISO/IEC 23008-2:2017/Amd.2:2018  
<https://standards.iteh.ai/catalog/standards/sist/a640131e-8782-4e7a-80ad-ecfb7eba9170> Partie 2: Codage vidéo à haute efficacité

*AMENDEMENT 2: Profil main 10 pour image fixe*



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# Information technology — High efficiency coding and media delivery in heterogeneous environments —

## Part 2: High efficiency video coding

### AMENDMENT 2: Main 10 still picture profile

*Clause 3*

Replace 3.56 with the following:

**3.56**

#### **frequency index**

one-dimensional or two-dimensional index associated with a *transform coefficient* prior to the application of a *transform* in the *decoding process*

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Delete 3.70, and renumber the current 3.71 through 3.164 as 3.70 through 3.163.  
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Add the following as 3.164:  
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**3.164**

#### **transform**

part of the *decoding process* by which a *block* of *transform coefficients* is converted to a *block* of spatial-domain values

Replace 3.165 with the following:

**3.165**

#### **transform block**

rectangular MxN *block* of samples resulting from the same *transform* in the *decoding process*

Replace 3.166 with the following:

**3.166**

#### **transform coefficient**

scalar quantity, considered to be in a frequency domain that is associated with a particular one-dimensional or two-dimensional *frequency index* in a *transform* in the *decoding process*

*Clause 7*

Replace 7.3.3 with the following:

**7.3.3 Profile, tier and level syntax**

profile_tier_level( profilePresentFlag, maxNumSubLayersMinus1 ) {	Descriptor
if(profilePresentFlag) {	
<b>general_profile_space</b>	u(2)
<b>general_tier_flag</b>	u(1)
<b>general_profile_idc</b>	u(5)
for(j = 0; j < 32; j++)	
<b>general_profile_compatibility_flag[ j ]</b>	u(1)
<b>general_progressive_source_flag</b>	u(1)
<b>general_interlaced_source_flag</b>	u(1)
<b>general_non_packed_constraint_flag</b>	u(1)
<b>general_frame_only_constraint_flag</b>	u(1)
if(general_profile_idc == 4    general_profile_compatibility_flag[ 4 ]	
general_profile_idc == 5    general_profile_compatibility_flag[ 5 ]	
general_profile_idc == 6    general_profile_compatibility_flag[ 6 ]	
general_profile_idc == 7    general_profile_compatibility_flag[ 7 ]	
general_profile_idc == 8    general_profile_compatibility_flag[ 8 ]	
general_profile_idc == 9    general_profile_compatibility_flag[ 9 ]	
general_profile_idc == 10    general_profile_compatibility_flag[ 10 ] ) {	
/* The number of bits in this syntax structure is not affected by this condition */	
<b>general_max_12bit_constraint_flag</b>	u(1)
<b>general_max_10bit_constraint_flag</b>	u(1)
<b>general_max_8bit_constraint_flag</b>	u(1)
<b>general_max_422chroma_constraint_flag</b>	u(1)
<b>general_max_420chroma_constraint_flag</b>	u(1)
<b>general_max_monochrome_constraint_flag</b>	u(1)
<b>general_intra_constraint_flag</b>	u(1)
<b>general_one_picture_only_constraint_flag</b>	u(1)
<b>general_lower_bit_rate_constraint_flag</b>	u(1)
if(general_profile_idc == 5    general_profile_compatibility_flag[ 5 ]	
general_profile_idc == 9    general_profile_compatibility_flag[ 9 ]	
general_profile_idc == 10    general_profile_compatibility_flag[ 10 ] ) {	
<b>general_max_14bit_constraint_flag</b>	u(1)
<b>general_reserved_zero_33bits</b>	u(33)
} else	
<b>general_reserved_zero_34bits</b>	u(34)
} else if(general_profile_idc == 2    general_profile_compatibility_flag[ 2 ] ) {	
<b>general_reserved_zero_7bits</b>	u(7)
<b>general_one_picture_only_constraint_flag</b>	u(1)
<b>general_reserved_zero_35bits</b>	u(35)
} else	
<b>general_reserved_zero_43bits</b>	u(43)

if((general_profile_idc >= 1 && general_profile_idc <= 5)    general_profile_idc == 9    general_profile_compatibility_flag[ 1 ]    general_profile_compatibility_flag[ 2 ]     general_profile_compatibility_flag[ 3 ]    general_profile_compatibility_flag[ 4 ]     general_profile_compatibility_flag[ 5 ]    general_profile_compatibility_flag[ 9 ]) /* The number of bits in this syntax structure is not affected by this condition */	
<b>general_inbld_flag</b>	u(1)
else	
<b>general_reserved_zero_bit</b>	u(1)
}	
<b>general_level_idc</b>	u(8)
for(i = 0; i < maxNumSubLayersMinus1; i++) {	
<b>sub_layer_profile_present_flag[ i ]</b>	u(1)
<b>sub_layer_level_present_flag[ i ]</b>	u(1)
}	
if(maxNumSubLayersMinus1 > 0)	
for(i = maxNumSubLayersMinus1; i < 8; i++)	
<b>reserved_zero_2bits[ i ]</b>	u(2)
for(i = 0; i < maxNumSubLayersMinus1; i++) {	
if(sub_layer_profile_present_flag[i]) {	
<b>sub_layer_profile_space[ i ]</b>	u(2)
<b>sub_layer_tier_flag[ i ]</b>	u(1)
<b>sub_layer_profile_idc[ i ]</b>	u(5)
for(j = 0; j < 32; j++)	
<b>sub_layer_profile_compatibility_flag[ i ][ j ]</b>	ISO/IEC 23008-2:2017/Amd.2:2018 <a href="https://standards.iteh.ai/catalog/standards/sist/a640131e-8782-4e7a-80ad-000000000000/2017-03-2018">https://standards.iteh.ai/catalog/standards/sist/a640131e-8782-4e7a-80ad-000000000000/2017-03-2018</a>
<b>sub_layer_profile_compatibility_flag[ i ][ j ]</b>	u(1)
<b>sub_layer_progressive_source_flag[ i ]</b>	u(1)
<b>sub_layer_interlaced_source_flag[ i ]</b>	u(1)
<b>sub_layer_non_packed_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_frame_only_constraint_flag[ i ]</b>	u(1)
if(sub_layer_profile_idc[ i ] == 4    sub_layer_profile_compatibility_flag[ i ][ 4 ]     sub_layer_profile_idc[ i ] == 5    sub_layer_profile_compatibility_flag[ i ][ 5 ]     sub_layer_profile_idc[ i ] == 6    sub_layer_profile_compatibility_flag[ i ][ 6 ]     sub_layer_profile_idc[ i ] == 7    sub_layer_profile_compatibility_flag[ i ][ 7 ]     sub_layer_profile_idc[ i ] == 8    sub_layer_profile_compatibility_flag[ i ][ 8 ]     sub_layer_profile_idc[ i ] == 9    sub_layer_profile_compatibility_flag[ i ][ 9 ]     sub_layer_profile_idc[ i ] == 10    sub_layer_profile_compatibility_flag[ i ][ 10 ] ) {	
/* The number of bits in this syntax structure is not affected by this condition */	
<b>sub_layer_max_12bit_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_max_10bit_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_max_8bit_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_max_422chroma_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_max_420chroma_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_max_monochrome_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_intra_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_one_picture_only_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_lower_bit_rate_constraint_flag[ i ]</b>	u(1)
if(sub_layer_profile_idc[ i ] == 5    sub_layer_profile_compatibility_flag[ i ][ 5 ]) {	

<b>sub_layer_max_14bit_constraint_flag[ i ]</b>	u(1)
<b>sub_layer_reserved_zero_33bits[ i ]</b>	u(33)
<b>} else</b>	
<b>    sub_layer_reserved_zero_34bits[ i ]</b>	u(34)
<b>} else if(sub_layer_profile_idc[ i ] == 2   </b> <b>    sub_layer_profile_compatibility_flag[ i ][ 2 ]) {</b>	
<b>    sub_layer_reserved_zero_7bits[ i ]</b>	u(7)
<b>    sub_layer_one_picture_only_constraint_flag[ i ]</b>	u(1)
<b>    sub_layer_reserved_zero_35bits[ i ]</b>	u(35)
<b>} else</b>	
<b>    sub_layer_reserved_zero_43bits[ i ]</b>	u(43)
<b>if([sub_layer_profile_idc[ i ] &gt;= 1 &amp;&amp; sub_layer_profile_idc[ i ] &lt;= 5)   </b> <b>    sub_layer_profile_idc[ i ] == 9   </b> <b>    sub_layer_profile_compatibility_flag[ 1 ]   </b> <b>    sub_layer_profile_compatibility_flag[ 2 ]   </b> <b>    sub_layer_profile_compatibility_flag[ 3 ]   </b> <b>    sub_layer_profile_compatibility_flag[ 4 ]   </b> <b>    sub_layer_profile_compatibility_flag[ 5 ]   </b> <b>    sub_layer_profile_compatibility_flag[ 9 ])</b> /* The number of bits in this syntax structure is not affected by this condition */	
<b>    sub_layer_inblk_flag[ i ]</b>	u(1)
<b>    else</b>	
<b>        sub_layer_reserved_zero_bit[ i ]</b>	u(1)
<b>}</b>	
<b>if(sub_layer_level_present_flag[ i ]) ISO/IEC 23008-2:2017/Amd 2:2018</b> <b>    <a href="https://sia31e.iteh.ai/catalog/standards/sist/a640131e-8782-4e7a-80ad-ecfb7eba9abf/iso-iec-23008-2-2017-amd-2-2018">https://sia31e.iteh.ai/catalog/standards/sist/a640131e-8782-4e7a-80ad-ecfb7eba9abf/iso-iec-23008-2-2017-amd-2-2018</a></b>	
<b>    sub_layer_level_idc[ i ]</b>	u(8)
<b>}</b>	
<b>}</b>	

In 7.4.4, delete the two sentences:

“When profilePresentFlag is equal to 1 and one or more of these syntax elements are not present, their values are inferred to be equal to 0. When general\_profile\_idc is not equal to 10 and is not in the range of 4 to 7, inclusive, and general\_profile\_compatibility\_flag[ 10 ] is not equal to 1 and general\_profile\_compatibility\_flag[ j ] is not equal to 1 for any value of j in the range of 4 to 7, inclusive, the value of each of these syntax elements shall be equal to 0.”

In 7.4.4, after the semantics of general\_reserved\_zero\_34bits, add the following:

**general\_reserved\_zero\_7bits**, when present, shall be equal to 0 in bitstreams conforming to this version of this Specification. Other values for general\_reserved\_zero\_7bits are reserved for future use by ITU-T | ISO/IEC. Decoders shall ignore the value of general\_reserved\_zero\_7bits.

**general\_reserved\_zero\_35bits**, when present, shall be equal to 0 in bitstreams conforming to this version of this Specification. Other values for general\_reserved\_zero\_35bits are reserved for future use by ITU-T | ISO/IEC. Decoders shall ignore the value of general\_reserved\_zero\_35bits.

In 7.4.4, replace the paragraph specifying the semantics of `sub_layer_profile_space[ i ]`, `sub_layer_tier_flag[ i ]`, `sub_layer_profile_idc[ i ]`, etc., with the following:

The semantics of the syntax elements `sub_layer_profile_space[ i ]`, `sub_layer_tier_flag[ i ]`, `sub_layer_profile_idc[ i ]`, `sub_layer_profile_compatibility_flag[ i ][ j ]`, `sub_layer_progressive_source_flag[ i ]`, `sub_layer_interlaced_source_flag[ i ]`, `sub_layer_non_packed_constraint_flag[ i ]`, `sub_layer_frame_only_constraint_flag[ i ]`, `sub_layer_max_12bit_constraint_flag[ i ]`, `sub_layer_max_10bit_constraint_flag[ i ]`, `sub_layer_max_8bit_constraint_flag[ i ]`, `sub_layer_max_422chroma_constraint_flag[ i ]`, `sub_layer_max_420chroma_constraint_flag[ i ]`, `sub_layer_max_monochrome_constraint_flag[ i ]`, `sub_layer_intra_constraint_flag[ i ]`, `sub_layer_one_picture_only_constraint_flag[ i ]`, `sub_layer_lower_bit_rate_constraint_flag[ i ]`, `sub_layer_max_14bit_constraint_flag`, `sub_layer_reserved_zero_33bits[ i ]`, `sub_layer_reserved_zero_34bits[ i ]`, `sub_layer_reserved_zero_7bits[ i ]`, `sub_layer_reserved_zero_35bits[ i ]`, `sub_layer_reserved_zero_43bits[ i ]`, `sub_layer_inbld_flag[ i ]`, `sub_layer_reserved_zero_bit[ i ]`, and `sub_layer_level_idc[ i ]` are, apart from the specification of the inference of not present values, the same as the syntax elements `general_profile_space`, `general_tier_flag`, `general_profile_idc`, `general_profile_compatibility_flag[ j ]`, `general_progressive_source_flag`, `general_interlaced_source_flag`, `general_non_packed_constraint_flag`, `general_frame_only_constraint_flag`, `general_max_12bit_constraint_flag`, `general_max_10bit_constraint_flag`, `general_max_8bit_constraint_flag`, `general_max_422chroma_constraint_flag`, `general_max_420chroma_constraint_flag`, `general_max_monochrome_constraint_flag`, `general_intra_constraint_flag`, `general_one_picture_only_constraint_flag`, `general_lower_bit_rate_constraint_flag`, `general_max_14bit_constraint_flag`, `general_reserved_zero_33bits`, `general_reserved_zero_34bits`, **general\_reserved\_zero\_7bits**, **general\_reserved\_zero\_35bits**, `general_reserved_zero_43bits`, `general_inbld_flag`, `general_reserved_zero_bit`, and `general_level_idc`, respectively, but apply to the sub-layer representation with TemporalId equal to i.

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In 7.4.3.2.2, replace the paragraph specifying the semantics of the `extended_precision_processing_flag` syntax element with the following:

<http://standards.iteh.ai/catalog/standards/sist/a640131e-8782-4e7a-80ad-ecfb7eba9abf/iso-iec-23008-2-2017-amd-2-2018>

**extended\_precision\_processing\_flag** equal to 1 specifies that an extended dynamic range is used for transform coefficients and transform processing. **extended\_precision\_processing\_flag** equal to 0 specifies that the extended dynamic range is not used. When not present, the value of **extended\_precision\_processing\_flag** is inferred to be equal to 0.

In 7.4.2.2, replace NOTE 1 with the following:

NOTE 1 NAL unit types in the range of UNSPEC48..UNSPEC63 can be used as determined by the application. No decoding process for these values of `nal_unit_type` is specified in this Specification. Since different applications might use these NAL unit types for different purposes, it is expected that particular care would be exercised in the design of encoders that generate NAL units with these `nal_unit_type` values, and in the design of decoders that interpret the content of NAL units with these `nal_unit_type` values. This specification does not define any management for these values. These `nal_unit_type` values might only be suitable for use in contexts in which “collisions” of usage (i.e. different definitions of the meaning of the NAL unit content for the same `nal_unit_type` value) are unimportant, or not possible, or are managed – e.g. defined or managed in the controlling application or transport specification, or by controlling the environment in which bitstreams are distributed.