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TETRA Air Interface Security, Algorithms Specifications; Part 2: TETRA Encryption Algorithms, TEA Set B

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Foreword

This Technical Specification (TS) has been produced by ETSI Technical Committee TETRA and Critical Communications Evolution (TCCE).

The present document is part 2 of a multi-part deliverable covering the specifications of the TETRA standard encryption, authentication and key management algorithms, as identified below:

- Part 1: "TETRA Encryption Algorithms, TEA Set A";
- Part 2: "TETRA Encryption Algorithms, TEA Set B";**
- Part 3: "TETRA Authentication and Key Management Algorithms TAA1";
- Part 4: "TETRA Authentication and Key Management Algorithms TAA2".

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1 Scope

The present document specifies the Terrestrial Trunked Radio system (TETRA) set B encryption algorithms TEA 5, 6 and 7. These algorithms are designed to meet the requirements set out in the requirements specification for the Additional TETRA Encryption Algorithm Suite [i.2].

The TETRA Air interface security function provides mechanisms for confidentiality of control signalling and user speech and data at the air interface, authentication and key management mechanisms for the air interface and for the Inter-System Interface (ISI). TETRA Air Interface security mechanisms are described in the TETRA V+D security specification [1] and the TETRA Direct Mode security specification [2].

2 References

2.1 Normative references

References are either specific (identified by date of publication and/or edition number or version number) or non-specific. For specific references, only the cited version applies. For non-specific references, the latest version of the referenced document (including any amendments) applies.

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The following referenced documents are necessary for the application of the present document:

- [1] [ETSI TS 100 392-7](#): "Terrestrial Trunked Radio (TETRA); Voice plus Data (V+D); Part 7: Security".
- [2] [ETSI TS 100 396-6](#): "Terrestrial Trunked Radio (TETRA); Direct Mode Operation (DMO); Part 6: Security".

2.2 Informative references

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The following referenced documents are not necessary for the application of the present document but they assist the user with regard to a particular subject area.

- [i.1] [Daemen, J. and Rijmen, V. \(1999\)](#): "AES proposal: Rijndael", document version 2". Submission to NIST AES competition (1999).
- [i.2] ETSI TCCE(21)000002r2: "Requirements Specification for the Additional TETRA Encryption Algorithm Suite".

3 Definition of terms, symbols and abbreviations

3.1 Terms

For the purposes of the present document, the following terms apply:

Cipher Key (CK): value that is used to determine the transformation of plain text to cipher text in a cryptographic algorithm

Initialization Vector (IV): sequence of symbols that randomize the KSG inside the encryption unit

key stream: pseudo random stream of symbols that is generated by a KSG for encipherment and decipherment

Key Stream Generator (KSG): cryptographic algorithm which produces a stream of binary digits, which can be used for encipherment and decipherment

NOTE: The initial state of the KSG is determined by the IV value.

Key Stream Segment (KSS): key stream of arbitrary length

LENGTH: required length of the key stream in bits

TEA set A: set of air interface encryption algorithms comprising TEA1, TEA2, TEA3 and TEA4

TEA set B: set of air interface encryption algorithms comprising TEA5, TEA6 and TEA7

TETRA algorithm: mathematical description of a cryptographic process used for either of the security processes authentication or encryption

3.2 Symbols

Void.

3.3 Abbreviations

For the purposes of the present document, the following abbreviations apply:

CK	Cipher Key
CKM	Mode Key
GF	Galois Field
IV	Initialization Vector
IVM	Mode IV
IVX	eXpanded IV
KSS	Key Stream Segment
ISI	Inter System Interface
KSG	Key Stream Generator

4 TEA encryption Set B Algorithm specifications

4.1 Input and Output Parameters

As specified in clause 8.3 of [i.2], the input parameters to the algorithm are:

- an initialization vector IV consisting of 80 bits IV[0], ..., IV[79];
- a cipher key CK consisting of 192 bits CK[0], ..., CK[191];

- the required length LENGTH of the key stream in bits. This can, according to [i.2], take any value from 1 up to 8 288. However, the design of the algorithm allows it to deliver, securely, a length of keystream up to 2^{40} bits, making it potentially suitable for future applications where an increased length of KSS output is required.

The corresponding output from the algorithm is then a key stream segment KSS consisting of LENGTH bits KSS[0], ..., KSS[LENGTH-1].

5 TEA5 - Specification of the Algorithm

5.1 Introduction

In outline, the algorithm operates as follows:

- the 80 bits of initialization vector IV, considered as 10 elements of the Galois field $GF(2^8)$, are mixed using a 10-stage linear recursion over $GF(2^8)$ to give 24 bytes which form a 192-bit mixed initialization vector IVX;
- the cipher key CK and mixed initialization vector IVX are combined to produce a 192-bit Mode Key, CKM, and a 192-bit Mode IV, IVM;
- successive 256-bit blocks are formed as a concatenation Mode IV || 'T', 'E', 'A', '5' || counter, where the byte values 'T', 'E', 'A', '5' code the name of the algorithm in ASCII, and the 32-bit counter takes successive values 0, 1, ...;
- these successive 256-blocks are encrypted using the variant of Rijndael [i.1] with parameters giving a block length of 256 bits and key length 192 bits. The Mode Key is used as the Rijndael key. The 256-bit blocks obtained as a result of these Rijndael encryptions are concatenated to form KSS; some bits will be discarded from the final ciphertext block if LENGTH is not exactly divisible by 256.

The algorithm is specified precisely in clauses 5.2 to 5.6.

5.2 TEA5 IV Expansion

The 80-bit IV is expanded to a 192-bit mixed IV, IVX, as follows:

- from the initialization vector bits IV[0], ..., IV[79], form 10 bytes b[0], ..., b[9], where $b[i] = 2^7 IV[8 \times i] + 2^6 IV[8 \times i + 1] + \dots + IV[8 \times i + 7]$, for $i = 0, \dots, 9$;
- for any bits B[0], ..., B[7], the byte is identified as $2^7 B[0] + 2^6 B[1] + \dots + B[7]$ with the element $z^7 B[0] + z^6 B[1] + \dots + B[7]$ of $GF(2^8)$, where z is a generator of $GF(2^8)$ satisfying the Rijndael polynomial $x^8 + x^4 + x^3 + x + 1$ in $GF(2)[x]$;
- for $i = 10, \dots, 43$, a byte b[i] is obtained from bytes b[i-1], ..., b[i-10] according to the rule $b[i] = b[i-10] \oplus b[i-9] \oplus (z^7 + z^6 + z^4 + z^2 + z + 1) b[i-1]$. The byte $(z^7 + z^6 + z^4 + z^2 + z + 1) b[i-1]$ can be obtained from b[i-1] using the lookup table defined in clause 5.5;
- the 24 bytes b[20], b[21], ..., b[43] contain the bits IVX[0], ..., IVX[191], where $b[20+i] = 2^7 IVX[8i] + 2^6 IVX[8i + 1] + \dots + IVX[8i + 7]$ for $i = 0, \dots, 23$.

This process is illustrated in figures 1 and 2 below.

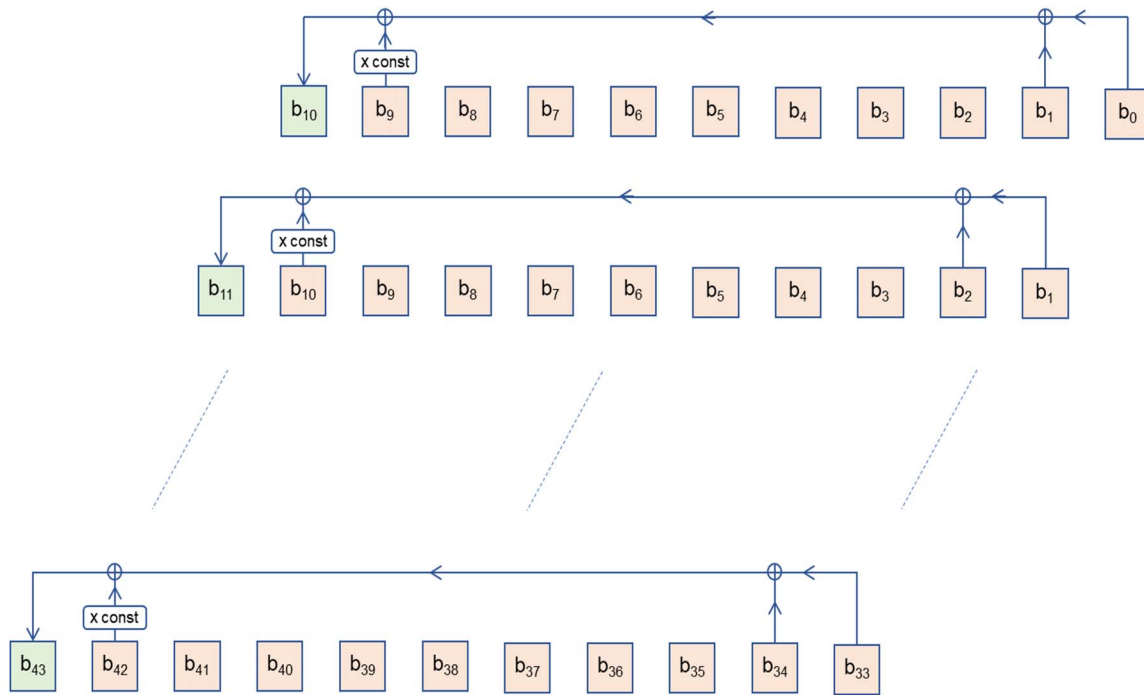


Figure 1: IV expansion

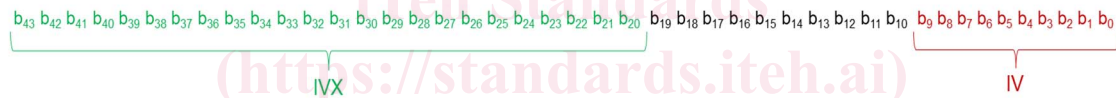


Figure 2: IVX extraction

5.3 TEA5 Derivation of the Mode Key and Mode IV

The Mode Key consists of 192 bits CKM[0], ..., CKM[191] and the Mode IV consists of 192 bits IVM[0], ..., IVM[191]. An 8-bit to 8-bit combining function f is applied to successive 8-bit inputs formed from 4 bits of the cipher key CK and 4 bits from the mixed initialization vector IVX, and the result is taken to be a further 4 bits of Mode Key and 4 bits of Mode IV. More precisely, for each i in the range 0, ..., 47,

$$2^7 \text{CKM}[4i] + 2^6 \text{CKM}[4i+1] + \dots + 2^4 \text{CKM}[4i+3] + 2^3 \text{IVM}[4i] + 2^2 \text{IVM}[4i+1] + \dots + \text{IVM}[4i+3] = f(2^7 \text{CK}[4i] + 2^6 \text{CK}[4i+1] + \dots + 2^4 \text{CK}[4i+3] + 2^3 \text{IVX}[4i] + 2^2 \text{IVX}[4i+1] + \dots + \text{IVX}[4i+3])$$

The combining function f is defined in clause 5.6.

The process for deriving the Mode Key and Mode IV from the cipher key CK and mixed initialization vector IVX is illustrated in figure 3 below.

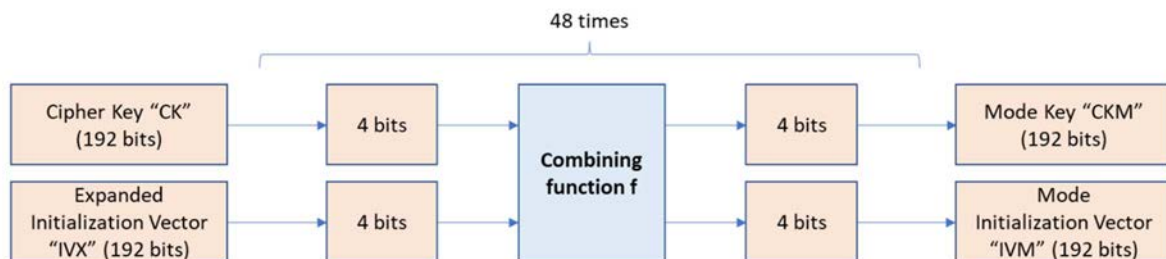


Figure 3: Mode Key and Mode IV derivation

5.4 TEA5 Derivation of the Keystream Bits

The keystream bits KSS are obtained using Rijndael [i.1] with key length 192 bits and block size 256 bits, used in a counter mode. The key used is the Mode Key CKM, arranged into bytes $2^7\text{CKM}[8i] + 2^6\text{CKM}[8i+1] + \dots + \text{CKM}[8i+7]$ for $i = 0, \dots, 23$. The Rijndael algorithm is run in encryption mode to encrypt $\lceil \text{LENGTH}/256 \rceil$ successive plaintext blocks, where the notation $\lceil \text{LENGTH}/256 \rceil$ denotes the least integer $\geq$ the floating-point quotient $\text{LENGTH}/256$. The plaintext for encryption j , for $j = 0, \dots, \lceil \text{LENGTH}/256 \rceil - 1$, is, informally, IVM || 'T', 'E', 'A', '5' || j , where the byte values 'T', 'E', 'A', '5' code the name of the algorithm in ASCII, and j is coded as 4 bytes; more precisely, it is the 32-byte sequence $p_0, \dots, p_{31}$, where:

- $p_i = 2^7\text{IVM}[8i] + 2^6\text{IVM}[8i+1] + \dots + \text{IVM}[8i+7]$ for $i = 0, \dots, 23$;
- $p_{24} = 84, p_{25} = 69, p_{26} = 65, p_{27} = 53$ (those four values being in decimal);
- $2^{24}p_{28} + 2^{16}p_{29} + 2^8p_{30} + p_{31} = j$.

The keystream bit KSS[i] is the bit $C_s[t]$, which is written as:

- $i = 256r + 8s + t$, for $0 \leq s \leq 31$ and $0 \leq t \leq 7$;
- $c_0, \dots, c_{31}$ are the ciphertext bytes obtained from the encryption where $j = r$;
- $c_s = 2^7C_s[0] + 2^6C_s[1] + \dots + C_s[7]$, for bits $C_s[0], \dots, C_s[7]$.

Note that if $(\text{LENGTH} \bmod 256) \leq 248$ then one or more higher numbered ciphertext bytes from the last block will be discarded. If $(\text{LENGTH} \bmod 8) > 0$ then one or more less significant bits from the last used ciphertext byte will be discarded.

Note that the maximum value of LENGTH, the number of bits of required keystream, is 8 288, according to the specification [i.2]. Since this maximum number of required keystream bits $\leq 2^{16}$, the 32-bit counter j can be implemented as an 8-bit counter with the other three bytes fixed to zero. If, in a future application, the maximum number of bits of required keystream is no more than 2^{24} bits, then the 32-bit counter can be implemented as a 16-bit counter with the other two bytes fixed to zero. If a full range of values for the 32-bit counter is implemented, keystream sequences of length up to 2^{40} can be generated.

The use of Rijndael in counter mode to produce keystream bits is shown in figure 4 below.

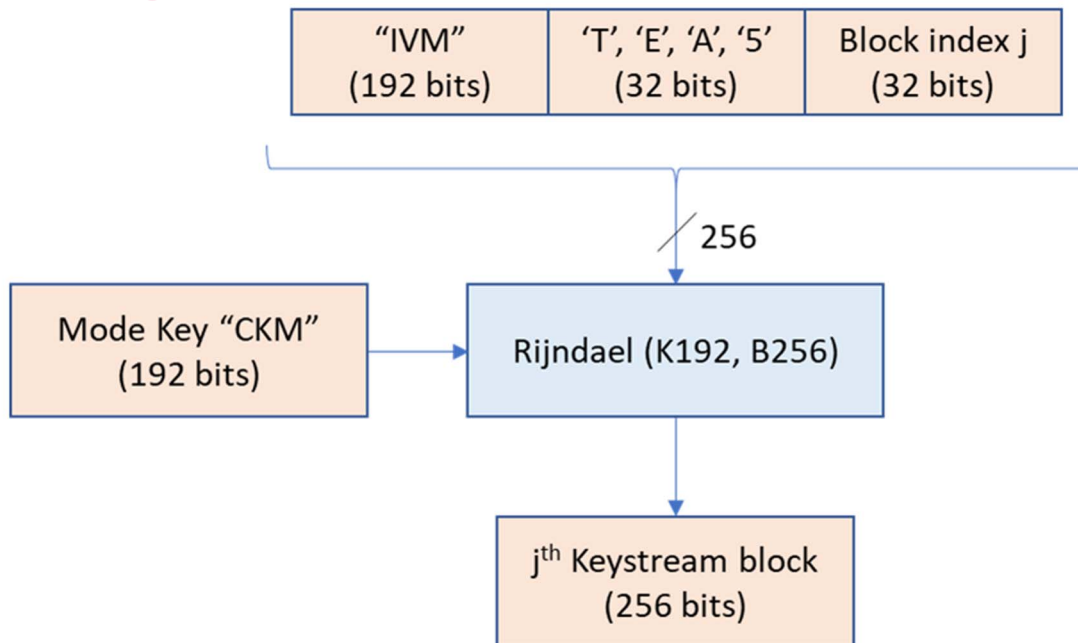


Figure 4: Keystream generation

5.5 TEA5 - Lookup Table for IV Mixing

Table 1 implements Galois Field multiplication by $z^7 + z^6 + z^4 + z^2 + z + 1$, as discussed in clause 5.2. Different rows correspond to different values of the most significant 4 bits of the input, and columns to the least significant 4 bits. For example, the value corresponding to 0x12 is found in the row labelled 0x1? and column labelled 0x2?, and is the byte value 0x6a.

Table 1

	0x?0	0x?1	0x?2	0x?3	0x?4	0x?5	0x?6	0x?7	0x?8	0x?9	0x?a	0x?b	0x?c	0x?d	0x?e	0x?f
0x0?	0x00	0xd7	0xb5	0x62	0x71	0xa6	0xc4	0x13	0xe2	0x35	0x57	0x80	0x93	0x44	0x26	0xf1
0x1?	0xdf	0x08	0x6a	0xbd	0xae	0x79	0x1b	0xcc	0x3d	0xea	0x88	0x5f	0x4c	0x9b	0xf9	0x2e
0x2?	0xa5	0x72	0x10	0xc7	0xd4	0x03	0x61	0xb6	0x47	0x90	0xf2	0x25	0x36	0xe1	0x83	0x54
0x3?	0x7a	0xad	0xcf	0x18	0x0b	0xdc	0xbe	0x69	0x98	0x4f	0x2d	0xfa	0xe9	0x3e	0x5c	0x8b
0x4?	0x51	0x86	0xe4	0x33	0x20	0xf7	0x95	0x42	0xb3	0x64	0x06	0xd1	0xc2	0x15	0x77	0xa0
0x5?	0x8e	0x59	0x3b	0xec	0xff	0x28	0x4a	0x9d	0x6c	0xbb	0xd9	0x0e	0x1d	0xca	0xa8	0x7f
0x6?	0xf4	0x23	0x41	0x96	0x85	0x52	0x30	0xe7	0x16	0xc1	0xa3	0x74	0x67	0xb0	0xd2	0x05
0x7?	0x2b	0xfc	0x9e	0x49	0x5a	0x8d	0xef	0x38	0xc9	0x1e	0x7c	0xab	0xb8	0x6f	0x0d	0xda
0x8?	0xa2	0x75	0x17	0xc0	0xd3	0x04	0x66	0xb1	0x40	0x97	0xf5	0x22	0x31	0xe6	0x84	0x53
0x9?	0x7d	0xaa	0xc8	0x1f	0x0c	0xdb	0xb9	0x6e	0x9f	0x48	0x2a	0xfd	0xee	0x39	0x5b	0x8c
0xa?	0x07	0xd0	0xb2	0x65	0x76	0xa1	0xc3	0x14	0xe5	0x32	0x50	0x87	0x94	0x43	0x21	0xf6
0xb?	0xd8	0x0f	0x6d	0xba	0xa9	0x7e	0x1c	0xcb	0x3a	0xed	0x8f	0x58	0x4b	0x9c	0xfe	0x29
0xc?	0xf3	0x24	0x46	0x91	0x82	0x55	0x37	0xe0	0x11	0xc6	0xa4	0x73	0x60	0xb7	0xd5	0x02
0xd?	0x2c	0xfb	0x99	0x4e	0x5d	0x8a	0xe8	0x3f	0xce	0x19	0x7b	0xac	0xbf	0x68	0x0a	0xdd
0xe?	0x56	0x81	0xe3	0x34	0x27	0xf0	0x92	0x45	0xb4	0x63	0x01	0xd6	0xc5	0x12	0x70	0xa7
0xf?	0x89	0x5e	0x3c	0xeb	0xf8	0x2f	0x4d	0x9a	0x6b	0xbc	0xde	0x09	0x1a	0xcd	0xaf	0x78

5.6 TEA5 - Definition of the Combining Function f

Table 2 defines the combining function f, which is used as defined in clause 5.3. Different rows correspond to different values of the most significant 4 bits of the input, and columns to the least significant 4 bits. For example, the value corresponding to 0x12 is found in the row labelled 0x1? and column labelled 0x2?, and is the byte value 0xcc.

Table 2

	0x?0	0x?1	0x?2	0x?3	0x?4	0x?5	0x?6	0x?7	0x?8	0x?9	0x?a	0x?b	0x?c	0x?d	0x?e	0x?f
0x0?	0x91	0x64	0x2c	0xc3	0x80	0xd8	0x32	0x5e	0x16	0xe7	0x09	0xbd	0x4f	0xa5	0xfa	0x7b
0x1?	0xbb	0x41	0xcc	0x67	0x36	0xe3	0x7d	0xa9	0x8e	0x52	0xf0	0xd4	0x28	0x1f	0x9a	0x05
0x2?	0xaf	0x92	0x78	0x33	0x4e	0xb6	0x8d	0xc7	0xd5	0xf9	0x11	0x60	0xec	0x04	0x5a	0x2b
0x3?	0x7c	0xd1	0x6f	0x57	0xa6	0x10	0xb9	0x25	0x43	0x0d	0x3b	0x9e	0xf8	0xe4	0x82	0xca
0x4?	0x5c	0x8a	0xe9	0x0e	0xb8	0xa2	0x66	0xf3	0x34	0x15	0x70	0x47	0x9f	0xcd	0x21	0xdb
0x5?	0x4c	0xb5	0xf1	0xe2	0x7f	0xce	0x90	0x1a	0x63	0x88	0xd6	0x2d	0x07	0x39	0xab	0x54
0x6?	0x2f	0x1d	0x89	0xf6	0xe1	0x0c	0xae	0xb3	0x97	0x45	0xc8	0x3a	0x74	0x50	0xd2	0x6b
0x7?	0x3e	0x01	0xdd	0x20	0xcf	0x62	0x1c	0xe8	0xba	0x76	0x55	0xa3	0x87	0x99	0x44	0xfb
0x8?	0x8f	0xee	0x13	0x7a	0xf5	0x49	0xc0	0xd7	0x08	0x3d	0xa4	0x5b	0x61	0x26	0xb2	0x9c
0x9?	0xdf	0x3c	0xa8	0x94	0x27	0x73	0x0a	0x8b	0x51	0xc9	0x65	0xe6	0xb0	0xfd	0x1e	0x42
0xa?	0x1b	0xfe	0x37	0xa1	0xd0	0x23	0xea	0x9d	0x72	0x6c	0xbf	0xc4	0x59	0x48	0x06	0x85
0xb?	0xe5	0x24	0x98	0xd3	0x5d	0x81	0xfc	0x69	0xc6	0xa0	0x4a	0x0b	0x12	0xb7	0x7e	0x3f
0xc?	0xff	0xc2	0x00	0x84	0x93	0x58	0x46	0x75	0xa7	0x2e	0xeb	0x19	0xda	0x6d	0x31	0xbc
0xd?	0x0f	0x79	0x56	0x40	0x14	0xf7	0x22	0x35	0xed	0xbe	0x9b	0x83	0xc1	0xdc	0x68	0xaa
0xe?	0xc5	0xad	0x4b	0xb1	0x6e	0x96	0x53	0x02	0x2a	0xd9	0x8c	0xf4	0x30	0x77	0xef	0x18
0xf?	0x6a	0x5f	0xb4	0x17	0x03	0x38	0xde	0x4d	0xf2	0x95	0x29	0x71	0xac	0x86	0xcb	0xe0