

SLOVENSKI STANDARD oSIST prEN IEC 63378-3:2024

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Standardizacija toplotnih lastnosti pri polprevodniških ohišjih - 3. del: Simulacijski modeli toplotnih vezij diskretnih polprevodniških ohišij za prehodno analizo

Thermal standardization on semiconductor packages - Part 3: Thermal circuit simulation models of discrete semiconductor packages for transient analysis

iTeh Standards

Normalisation thermique des boîtiers de semiconducteurs - Partie 3: Modèles de simulation de circuits thermiques de boîtiers de semiconducteurs discrets pour analyse transitoire

Document Preview

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(naprave) na splošno

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47D/967/CDV

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| IEC SC 47D : SEMICONDUCTOR DEVICES PACKAGING | | | | | | | |
| SECRETARIAT: | | SECRETARY: | | | | | |
| Japan | | Mr Hiroyoshi Yoshida | | | | | |
| OF INTEREST TO THE FOLLOWING COMMITTEES: TC 91 | | PROPOSED HORIZONTAL STANDARD: | | | | | |
| | | Other TC/SCs are requested to indicate their interest, if any, in this CDV to the secretary. | | | | | |
| FUNCTIONS CONCERNED: | | | | | | | |
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| The CENELEC members are invited to CENELEC online voting system. | | EC 63378-3:202 564-4cfd-b332- | <u>24</u> -78cefad77780/osist-pren-iec-61 | | | | |
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INTERNATIONAL ELECTROTECHNICAL COMMISSION

Part 3: Thermal circuit simulation models of discrete semiconductor packages for transient analysis

THERMAL STANDARDIZATION ON SEMICONDUCTOR PACKAGES -

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| Draft | Report on voting | |
|------------|------------------|--|
| XX/XX/FDIS | XX/XX/RVD | |

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