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Transmission and Multiplexing (TM); Generic requirements of transport functionality of equipment; Part 2-2: Synchronous Digital Hierarchy (SDH) and Plesiochronous Digital Hierarchy (PDH) physical section layer functions; Implementation Conformance Statement (ICS) proforma specification

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EN 300 417-2-2 V1.1.3 (1999-02)

European Standard (Telecommunications series)

**Transmission and Multiplexing (TM);
Generic requirements of transport functionality of equipment;
Part 2-2: Synchronous Digital Hierarchy (SDH) and
Plesiochronous Digital Hierarchy (PDH)
physical section layer functions;
Implementation Conformance Statement (ICS)
proforma specification**

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Foreword

This European Standard (Telecommunications series) has been produced by ETSI Technical Committee Transmission and Multiplexing (TM).

The present document is one of a family of documents that has been produced in order to provide inter-vendor and inter-operator compatibility of Synchronous Digital Hierarchy (SDH) equipment.

The present document is part 2-2 of a multi-part EN covering the Generic requirements of transport functionality of equipment, as identified below:

Part 1-1: "Generic processes and performance".

Part 1-2: "General information about Implementation Conformance Statement (ICS) proforma".

Part 1-3: "Generic processes and performance; Abstract Test Suite (ATS)".

Part 2-1: "Synchronous Digital Hierarchy (SDH) and Plesiochronous Digital Hierarchy (PDH) physical section layer functions".

Part 2-2: "Synchronous Digital Hierarchy (SDH) and Plesiochronous Digital Hierarchy (PDH) physical section layer functions; Implementation Conformance Statement (ICS) proforma specification".

Part 2-3: "Synchronous Digital Hierarchy (SDH) and Plesiochronous Digital Hierarchy (PDH) physical section layer functions; Abstract Test Suite (ATS)".

Part 3-1: "Synchronous Transport Module-N (STM-N) regenerator and multiplex section layer functions".

Part 3-2: "Synchronous Transport Module-N (STM-N) regenerator and multiplex section layer functions; Implementation Conformance Statement (ICS) proforma specification".

Part 3-3: "Synchronous Transport Module-N (STM-N) regenerator and multiplex section layer functions; Abstract Test Suite (ATS)".

Part 4-1: "Synchronous Digital Hierarchy (SDH) path layer functions".

Part 4-2: "Synchronous Digital Hierarchy (SDH) path layer functions; Implementation Conformance Statement (ICS) proforma specification".

Part 4-3: "Synchronous Digital Hierarchy (SDH) path layer functions; Abstract Test Suite (ATS)".

Part 5-1: "Plesiochronous Digital Hierarchy (PDH) path layer functions".

Part 5-2: "Plesiochronous Digital Hierarchy (PDH) path layer functions; Implementation Conformance Statement (ICS) proforma specification".

Part 5-3: "Plesiochronous Digital Hierarchy (PDH) path layer functions; Abstract Test Suite (ATS)".

Part 6-1: "Synchronization layer functions".

Part 6-2: "Synchronization layer functions; Implementation Conformance Statement (ICS) proforma specification".

Part 6-3: "Synchronization layer functions; Abstract Test Suite (ATS)".

Part 7-1: "Auxiliary layer functions".

Part 7-2: "Auxiliary layer functions; Implementation Conformance Statement (ICS) proforma specification".

Part 7-3: "Auxiliary layer functions; Abstract Test Suite (ATS)".

Parts 2 to 7 specify the layers and their atomic functions.

NOTE 1: The present document does not currently address configuration management.

NOTE 2: The SDH radio equipment functional blocks are addressed by ETSI WG TM4.

Various of the above parts have previously been published as parts of ETS 300 417.

They have been converted to parts of EN 300 417 without technical changes, but some editorial changes have been necessary (e.g. references). In particular:

- Parts 2-1 and 3-2 have been modified to take account of editorial errors present in edition 1.
- Part 1-1 has had its title change of to align with other parts published at a later date.

Also note that in the meantime parts 8-1, 8-2 and 8-3 have been stopped.

National transposition dates	
Date of adoption of this EN:	24 October 1997
Date of latest announcement of this EN (doa):	28 February 1998
Date of latest publication of new National Standard or endorsement of this EN (dop/e):	31 August 1998
Date of withdrawal of any conflicting National Standard (dow):	31 August 1998

Introduction

To evaluate conformance of a particular implementation, it is necessary to have a statement of which capabilities and options have been implemented for a telecommunication specification. Such a statement is called an Implementation Conformance Statement (ICS).

A client of a test laboratory who requests a conformance / approval test shall provide to the test laboratory a completed ICS proforma for each layer to be tested and a detailed system description of the implementation.

The ICS proforma is not another complete description of the related specification, but rather a compact form of its static conformance requirements, to be used by the test laboratory to identify which test shall be performed on a given implementation. Not every feature of a profile specification is contained in the related ICS proforma. For particular cases requiring specific information the ICS can refer to the appropriate clause of the related specification by means of references, notes and or comments.

The ICS proforma captures the implementation flexibility allowed by the related specification and details which option are left to the implementor, which are conditionally dependent on other option taken by the implementor.

1 Scope

The present document provides the Implementation Conformance Statement (ICS) proforma specification for the Synchronous Digital Hierarchy (SDH) equipment physical section layer functions defined in EN 300 417-2-1 [7] in compliance with the relevant requirements, and in accordance with the relevant guidance given in ISO/IEC 9646-7 [10] and ETS 300 406 [5].

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
 - For a specific reference, subsequent revisions do not apply.
 - For a non-specific reference, the latest version applies.
 - A non-specific reference to an ETS shall also be taken to refer to later versions published as an EN with the same number.
- [1] ETS 300 166 (1993): "Transmission and Multiplexing (TM); Physical and electrical characteristics of hierarchical digital interfaces for equipment using the 2 048 kbit/s - based plesiochronous or synchronous digital hierarchies".
- [2] ETS 300 167 (1993): "Transmission and Multiplexing (TM); Functional characteristics of 2 048 kbit/s interfaces".
- [3] ETS 300 232: "Transmission and Multiplexing (TM); Optical interfaces for equipments and systems relating to the Synchronous Digital Hierarchy (ITU-T Recommendation G.957 (1993) modified)".
- [4] ETS 300 337 (1993): "Transmission and Multiplexing (TM); Generic frame structures for the transport of various signals (including Asynchronous Transfer Mode (ATM) cells and Synchronous Digital Hierarchy (SDH) elements) at the CCITT Recommendation G.702 hierarchical rates of 2 048 kbit/s, 34 368 kbit/s and 139 264 kbit/s".
- [5] ETS 300 406 (1995): "Methods for Testing and Specification (MTS); Protocol and profile conformance testing specifications; Standardization methodology".
- [6] EN 300 417-1-1: "Transmission and Multiplexing (TM); Generic requirements of transport functionality of equipment; Part 1-1: Generic processes and performance".
- [7] EN 300 417-2-1: "Transmission and Multiplexing (TM); Generic requirements of transport functionality of equipment; Part 2-1: Synchronous Digital Hierarchy (SDH) and Plesiochronous Digital Hierarchy (PDH) physical section layer functions".
- [8] EN 300 417-6-1: "Transmission and Multiplexing (TM); Generic requirements of transport functionality of equipment; Part 6-1: Synchronization layer functions".
- [9] ISO/IEC 9646-1 (1994): "Information technology - Open Systems Interconnection - Conformance testing methodology and framework - Part 1: General concepts".
- [10] ISO/IEC 9646-7 (1995): "Information technology - Open Systems Interconnection - Conformance testing methodology and framework - Part 7: Implementation Conformance Statements".
- [11] ITU-T Recommendation G.652 (1993): "Characteristics of a single-mode optical fibre cable".
- [12] ITU-T Recommendation G.653 (1993): "Characteristics of a dispersion-shifted single-mode optical fibre cable".

- [13] ITU-T Recommendation G.654 (1993): "Characteristics of a 1550 nm wavelength loss-minimized single-mode optical fibre cable".
- [14] CCITT Recommendation G.706 (1991): "Frame alignment and cyclic redundancy check (CRC) procedures relating to basic frame structures defined in Recommendation G.704".
- [15] CCITT Recommendation G.703 (1991): "Physical / electrical characteristics of hierarchical digital interfaces".
- [16] CCITT Recommendation G.704 (1991): "Synchronous frame structures used at primary and secondary hierarchical levels".
- [17] CCITT Recommendation G.742 (1988): "Second order digital multiplex equipment operating at 8448 kbit/s and using positive justification".
- [18] CCITT Recommendation G.751 (1988): "Digital multiplex equipments operating at third order bit rate of 34 368 kbit/s and fourth order bit rate of 139 264 kbit/s and using positive justification".
- [19] ITU-T Recommendation G.823 (1993): "The control of jitter and wander within digital networks which are based on the 2 048 kbit/s hierarchy".
- [20] ITU-T Recommendation G.825 (1993): "The control of jitter and wander within digital networks which are based on the synchronous digital hierarchy (SDH)".
- [21] ITU-T Recommendation G.957 (1993): "Optical interfaces for equipments and systems relating to the synchronous digital hierarchy".
- [22] ITU-T Recommendation G.958 (1993): "Digital line systems based on the synchronous digital hierarchy for use on optical fibre cables".

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3 Definitions and abbreviations

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3.1 Definitions

For the purposes of the present document, the following terms and definitions apply:

- terms defined in EN 300 417-2-1 [7];
- terms defined in ISO/IEC 9646-1 [9] and in ISO/IEC 9646-7 [10].

In particular, the following terms defined in ISO/IEC 9646-1 [9] apply:

Implementation Conformance Statement (ICS): a statement made by the supplier of an implementation or system claimed to conform to a given specification, stating which capabilities have been implemented. The ICS can take several forms: protocol ICS, profile ICS, profile specific ICS, information object ICS, etc.

ICS proforma: a document, in the form of a questionnaire, which when completed for an implementation or system becomes an ICS.

3.2 Abbreviations

For the purposes of the present document, the following abbreviations apply:

A	Adaptation function
AI	Adapted Information
AIS	Alarm Indication Signal
BER	Bit Error Ratio
C	Connection function
CI	Characteristic Information
CID	Consecutive Identical Digits

CMI	Coded Mark Inversion
CP	Connection Point
D	Data
EMF	Equipment Management Function
EMS	Equipment Management System
E0	Electrical interface signal 64 kbit/s
E12	Electrical interface signal 2 048 kbit/s
E22	Electrical interface signal 8 448 kbit/s
E31	Electrical interface signal 34 368 kbit/s
E4	Electrical interface signal 139 264 kbit/s
ES1	STM-1 electrical interface signal 155 520 kbit/s
EX	EXtinction ratio
FAS	Frame Alignment Signal
FS	Frame Start
HDB3	High Density Bipolar of order 3
IF	In Frame
ICS	Implementation Conformance Statement
ID	IDentifier
IUT	Implementation Under Test
LED	Light Emitting Diode
LOF	Loss Of Frame
LOS	Loss Of Signal
MI	Management Information
MLM	Multi-Longitudinal Mode (laser)
MON	MONitored
N_B	Near-end Block
NE	Network Element
NMON	Not MONitored
OFS	Out of Frame Second
OOF	Out Of Frame state
OS	Optical Section
OS1	STM-1 Optical Section
OS4	STM-4 Optical Section
OS16	STM-16 Optical Section
P0s	64 kbit/s layer (transparent)
P12s	2 048 kbit/s PDH path layer with synchronous 125 µs frame structure according to ETS 300 167 [2]
P12x	2 048 kbit/s layer (transparent)
P22e	8 448 kbit/s PDH path layer with 4 plesiochronous 2 048 kbit/s
P22x	8 448 kbit/s layer (transparent)
P31e	34 368 kbit/s PDH path layer with 4 plesiochronous 8 448 kbit/s
P31s	34 368 kbit/s PDH path layer with synchronous 125 µs frame structure according to ETS 300 337 [4]
P31x	34 368 kbit/s layer (transparent)
P4e	139 264 kbit/s PDH path layer with 4 plesiochronous 34 368 kbit/s
P4s	139 264 kbit/s PDH path layer with synchronous 125 µs frame structure according to ETS 300 337 [4]
P4x	139 264 kbit/s layer (transparent)
PDH	Plesiochronous Digital Hierarchy
QL	Quality Level
RS	Regenerator Section
RS1	STM-1 Regenerator Section
RS4	STM-4 Regenerator Section
RS16	STM-16 Regenerator Section
SEC	SDH Equipment Clock
SCS	System Conformance Statement
SD	Synchronization Distribution layer
SDH	Synchronous Digital Hierarchy
Sk	Sink
SLM	Single-Longitudinal Mode (laser)

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