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Part 4-2:

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LSB Core - X86-64 5.0

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Contents

Contents	iv
List of Tables	vi
Foreword	xv
Status of this Document	xvii
Introduction	xviii
I Introductory Elements	1
1 Scope.....	2
1.1 General	2
1.2 Module Specific Scope	2
2 References.....	3
2.1 Normative References	3
2.2 Informative References/Bibliography	5
3 Requirements	8
3.1 Relevant Libraries	8
3.2 LSB Implementation Conformance.....	8
3.3 LSB Application Conformance	9
4 Terms and Definitions.....	11
5 Documentation Conventions	13
II Executable and Linking Format (ELF)	14
6 Introduction	15
7 Low Level System Information	16
7.1 Machine Interface.....	16
7.2 Function Calling Sequence	17
7.3 Operating System Interface.....	18
7.4 Process Initialization	18
7.5 Coding Examples.....	19
7.6 C Stack Frame.....	19
7.7 Debug Information.....	19
8 Object Format	20
8.1 Introduction.....	20
8.2 ELF Header	20
8.3 Sections.....	20
8.4 Symbol Table	21
8.5 Relocation	21
9 Program Loading and Dynamic Linking	22
9.1 Introduction.....	22
9.2 Program Header.....	22
9.3 Program Loading	22
9.4 Dynamic Linking.....	22
III Base Libraries	24
10 Libraries	25
10.1 Program Interpreter/Dynamic Linker	25
10.2 Interfaces for libc	25
10.3 Data Definitions for libc	49
10.4 Interface Definitions for libc	68
10.5 Interfaces for libm	69
10.6 Data Definitions for libm	74
10.7 Interface Definitions for libm	76

10.8 Interfaces for libpthread.....	77
10.9 Data Definitions for libpthread.....	82
10.10 Interfaces for libgcc_s.....	84
10.11 Data Definitions for libgcc_s.....	84
10.12 Interface Definitions for libgcc_s.....	85
10.13 Interfaces for libdl.....	86
10.14 Data Definitions for libdl.....	86
10.15 Interfaces for libcrypt.....	87
10.16 Data Definitions for libcrypt.....	87
IV Utility Libraries.....	89
11 Libraries.....	90
11.1 Interfaces for libz.....	90
11.2 Data Definitions for libz.....	90
11.3 Interfaces for libncurses.....	91
11.4 Data Definitions for libncurses.....	91
11.5 Interfaces for libncursesw.....	91
11.6 Data Definitions for libncursesw.....	92
11.7 Interfaces for libutil.....	93
V Base Libraries.....	94
12 Libraries.....	95
12.1 Interfaces for libstdcxx.....	95
12.2 Interface Definitions for libstdcxx.....	228
VI Package Format and Installation.....	229
13 Software Installation.....	230
13.1 Package Dependencies.....	230
13.2 Package Architecture Considerations.....	230
Annex A Alphabetical Listing of Interfaces by Library.....	231
A.1 libc.....	231
A.2 libcrypt.....	247
A.3 libdl.....	247
A.4 libgcc_s.....	247
A.5 libm.....	248
A.6 libpthread.....	253
A.7 librt.....	256
A.8 libutil.....	257
Annex B GNU Free Documentation License (Informative).....	258
B.1 PREAMBLE.....	258
B.2 APPLICABILITY AND DEFINITIONS.....	258
B.3 VERBATIM COPYING.....	259
B.4 COPYING IN QUANTITY.....	259
B.5 MODIFICATIONS.....	260
B.6 COMBINING DOCUMENTS.....	262
B.7 COLLECTIONS OF DOCUMENTS.....	262
B.8 AGGREGATION WITH INDEPENDENT WORKS.....	262
B.9 TRANSLATION.....	262
B.10 TERMINATION.....	263
B.11 FUTURE REVISIONS OF THIS LICENSE.....	263
B.12 How to use this License for your documents.....	263

List of Tables

Table 2-1 Normative References	3
Table 2-2 Other References	6
Table 3-1 Standard Library Names	8
Table 7-1 Non Conforming Instructions	16
Table 8-1 ELF Special Sections	20
Table 8-2 Additional Special Sections	21
Table 10-1 libc Definition	25
Table 10-2 libc - RPC Function Interfaces	25
Table 10-3 libc - RPC Deprecated Function Interfaces	27
Table 10-4 libc - System Calls Function Interfaces	27
Table 10-5 libc - System Calls Deprecated Function Interfaces	29
Table 10-6 libc - Standard I/O Function Interfaces	29
Table 10-7 libc - Standard I/O Deprecated Function Interfaces	31
Table 10-8 Sub Macro1()	31
Table 10-9 libc - Signal Handling Function Interfaces	35
Table 10-10 libc - Signal Handling Deprecated Function Interfaces	35
Table 10-11 libc - Signal Handling Data Interfaces	35
Table 10-12 libc - Localization Functions Function Interfaces	36
Table 10-13 libc - Localization Functions Data Interfaces	36
Table 10-14 libc - Posix Spawn Option Function Interfaces	36
Table 10-15 libc - Posix Advisory Option Function Interfaces	37
Table 10-16 libc - Socket Interface Function Interfaces	37
Table 10-17 libc - Socket Interface Data Interfaces	38
Table 10-18 libc - Wide Characters Function Interfaces	38
Table 10-19 libc - String Functions Function Interfaces	39
Table 10-20 libc - String Functions Deprecated Function Interfaces	41
Table 10-21 libc - IPC Functions Function Interfaces	41
Table 10-22 libc - Regular Expressions Function Interfaces	41
Table 10-23 libc - Character Type Functions Function Interfaces	41
Table 10-24 libc - Time Manipulation Function Interfaces	42
Table 10-25 libc - Time Manipulation Data Interfaces	42
Table 10-26 libc - Terminal Interface Functions Function Interfaces	43
Table 10-27 libc - System Database Interface Function Interfaces	43
Table 10-28 libc - System Database Interface Deprecated Function Interfaces	44
Table 10-29 libc - Language Support Function Interfaces	44
Table 10-30 libc - Large File Support Function Interfaces	44
Table 10-31 libc - Large File Support Deprecated Function Interfaces	45
Table 10-32 libc - Standard Library Function Interfaces	45
Table 10-33 libc - Standard Library Deprecated Function Interfaces	48
Table 10-34 libc - Standard Library Data Interfaces	48
Table 10-35 libc - GNU Extensions for libc Function Interfaces	49
Table 10-36 libm Definition	69
Table 10-37 libm - Math Function Interfaces	70
Table 10-38 libm - Math Deprecated Function Interfaces	74
Table 10-39 libm - Math Data Interfaces	74
Table 10-40 libpthread Definition	77
Table 10-41 libpthread - Realtime Threads Function Interfaces	77
Table 10-42 libpthread - Advanced Realtime Threads Function Interfaces	78

Table 10-43 libpthread - Posix Threads Function Interfaces	78
Table 10-44 Tableibpthread - Posix Threads Deprecated Function Interfaces	80
Table 10-45 libpthread - Thread aware versions of libc interfaces Function Interfaces	80
Table 10-46 libpthread - GNU Extensions for libpthread Function Interfaces	80
Table 10-47 libpthread - System Calls Function Interfaces.....	81
Table 10-48 libpthread - Standard I/O Function Interfaces	81
Table 10-49 libpthread - Signal Handling Function Interfaces	81
Table 10-50 libpthread - Standard Library Function Interfaces	82
Table 10-51 libpthread - Socket Interface Function Interfaces	82
Table 10-52 libpthread - Terminal Interface Functions Function Interfaces	82
Table 10-53 libgcc_s Definition.....	84
Table 10-54 libgcc_s - Unwind Library Function Interfaces.....	84
Table 10-55 libdl Definition	86
Table 10-56 libdl - Dynamic Loader Function Interfaces	86
Table 10-57 libcrypt Definition	87
Table 10-58 Tableibcrypt - Encryption Function Interfaces.....	87
Table 11-1 libz Definition	90
Table 11-2 libncurses Definition	91
Table 11-3 libncursesw Definition	91
Table 11-4 libutil Definition	93
Table 11-5 libutil - Utility Functions Function Interfaces	93
Table 12-1 libstdcxx Definition.....	95
Table 12-2 libstdcxx - C++ Runtime Support Function Interfaces	95
Table 12-3 typeinfo for type_info.....	96
Table 12-4 typeinfo for _cxxabiv1::_enum_type_info.....	96
Table 12-5 typeinfo for _cxxabiv1::_array_type_info.....	97
Table 12-6 Primary vtable for _cxxabiv1::_class_type_info.....	97
Table 12-7 typeinfo for _cxxabiv1::_class_type_info.....	98
Table 12-8 libstdcxx - Class _cxxabiv1::_class_type_info Function Interfaces	98
Table 12-9 2-1 Normative References	99
Table 12-10 typeinfo for _cxxabiv1::_pointer_type_info.....	115
Table 12-11 typeinfo for _cxxabiv1::_function_type_info.....	115
Table 12-12 Primary vtable for _cxxabiv1::_si_class_type_info	116
Table 12-13 typeinfo for _cxxabiv1::_si_class_type_info.....	117
Table 12-14 libstdcxx - Class _cxxabiv1::_si_class_type_info Function Interfaces ..	117
Table 12-15 Primary vtable for _cxxabiv1::_vmi_class_type_info	117
Table 12-16 typeinfo for _cxxabiv1::_vmi_class_type_info	118
Table 12-17 libstdcxx - Class _cxxabiv1::_vmi_class_type_info Function Interfaces	118
Table 12-18 typeinfo for _cxxabiv1::_fundamental_type_info.....	119
Table 12-19 typeinfo for _cxxabiv1::_pointer_to_member_type_info	119
Table 12-20 libstdcxx - Class _gnu_cxx::_pool_alloc_base Function Interfaces	120
Table 12-21 Primary vtable for _gnu_cxx::stdio_sync_filebuf<char, char_traits<char>>	120
Table 12-22 Primary vtable for _gnu_cxx::stdio_sync_filebuf<wchar_t, char_traits<wchar_t>>	122
Table 12-23 typeinfo for exception	123
Table 12-24 typeinfo for bad_typeid	124
Table 12-25 typeinfo for logic_error.....	124

Table 12-26 typeinfo for range_error.....	124
Table 12-27 typeinfo for domain_error.....	125
Table 12-28 typeinfo for length_error.....	125
Table 12-29 typeinfo for out_of_range.....	125
Table 12-30 typeinfo for bad_exception.....	126
Table 12-31 typeinfo for runtime_error.....	126
Table 12-32 Sub Macro1()	127
Table 12-33 typeinfo for underflow_error.....	130
Table 12-34 typeinfo for invalid_argument.....	131
Table 12-35 typeinfo for bad_cast.....	131
Table 12-36 typeinfo for bad_alloc.....	131
Table 12-37 typeinfo for ctype_base.....	134
Table 12-38 libstdcxx - Class ctype<char> Function Interfaces.....	135
Table 12-39 typeinfo for ctype<wchar_t>.....	135
Table 12-40 libstdcxx - Class ctype<wchar_t> Function Interfaces.....	135
Table 12-41 typeinfo for ctype_byname<char>.....	136
Table 12-42 libstdcxx - Class ctype_byname<char> Function Interfaces.....	136
Table 12-43 typeinfo for ctype_byname<wchar_t>.....	136
Table 12-44 libstdcxx - Class ctype_byname<wchar_t> Function Interfaces.....	137
Table 12-45 libstdcxx - Class basic_string<char, char_traits<char>, allocator<char> > Function Interfaces.....	137
Table 12-46 libstdcxx - Class basic_string<wchar_t, char_traits<wchar_t>, allocator<wchar_t> > Function Interfaces.....	142
Table 12-47 Primary vtable for basic_stringstream<char, char_traits<char>, allocator<char> >.....	147
Table 12-48 Secondary vtable for basic_stringstream<char, char_traits<char>, allocator<char> >.....	148
Table 12-49 Secondary vtable for basic_stringstream<char, char_traits<char>, allocator<char> >.....	148
Table 12-50 VTT for basic_stringstream<char, char_traits<char>, allocator<char> >	148
Table 12-51 libstdcxx - Class basic_stringstream<char, char_traits<char>, allocator<char> > Function Interfaces.....	149
Table 12-52 Primary vtable for basic_stringstream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >.....	149
Table 12-53 Secondary vtable for basic_stringstream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >.....	149
Table 12-54 Secondary vtable for basic_stringstream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >.....	150
Table 12-55 VTT for basic_stringstream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >.....	150
Table 12-56 libstdcxx - Class basic_stringstream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> > Function Interfaces.....	151
Table 12-57 Primary vtable for basic_istreamstream<char, char_traits<char>, allocator<char> >.....	151
Table 12-58 Secondary vtable for basic_istreamstream<char, char_traits<char>, allocator<char> >.....	151
Table 12-59 VTT for basic_istreamstream<char, char_traits<char>, allocator<char> >	152
Table 12-60 libstdcxx - Class basic_istreamstream<char, char_traits<char>, allocator<char> >.....	152

allocator<char> > Function Interfaces.....	152
Table 12-61 Primary vtable for basic_istream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	153
Table 12-62 Secondary vtable for basic_istream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	153
Table 12-63 VTT for basic_istream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	153
Table 12-64 libstdc++ - Class basic_istream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> > Function Interfaces	154
Table 12-65 Primary vtable for basic_ostream<char, char_traits<char>, allocator<char> >	154
Table 12-66 Secondary vtable for basic_ostream<char, char_traits<char>, allocator<char> >	154
Table 12-67 VTT for basic_ostream<char, char_traits<char>, allocator<char> >	155
Table 12-68 libstdc++ - Class basic_ostream<char, char_traits<char>, allocator<char> > Function Interfaces.....	155
Table 12-69 Primary vtable for basic_ostream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	155
Table 12-70 Secondary vtable for basic_ostream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	156
Table 12-71 VTT for basic_ostream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	156
Table 12-72 libstdc++ - Class basic_ostream<wchar_t, char_traits<wchar_t>, allocator<wchar_t> > Function Interfaces	157
Table 12-73 Primary vtable for basic_stringbuf<char, char_traits<char>, allocator<char> >	157
Table 12-74 typeinfo for basic_stringbuf<char, char_traits<char>, allocator<char> >	158
Table 12-75 libstdc++ - Class basic_stringbuf<char, char_traits<char>, allocator<char> > Function Interfaces.....	158
Table 12-76 Primary vtable for basic_stringbuf<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	159
Table 12-77 typeinfo for basic_stringbuf<wchar_t, char_traits<wchar_t>, allocator<wchar_t> >	160
Table 12-78 libstdc++ - Class basic_stringbuf<wchar_t, char_traits<wchar_t>, allocator<wchar_t> > Function Interfaces	161
Table 12-79 Primary vtable for basic_istream<char, char_traits<char> >	161
Table 12-80 Secondary vtable for basic_istream<char, char_traits<char> >	161
Table 12-81 Secondary vtable for basic_istream<char, char_traits<char> >	162
Table 12-82 VTT for basic_istream<char, char_traits<char> >	162
Table 12-83 libstdc++ - Class basic_istream<char, char_traits<char> > Function Interfaces	162
Table 12-84 Primary vtable for basic_istream<wchar_t, char_traits<wchar_t> >	163
Table 12-85 Secondary vtable for basic_istream<wchar_t, char_traits<wchar_t> >	163
Table 12-86 Secondary vtable for basic_istream<wchar_t, char_traits<wchar_t> >	163
Table 12-87 VTT for basic_istream<wchar_t, char_traits<wchar_t> >	164
Table 12-88 libstdc++ - Class basic_istream<wchar_t, char_traits<wchar_t> >	

Function Interfaces.....	164
Table 12-89 Primary vtable for basic_istream<char, char_traits<char> >.....	164
Table 12-90 Secondary vtable for basic_istream<char, char_traits<char> >.....	164
Table 12-91 VTT for basic_istream<char, char_traits<char> >.....	165
Table 12-92 libstdcxx - Class basic_istream<char, char_traits<char> > Function Interfaces.....	165
Table 12-93 Primary vtable for basic_istream<wchar_t, char_traits<wchar_t> >.....	166
Table 12-94 Secondary vtable for basic_istream<wchar_t, char_traits<wchar_t> >.....	166
Table 12-95 VTT for basic_istream<wchar_t, char_traits<wchar_t> >.....	167
Table 12-96 libstdcxx - Class basic_istream<wchar_t, char_traits<wchar_t> > Function Interfaces.....	167
Table 12-97 Primary vtable for basic_ostream<char, char_traits<char> >.....	168
Table 12-98 Secondary vtable for basic_ostream<char, char_traits<char> >.....	168
Table 12-99 VTT for basic_ostream<char, char_traits<char> >.....	169
Table 12-100 libstdcxx - Class basic_ostream<char, char_traits<char> > Function Interfaces.....	169
Table 12-101 Primary vtable for basic_ostream<wchar_t, char_traits<wchar_t> >.....	169
Table 12-102 Secondary vtable for basic_ostream<wchar_t, char_traits<wchar_t> >.....	170
Table 12-103 VTT for basic_ostream<wchar_t, char_traits<wchar_t> >.....	170
Table 12-104 libstdcxx - Class basic_ostream<wchar_t, char_traits<wchar_t> > Function Interfaces.....	170
Table 12-105 Primary vtable for basic_fstream<char, char_traits<char> >.....	171
Table 12-106 Secondary vtable for basic_fstream<char, char_traits<char> >.....	171
Table 12-107 Secondary vtable for basic_fstream<char, char_traits<char> >.....	171
Table 12-108 VTT for basic_fstream<char, char_traits<char> >.....	172
Table 12-109 libstdcxx - Class basic_fstream<char, char_traits<char> > Function Interfaces.....	172
Table 12-110 Primary vtable for basic_fstream<wchar_t, char_traits<wchar_t> >.....	172
Table 12-111 Secondary vtable for basic_fstream<wchar_t, char_traits<wchar_t> >.....	173
Table 12-112 Secondary vtable for basic_fstream<wchar_t, char_traits<wchar_t> >.....	173
Table 12-113 VTT for basic_fstream<wchar_t, char_traits<wchar_t> >.....	173
Table 12-114 libstdcxx - Class basic_fstream<wchar_t, char_traits<wchar_t> > Function Interfaces.....	174
Table 12-115 Primary vtable for basic_ifstream<char, char_traits<char> >.....	174
Table 12-116 Secondary vtable for basic_ifstream<char, char_traits<char> >.....	174
Table 12-117 VTT for basic_ifstream<char, char_traits<char> >.....	175
Table 12-118 libstdcxx - Class basic_ifstream<char, char_traits<char> > Function Interfaces.....	175
Table 12-119 Primary vtable for basic_ifstream<wchar_t, char_traits<wchar_t> >.....	175
Table 12-120 Secondary vtable for basic_ifstream<wchar_t, char_traits<wchar_t> >.....	175
Table 12-121 VTT for basic_ifstream<wchar_t, char_traits<wchar_t> >.....	176
Table 12-122 libstdcxx - Class basic_ifstream<wchar_t, char_traits<wchar_t> > Function Interfaces.....	176
Table 12-123 Primary vtable for basic_ofstream<char, char_traits<char> >.....	176
Table 12-124 Secondary vtable for basic_ofstream<char, char_traits<char> >.....	177
Table 12-125 VTT for basic_ofstream<char, char_traits<char> >.....	177

Table 12-126 libstdcxx - Class basic_ofstream<char, char_traits<char> > Function Interfaces	177
Table 12-127 Primary vtable for basic_ofstream<wchar_t, char_traits<wchar_t> >	178
Table 12-128 Secondary vtable for basic_ofstream<wchar_t, char_traits<wchar_t> >	178
Table 12-129 VTT for basic_ofstream<wchar_t, char_traits<wchar_t> >	178
Table 12-130 libstdcxx - Class basic_ofstream<wchar_t, char_traits<wchar_t> > Function Interfaces	179
Table 12-131 Primary vtable for basic_streambuf<char, char_traits<char> >	179
Table 12-132 typeinfo for basic_streambuf<char, char_traits<char> >	180
Table 12-133 libstdcxx - Class basic_streambuf<char, char_traits<char> > Function Interfaces	180
Table 12-134 Primary vtable for basic_streambuf<wchar_t, char_traits<wchar_t> >	181
Table 12-135 typeinfo for basic_streambuf<wchar_t, char_traits<wchar_t> >	182
Table 12-136 libstdcxx - Class basic_streambuf<wchar_t, char_traits<wchar_t> > Function Interfaces	183
Table 12-137 Primary vtable for basic_filebuf<char, char_traits<char> >	183
Table 12-138 typeinfo for basic_filebuf<char, char_traits<char> >	184
Table 12-139 libstdcxx - Class basic_filebuf<char, char_traits<char> > Function Interfaces	184
Table 12-140 Primary vtable for basic_filebuf<wchar_t, char_traits<wchar_t> >	185
Table 12-141 typeinfo for basic_filebuf<wchar_t, char_traits<wchar_t> >	186
Table 12-142 libstdcxx - Class basic_filebuf<wchar_t, char_traits<wchar_t> > Function Interfaces	186
Table 12-143 typeinfo for ios_base	187
Table 12-144 typeinfo for basic_ios<wchar_t, char_traits<wchar_t> >	188
Table 12-145 typeinfo for ios_base::failure	188
Table 12-146 typeinfo for __timepunct<char>	189
Table 12-147 libstdcxx - Class __timepunct<char> Function Interfaces	189
Table 12-148 typeinfo for __timepunct<wchar_t>	190
Table 12-149 libstdcxx - Class __timepunct<wchar_t> Function Interfaces	190
Table 12-150 typeinfo for messages_base	190
Table 12-151 libstdcxx - Class messages<char> Function Interfaces	191
Table 12-152 libstdcxx - Class messages<wchar_t> Function Interfaces	191
Table 12-153 typeinfo for messages_byname<char>	192
Table 12-154 libstdcxx - Class messages_byname<char> Function Interfaces	192
Table 12-155 typeinfo for messages_byname<wchar_t>	192
Table 12-156 libstdcxx - Class messages_byname<wchar_t> Function Interfaces	192
Table 12-157 typeinfo for numpunct<char>	193
Table 12-158 libstdcxx - Class numpunct<char> Function Interfaces	193
Table 12-159 typeinfo for numpunct<wchar_t>	193
Table 12-160 libstdcxx - Class numpunct<wchar_t> Function Interfaces	194
Table 12-161 typeinfo for numpunct_byname<char>	194
Table 12-162 libstdcxx - Class numpunct_byname<char> Function Interfaces	194
Table 12-163 typeinfo for numpunct_byname<wchar_t>	195
Table 12-164 libstdcxx - Class numpunct_byname<wchar_t> Function Interfaces	195
Table 12-165 typeinfo for codecvt_base	196
Table 12-166 Primary vtable for codecvt<char, char, __mbstate_t>	196
Table 12-167 typeinfo for codecvt<char, char, __mbstate_t>	197

Table 12-168 libstdcxx - Class codecvt<char, char, __mbstate_t> Function Interfaces	197
Table 12-169 Primary vtable for codecvt<wchar_t, char, __mbstate_t>	198
Table 12-170 typeinfo for codecvt<wchar_t, char, __mbstate_t>	199
Table 12-171 libstdcxx - Class codecvt<wchar_t, char, __mbstate_t> Function Interfaces	199
Table 12-172 Primary vtable for codecvt_byname<char, char, __mbstate_t>	199
Table 12-173 typeinfo for codecvt_byname<char, char, __mbstate_t>	200
Table 12-174 libstdcxx - Class codecvt_byname<char, char, __mbstate_t> Function Interfaces	201
Table 12-175 Primary vtable for codecvt_byname<wchar_t, char, __mbstate_t>	201
Table 12-176 typeinfo for codecvt_byname<wchar_t, char, __mbstate_t>	202
Table 12-177 libstdcxx - Class codecvt_byname<wchar_t, char, __mbstate_t> Function Interfaces	202
Table 12-178 typeinfo for collate<char>	202
Table 12-179 libstdcxx - Class collate<char> Function Interfaces	203
Table 12-180 typeinfo for collate<wchar_t>	203
Table 12-181 libstdcxx - Class collate<wchar_t> Function Interfaces	203
Table 12-182 typeinfo for collate_byname<char>	204
Table 12-183 libstdcxx - Class collate_byname<char> Function Interfaces	204
Table 12-184 typeinfo for collate_byname<wchar_t>	204
Table 12-185 libstdcxx - Class collate_byname<wchar_t> Function Interfaces	205
Table 12-186 typeinfo for time_base	205
Table 12-187 typeinfo for time_get_byname<char, istreambuf_iterator<char, char_traits<char>>>	205
Table 12-188 libstdcxx - Class time_get_byname<char, istreambuf_iterator<char, char_traits<char>>> Function Interfaces	206
Table 12-189 typeinfo for time_get_byname<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t>>>	206
Table 12-190 libstdcxx - Class time_get_byname<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t>>> Function Interfaces	207
Table 12-191 typeinfo for time_put_byname<char, ostreambuf_iterator<char, char_traits<char>>>	207
Table 12-192 libstdcxx - Class time_put_byname<char, ostreambuf_iterator<char, char_traits<char>>> Function Interfaces	207
Table 12-193 typeinfo for time_put_byname<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t>>>	208
Table 12-194 libstdcxx - Class time_put_byname<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t>>> Function Interfaces	208
Table 12-195 libstdcxx - Class time_get<char, istreambuf_iterator<char, char_traits<char>>> Function Interfaces	209
Table 12-196 libstdcxx - Class time_get<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t>>> Function Interfaces	209
Table 12-197 typeinfo for time_put<char, ostreambuf_iterator<char, char_traits<char>>>	210
Table 12-198 libstdcxx - Class time_put<char, ostreambuf_iterator<char, char_traits<char>>> Function Interfaces	210
Table 12-199 typeinfo for time_put<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t>>>	211
Table 12-200 libstdcxx - Class time_put<wchar_t, ostreambuf_iterator<wchar_t,	

char_traits<wchar_t> > > Function Interfaces	211
Table 12-201 libstdcxx - Class moneypunct<char, false> Function Interfaces	212
Table 12-202 libstdcxx - Class moneypunct<char, true> Function Interfaces	212
Table 12-203 libstdcxx - Class moneypunct<wchar_t, false> Function Interfaces	213
Table 12-204 libstdcxx - Class moneypunct<wchar_t, true> Function Interfaces	214
Table 12-205 typeinfo for moneypunct_byname<char, false>	214
Table 12-206 libstdcxx - Class moneypunct_byname<char, false> Function Interfaces	214
Table 12-207 typeinfo for moneypunct_byname<char, true>	215
Table 12-208 libstdcxx - Class moneypunct_byname<char, true> Function Interfaces	215
Table 12-209 typeinfo for moneypunct_byname<wchar_t, false>	215
Table 12-210 libstdcxx - Class moneypunct_byname<wchar_t, false> Function Interfaces	216
Table 12-211 typeinfo for moneypunct_byname<wchar_t, true>	216
Table 12-212 libstdcxx - Class moneypunct_byname<wchar_t, true> Function Interfaces	216
Table 12-213 typeinfo for money_base	217
Table 12-214 typeinfo for money_get<char, istreambuf_iterator<char, char_traits<char> > >	217
Table 12-215 libstdcxx - Class money_get<char, istreambuf_iterator<char, char_traits<char> > > Function Interfaces	217
Table 12-216 typeinfo for money_get<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t> > >	218
Table 12-217 libstdcxx - Class money_get<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t> > > Function Interfaces	218
Table 12-218 typeinfo for money_put<char, ostreambuf_iterator<char, char_traits<char> > >	219
Table 12-219 libstdcxx - Class money_put<char, ostreambuf_iterator<char, char_traits<char> > > Function Interfaces	219
Table 12-220 typeinfo for money_put<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t> > >	219
Table 12-221 libstdcxx - Class money_put<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t> > > Function Interfaces	220
Table 12-222 libstdcxx - Class locale Function Interfaces	220
Table 12-223 typeinfo for locale::facet	220
Table 12-224 typeinfo for num_get<char, istreambuf_iterator<char, char_traits<char> > >	221
Table 12-225 libstdcxx - Class num_get<char, istreambuf_iterator<char, char_traits<char> > > Function Interfaces	222
Table 12-226 typeinfo for num_get<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t> > >	222
Table 12-227 libstdcxx - Class num_get<wchar_t, istreambuf_iterator<wchar_t, char_traits<wchar_t> > > Function Interfaces	222
Table 12-228 typeinfo for num_put<char, ostreambuf_iterator<char, char_traits<char> > >	223
Table 12-229 libstdcxx - Class num_put<char, ostreambuf_iterator<char, char_traits<char> > > Function Interfaces	223
Table 12-230 typeinfo for num_put<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t> > >	224

Table 12-231 libstdcxx - Class num_put<wchar_t, ostreambuf_iterator<wchar_t, char_traits<wchar_t>>> Function Interfaces.....	224
Table 12-232 libstdcxx - Class gsllice Function Interfaces.....	225
Table 12-233 libstdcxx - Class _basic_file<char> Function Interfaces.....	225
Table 12-234 libstdcxx - Class valarray<unsigned int> Function Interfaces.....	226
Table 12-235 libstdcxx - Class _gnu_cxx::_pool<true> Function Interfaces	226
Table 12-236 libstdcxx - Class _gnu_cxx::_pool<false> Function Interfaces.....	227
Table 12-237 libstdcxx - Class _gnu_cxx::free_list Function Interfaces	227
Table 12-238 libstdcxx - Class locale::_Impl Function Interfaces	227
Table 12-239 libstdcxx - Namespace std Functions Function Interfaces	227
Table A-1 libc Function Interfaces	231
Table A-2 libc Data Interfaces.....	247
Table A-3 libcrypt Function Interfaces	247
Table A-4 libdl Function Interfaces	247
Table A-5 libgcc_s Function Interfaces.....	247
Table A-6 libm Function Interfaces	248
Table A-7 libm Data Interfaces.....	253
Table A-8 libpthread Function Interfaces.....	253
Table A-9 librt Function Interfaces	256
Table A-10 libutil Function Interfaces	257

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This document is a direct adoption of the Linux Standard Base (LSB) 5.0 Common Definitions, issued by the Linux Foundation. The previous release of these standards, ISO/IEC 23360-1 through ISO/IEC 23360-8:2006 were international standards published under the ISO/IEC/JTC 1 Publicly Available Specification process. This document, and others in the series, are published under the GNU Free Documentation License (See Annex B).

This is version 1.0 of the Linux Standard Base (LSB) core specifications for AMD64 (X86-64) architecture. This standard replaces the core specification portion of ISO/IEC 23360-4:2006 Linux Standard Base, which is cancelled and replaced by ISO/IEC 23360-4-2 through ISO/IEC 23360-4-3. The general parts and the processor specific parts of the original Linux Standard Base are also subdivided as follows:

- The common definitions ISO/IEC 23360-1-1;
- The core specification generic part ISO/IEC 23360-1-2;
- The desktop specification generic part ISO/IEC 23360-1-3;
- The languages specification generic part ISO/IEC 23360-1-4;
- The imaging specification generic part ISO/IEC 23360-1-5;
- The Intel X86-32 architecture core and desktop specifications in ISO/IEC 23360-2-2 and ISO/IEC 23360-2-3 respectively;
- The Intel IA64 (Itanium) architecture core and desktop specification in ISO/IEC 23360-3-2 and ISO/IEC 23360-3-3 respectively;
- The AMD64 (X86-64) architecture core and desktop specification in ISO/IEC 23360-4-2 (this document) and ISO/IEC 23360-4-3 respectively;