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Linux Standard Base (LSB) —

Part 5-2:
Core specification for PowerPC 32 architecture

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ISO copyright office
CP 401 • Ch. de Blandonnet 8
CH-1214 Vernier, Geneva
Phone: +41 22 749 01 11
Fax: +41 22 749 09 47
Email: copyright@iso.org
Website: www.iso.org

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Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for worldwide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work. In the field of information technology, ISO and IEC have established a joint technical committee, ISO/IEC JTC 1.

International Standards are drafted in accordance with the rules given in the ISO/IEC Directives, Part 2.

The main task of the joint technical committee is to prepare International Standards. Draft International Standards adopted by the joint technical committee are circulated to national bodies for voting. Publication as an International Standard requires approval by at least 75 % of the national bodies casting a vote.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. ISO and IEC shall not be held responsible for identifying any or all such patent rights.

The committee responsible for this document is Joint Technical Committee ISO/IEC JTC 1, Information technology, Subcommittee SC 22, Programming languages, their environments and system software interfaces.

This document is a direct adoption of the Linux Standard Base (LSB) 5.0 Common Definitions, issued by the Linux Foundation. The previous release of these standards, ISO/IEC 23360-1 through ISO/IEC 23360-8:2006 were international standards published under the ISO/IEC/JTC 1 Publicly Available Specification process. This document, and others in the series, are published under the GNU Free Documentation License (See Annex B).

This is version 1.0 of the Linux Standard Base (LSB) core specification for the PowerPC 32 architecture. This standard replaces the core specification portion of ISO/IEC 23360-5:2006 Linux Standard Base, which is cancelled and replaced by ISO/IEC 23360-5-2 through ISO/IEC 23360-5-3. The general parts and the processor specific parts of the original Linux Standard Base are also subdivided as follows:

- The common definitions ISO/IEC 23360-1-1;
- The core specification generic part ISO/IEC 23360-1-2;
- The desktop specification generic part ISO/IEC 23360-1-3;
- The languages specification generic part ISO/IEC 23360-1-4;
- The imaging specification generic part ISO/IEC 23360-1-5;
- The Intel X86-32 architecture core and desktop specification in ISO/IEC 23360-2-2 and ISO/IEC 23360-2-3 respectively;

- The Intel IA64 (Itanium) architecture core and desktop specification in ISO/IEC 23360-3-2 and ISO/IEC 23360-3-3 respectively;
- The AMD64 (X86-64) architecture core and desktop specification in ISO/IEC 23360-4-2 and ISO/IEC 23360-4-3 respectively;
- The PowerPC 32 architecture core and desktop specification in ISO/IEC 23360-5-2 (this document) and ISO/IEC 23360-5-3 respectively;
- The PowerPC 64 architecture core and desktop specification in ISO/IEC 23360-6-2 and ISO/IEC 23360-6-3 respectively;
- The IBM S390 architecture core and desktop specification in ISO/IEC 23360-7-2 and ISO/IEC 23360-7-3 respectively; and
- The IBM S390X architecture core and desktop specification in ISO/IEC 23360-8-2 and ISO/IEC 23360-8-3 respectively.

Throughout this document, there are many clauses where the generic specification of the component being specified is in ISO/IEC 23360-1-2. To find the corresponding generic text, a search of ISO/IEC 23360-1-2 with the clause title as text will take you to the corresponding generic clause.