



SLOVENSKI STANDARD

oSIST prEN IEC 60749-21:2025

01-januar-2025

Polprevodniški elementi - Metode za mehansko in klimatsko preskušanje - 21. del: Spajkljivost

Semiconductor devices - Mechanical and climatic test methods - Part 21: Solderability

Halbleiterbauelemente - Mechanische und klimatische Prüfverfahren - Teil 21: Lötbarkeit

Dispositifs à semiconducteurs - Méthodes d'essais mécaniques et climatiques - Partie
21: Brasabilité

Ta slovenski standard je istoveten z: **prEN IEC 60749-21:2024**

[oSIST prEN IEC 60749-21:2025](https://standards.iteh.ai/catalog/standards/sist/9200bacf-a207-4e9d-bec9-edb4ccb77435/osist-pren-iec-60749-21-2025)

<https://standards.iteh.ai/catalog/standards/sist/9200bacf-a207-4e9d-bec9-edb4ccb77435/osist-pren-iec-60749-21-2025>

ICS:

31.080.01	Polprevodniški elementi (naprave) na splošno	Semiconductor devices in general
-----------	---	-------------------------------------

oSIST prEN IEC 60749-21:2025

en



47/2862/CDV

COMMITTEE DRAFT FOR VOTE (CDV)

PROJECT NUMBER:

IEC 60749-21 ED3

DATE OF CIRCULATION:

2024-09-06

CLOSING DATE FOR VOTING:

2024-11-29

SUPERSEDES DOCUMENTS:

47/2857/RR

IEC TC 47 : SEMICONDUCTOR DEVICES

SECRETARIAT:

Korea, Republic of

SECRETARY:

Mr Cheolung Cha

OF INTEREST TO THE FOLLOWING COMMITTEES:

HORIZONTAL FUNCTION(S):

ASPECTS CONCERNED:

☐ SUBMITTED FOR CENELEC PARALLEL VOTING

☒ NOT SUBMITTED FOR CENELEC PARALLEL VOTING

This document is still under study and subject to change. It should not be used for reference purposes.

Recipients of this document are invited to submit, with their comments, notification of any relevant patent rights of which they are aware and to provide supporting documentation.

Recipients of this document are invited to submit, with their comments, notification of any relevant "In Some Countries" clauses to be included should this proposal proceed. Recipients are reminded that the CDV stage is the final stage for submitting ISC clauses. (SEE [AC/22/2007](#) OR [NEW GUIDANCE DOC](#)).

TITLE:

Semiconductor devices - Mechanical and climatic test methods - Part 21: Solderability

PROPOSED STABILITY DATE: 2030

NOTE FROM TC/SC OFFICERS:

Copyright © 2024 International Electrotechnical Commission, IEC. All rights reserved. It is permitted to download this electronic file, to make a copy and to print out the content for the sole purpose of preparing National Committee positions. You may not copy or "mirror" the file or printed version of the document, or any part of it, for any other purpose without permission in writing from IEC.

CONTENTS

FOREWORD	4
1 Scope	6
2 Normative references	6
3 Test apparatus	6
3.1 Solder bath	6
3.2 Dipping device	6
3.3 Optical equipment	7
3.4 Steam ageing equipment	7
3.5 Lighting equipment	7
3.6 Materials	7
3.6.1 Flux	7
3.6.2 Solder	8
3.7 SMD reflow equipment	8
3.7.1 Stencil or screen	8
3.7.2 Rubber squeegee or metal spatula	8
3.7.3 Test substrate	9
3.7.4 Solder paste	9
3.7.5 Reflow equipment	9
3.7.6 Flux removal solvent	10
4 Procedure	10
4.1 Lead-free backward compatibility	10
4.2 Preconditioning	10
4.2.1 General	10
4.2.2 Preconditioning by steam ageing	10
4.2.3 Preconditioning by high temperature storage	11
4.3 Procedure for dip and look solderability testing	11
4.3.1 General	11
4.3.2 Solder dip conditions	11
4.3.3 Procedure	12
4.4 Procedure for simulated board mounting reflow solderability testing of SMDs	19
4.4.1 General	19
4.4.2 Test equipment set-up	19
4.4.3 Specimen preparation and surface condition	20
4.4.4 Visual inspection	21
5 Summary	21
Bibliography	22
Figure 1 – Areas to be inspected for gullwing packages	15
Figure 2 – Areas to be inspected for J-lead packages	16
Figure 3 – Areas to be inspected in rectangular components (SMD method)	17
Figure 4 – Areas to be inspected in SOIC and QFP packages (SMD method)	18
Figure 5 – Flat peak type reflow profile	20
Table 1 – Steam ageing conditions	10
Table 2 – Altitude versus steam temperature	11

Table 3 – Solder dip test conditions	11
Table 4 – Maximum limits of solder bath contaminant	13

iTeh Standards
(<https://standards.iteh.ai>)
Document Preview

[oSIST prEN IEC 60749-21:2025](https://standards.iteh.ai/catalog/standards/sist/9200bacf-a207-4e9d-bec9-edb4ccb77435/osist-pren-iec-60749-21-2025)

<https://standards.iteh.ai/catalog/standards/sist/9200bacf-a207-4e9d-bec9-edb4ccb77435/osist-pren-iec-60749-21-2025>

INTERNATIONAL ELECTROTECHNICAL COMMISSION

**SEMICONDUCTOR DEVICES –
MECHANICAL AND CLIMATIC TEST METHODS –****Part 21: Solderability****FOREWORD**

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.
- 5) IEC itself does not provide any attestation of conformity. Independent certification bodies provide conformity assessment services and, in some areas, access to IEC marks of conformity. IEC is not responsible for any services carried out by independent certification bodies.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

International Standard IEC 60749-21 has been prepared by IEC technical committee 47: Semiconductor devices.

This standard cancels and replaces the second edition published in 2011 and constitutes a technical revision. The significant change is revision to certain operating conditions in line with current working practices.

The text of this standard is based on the following documents:

FDIS	Report on voting
47/XXXX/FDIS	47/YYYY/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

- 54 This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.
- 55 A list of all parts in the IEC 60749 series, under the general title *Semiconductor devices –*
56 *Mechanical and climatic test methods* can be found on the IEC website.
- 57 The committee has decided that the contents of this publication will remain unchanged until
58 the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data
59 related to the specific publication. At this date, the publication will be
- 60 • reconfirmed,
 - 61 • withdrawn,
 - 62 • replaced by a revised edition, or
 - 63 • amended.
- 64
- 65

iTeh Standards
(<https://standards.iteh.ai>)
Document Preview

[oSIST prEN IEC 60749-21:2025](https://standards.iteh.ai/catalog/standards/sist/9200bacf-a207-4e9d-bec9-edb4ccb77435/osist-pren-iec-60749-21-2025)

<https://standards.iteh.ai/catalog/standards/sist/9200bacf-a207-4e9d-bec9-edb4ccb77435/osist-pren-iec-60749-21-2025>

SEMICONDUCTOR DEVICES – MECHANICAL AND CLIMATIC TEST METHODS –

Part 21: Solderability

1 Scope

This part of IEC 60749 establishes a standard procedure for determining the solderability of device package terminations that are intended to be joined to another surface using tin-lead (SnPb) or lead-free (Pb-free) solder for the attachment.

This test method provides a procedure for 'dip and look' solderability testing of through hole, axial and surface mount devices (SMDs) as well as an optional procedure for a board mounting solderability test for SMDs for the purpose of allowing simulation of the soldering process to be used in the device application. The test method also provides optional conditions for ageing.

This test is considered destructive unless otherwise detailed in the relevant specification.

NOTE 1 This test method does not assess the effect of thermal stresses which may occur during the soldering process. Reference should be made IEC 60749-15 or IEC 60749-20.

2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 61190-1-2:2014, *Attachment materials for electronic assembly – Part 1-2: Requirements for soldering pastes for high-quality interconnects in electronics assembly*

IEC 61190-1-3:2018, *Attachment materials for electronic assembly – Part 1-3: Requirements for electronic grade solder alloys and fluxed and non-fluxed solid solders for electronic soldering applications*

3 Test apparatus and materials

3.1 Solder bath

The solder bath shall be not less than 40 mm in depth and not less than 300 ml in volume such that it can contain at least 1 kg of solder. The apparatus shall be capable of maintaining the solder at the specified temperature within ± 5 °C. The solder in solder baths used for solderability testing shall be analysed or replaced to ensure that the composition complies with 3.6.2

3.2 Dipping device

A mechanical dipping device capable of controlling the rates of immersion and emersion of the terminations and providing a dwell time (time of total immersion to the required depth) in the solder bath as specified shall be used.

106 3.3 Optical equipment

107 An optical microscope capable of providing magnification inspection from 10× to 20× shall be
108 used.

109 3.4 Steam ageing equipment

110 A non-corrodible container and cover of sufficient size to allow the placement of specimens
111 inside the vessel shall be used. The specimens shall be placed such that the lowest portion of
112 the specimen is a minimum of 40 mm above the surface of the water. A suitable method of
113 supporting the specimens shall be improvised using non-contaminating material.

114 NOTE During steam ageing, the test devices should be located in a manner so as to prevent water (steam
115 condensate) from dripping on them.

116 3.5 Lighting equipment

117 A lighting system shall be used that will provide a uniform, non-glare, non-directional
118 illumination of the specimen.

119 3.6 Materials

120 3.6.1 Flux

121 Unless otherwise detailed in the relevant specification, the flux for SnPb solderability tests shall
122 be a standard activated rosin flux (type ROL1 in accordance with IEC 61190-1-3 (2018), Table
123 2, Flux type and designating symbols) having a composition of 25 % ± 0,5 % by weight of
124 colophony and 0,15 % ± 0,01 % by weight diethylammonium hydrochloride, in 74,85 % ±
125 0,5 % by weight of in 2-propanol (isopropanol). The specific gravity of the standard activated
126 rosin flux shall be $0,843 \pm 0,005$ at $25\text{ °C} \pm 2\text{ °C}$.

127 The specification shall be as follows:

128 Colophony

129	Colour	To WW colour specification or paler
130	Acid value (mg KOH/g colophony)	155 (minimum)
131	Softening point (ball and ring)	70 °C (minimum)
132	Flow point (Ubbelohde)	76 °C (minimum)
133	Ash	0,05 % (maximum)
134	Solubility	A solution of the colophony in an equal part by weight
135		of 2-propanol (isopropanol) shall be clear, and after a
136		week at room temperature there shall be no sign of a
137		deposit.

138 2-propanol (isopropanol)

139	Purity	Minimum 99,5 % 2-propanol (isopropanol) by weight
140	Acidity as acetic acid	Maximum 0,002 % weight (other than carbon dioxide)
141	Non-volatile matter	Maximum 2 mg per 100 ml.

142 Unless otherwise detailed in the relevant specification, the flux for Pb-free solderability tests
143 shall be standard activated rosin flux having a composition of 25% ± 0.5% by weight of
144 colophony and 0.39% ± 0.01% by weight diethylammonium hydrochloride, in 74.61% ± 0.5%
145 by weight of isopropyl

146

147 **3.6.2 Solder**

148 **3.6.2.1 Tin-lead**

149 Unless otherwise detailed in the relevant specification, the solder specification for SnPb shall
150 be as follows:

151 *Chemical composition*

152 The composition in percentage by weight shall be as follows:

153	Tin	59 % to 61 %
154	Antimony	0,5 % maximum
155	Copper	0,1 % maximum
156	Arsenic	0,05 % maximum
157	Iron	0,02 % maximum
158	Lead	the remainder.

159 The solder shall not contain such impurities as aluminium, zinc or cadmium in amounts which
160 will adversely affect the properties of the solder.

161 *Melting temperature range*

162 The melting temperature range of the 60 % solder is as follows:

163	Completely solid	183 °C
164	Completely liquid	188 °C.

165 **3.6.2.2 Lead-free**

166 Unless otherwise detailed in the relevant specification, the solder specification for Pb-free
167 shall be as follows:

168 The composition in percentage by weight shall be as follows:

169	Tin	95 % to 96,5 %
170	Silver	3 % to 4 %
171	Copper	0,5 % to 1 %.

172 **3.7 SMD reflow equipment**

173 **3.7.1 Stencil or screen**

174 A stencil or screen with pad geometry opening that is appropriate for the terminals being
175 tested. Unless otherwise agreed upon between vendor and user, nominal stencil thickness
176 should be 0,1 mm for terminals with less than 0,5 mm component lead pitch, 0,15 mm for a
177 component with lead pitch of 0,5 mm to 0,65 mm and 0,2 mm for a component with lead pitch
178 greater than 0,65 mm.

179 **3.7.2 Rubber squeegee or metal spatula**

180 Solder paste shall be applied on to the stencil or screen using a spatula for fine pitch or a
181 squeegee for standard pitch.