

~~ISO/IEC TR 6114:2023(E)~~

~~ISO/IEC TR 6114~~

~~ISO/IEC JTC 1/SC 27 N-#:####(X)~~

~~ISO/IEC TR 6114~~

~~ISO/IEC JTC 1/SC 27/WG 4~~

~~Date: 2023-01-3106-23~~

~~Secretariat: DIN~~

Cybersecurity – Security considerations throughout the product life cycle

Cybersécurité — Considérations relatives à la sécurité tout au long du cycle de vie du produit

ISO/IEC DTR 6114

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Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for worldwide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work.

The procedures used to develop this document and those intended for its further maintenance are described in the ISO/IEC Directives, Part 1. In particular, the different approval criteria needed for the different types of document should be noted. This document was drafted in accordance with the editorial rules of the ISO/IEC Directives, Part 2 (see www.iso.org/directives or www.iec.ch/members_experts/refdocs).

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For an explanation of the voluntary nature of standards, the meaning of ISO specific terms and expressions related to conformity assessment, as well as information about ISO's adherence to the World Trade Organization (WTO) principles in the Technical Barriers to Trade (TBT) see www.iso.org/iso/foreword.html. In the IEC, see www.iec.ch/understanding-standards.

This document was prepared by Joint Technical Committee ISO/IEC JTC 1, Information technology, Subcommittee SC 27, Information security, cybersecurity and privacy protection.

Any feedback or questions on this document should be directed to the user's national standards body. A complete listing of these bodies can be found at www.iso.org/members.html and www.iec.ch/national-committees.

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Introduction

The globalization of technology design, development, manufacturing, and distribution has created an environment of complicated supply chains with limited transparency. This presents an incredible challenge for the industry and highlights a growing need to ensure product integrity for all stages of the ~~ICT~~ information and communications technology (ICT) product life cycle.

The call for assurance across the supply chain landscape has evolved over several decades. More recently, policy makers around the world have begun to focus on supply chain risks in new ways: from policies considering supply chain security risks for government procurement to various initiatives by adding security considerations throughout the supply chain which spotlight the such as trust and transparency of in the supply chainschain for information and communications technologyICT.

Vendors have been doing their part as well. Over the past several years, Information Communications Technology (ICT) suppliers have taken important steps towards increasing supply chain transparency, including. These steps include, sourcing conflict-free minerals [1], [1], and implementing a set of policies, procedures and tools at factories to improve security consideration throughout the supply chain by validating where and when each component of an ICT product was manufactured.

These are important first steps, however they primarily focus on the production stage, just one stage of the ICT product life cycle. In today's complex environment, hardware platform providers are expected to enable a full range of tools and solutions that improve security consideration across the entire life cycle, from design and sourcing to secure retirement.

Security considerations throughout the product life cycle (SCLC), establishes an end to end framework that can be applied to the multi-year life cycle of ICT product, to comprehend and address potential risks for improved transparency and higher levels of security assurances. By enabling transparency and assurances across the ICT product life cycle, supply chain owners can improve platform integrity, resilience and security. The life cycle phases are both iterative and recursive in nature.

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¹ The main target of the document is ICT hardware products including software such as microcode, firmware, and other software to support the hardware products.

Cybersecurity - Security considerations throughout the product life cycle

1 Scope

This document describes security considerations throughout the product life cycle (SCLC), which is a framework that spans the entire **information and communications technology (ICT)** product life cycle. The aim of the framework is to align the industry and bring greater transparency to customers at every point on the ICT product life cycle.

This document describes the following items for ~~suppliers~~suppliers, end users (~~consumer~~consumers), intermediaries of the ICT supply chain, service ~~provider~~providers, and regulators:

- ~~—~~ definition of phases in **the** ICT product life cycle from concept to retirement;
- ~~—~~ threat vectors possible in each phase of the life cycle;
- ~~—~~ potential controls against those threat vectors.

The target audiences of this document are suppliers and consumers of ICT products, including all participants throughout the supply chain such as silicon chip designers, fabricators, product assemblers, logistics providers, service providers, and information security organizations. ~~Clauses 5 to 11~~ target an organization's strategic and risk management teams. ~~This document provides an end-to-end view of the threats by in each phase to help the organization shape their plans, procedures and policies.~~

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

- ISO/IEC/~~IEEE 15288~~IEEE 15288:2015, *Systems and software engineering — System life cycle processes*
- ISO/IEC/IEEE 24748-1:2018, *Systems and software engineering — Life cycle management — Part 1: Guidelines for life cycle management*

3 Terms and definitions

For the purposes of this document, the terms and definitions given in ISO/IEC/IEEE 15288:2015, ISO/IEC/IEEE 24748-1:2018, and the following apply.

ISO and IEC maintain ~~terminological~~terminology databases for use in standardization at the following addresses:

- ISO Online browsing platform: available at <https://www.iso.org/obp><https://www.iso.org/obp>

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— IEC Electropedia: available at <http://www.electropedia.org/https://www.electropedia.org/>

3.1 digital signature

data appended to, or a cryptographic transformation of, a data unit that allows the recipient of the data unit to verify the source and integrity of the data unit

[SOURCE: ISO/IEC 9798-3:2019, 3.3]

3.2 code scanner

program source code and binary file security analysis tool

3.3 hardware trojan

malicious program or hardware that masquerades as a benign application

4 Acronyms

- ASIC — Application specific integrated circuit
- BOM — Bill of materials
- CPU — Central processing unit
- DRM — Digital rights management
- DSP — Digital signal processor
- ERP — Enterprise resource planning
- FPGA — Field programable gate array
- HDL — Hardware description language
- IC — Integrated circuit
- ICT — Information and communication technology
- OEM — Original equipment manufacture
- OSAT — Outsourced semiconductor assembly and test
- PUF — Physically unclonable function
- SaaS — Software as a service
- SDL — Security Development Life-cycle
- SoC — System on Chip
- VHDL — Very high-speed integrated circuit hardware description language

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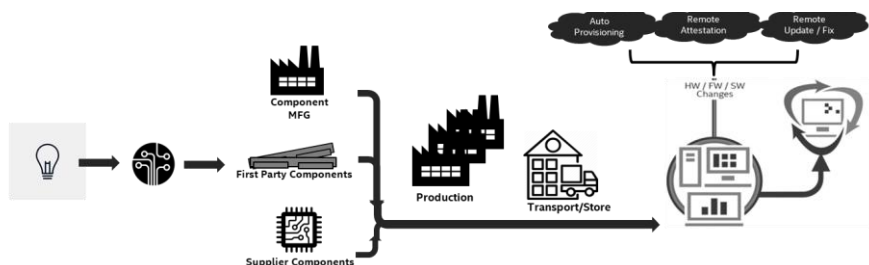
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Stages in ISO/IEC/IEEE 24748



4 ICT system life cycle of ISO/IEC/IEEE 24748-1
Abbreviated terms

- ASIC application specific integrated circuit
- BOM bill of materials
- CPU central processing unit
- DRM digital rights management
- DSP digital signal processor
- ERP enterprise resource planning
- FPGA field programable gate array
- FW firmware
- HDL hardware description language
- HW hardware
- IC integrated circuit
- ICT information and communication technology
- ISV independent software vender
- OEM original equipment manufacture
- OSAT outsourced semiconductor assembly and test
- OS operating system
- PUF physically unclonable function
- SaaS software as a service
- SDL security development life cycle
- SoC system on chip
- SW software
- VHDL very high-speed integrated circuit hardware description language

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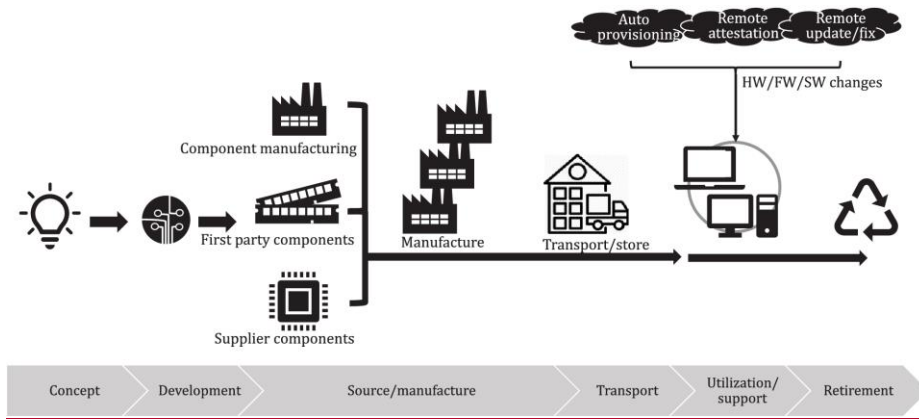


Figure 1 — SOURCE: ISO/IEC/IEEE 24748-1:2018 [3], reproduced with the permission of the authors.

Figure 1 — ICT system life cycle

5 Security considerations throughout the product life cycle

5.1 Security considerations throughout the product life cycle overview

Security considerations throughout the product life cycle (SCLC) is a framework to describe the ICT products life cycle from security assurance and manufacturing perspectives (see Figure-1). The concept of SCLC can be applied to hardware products and software such as microcode, firmware, and other software to support the hardware. SCLC consists of six common product life cycle phases which are shown in Figure-2 with typical threat vectors. The names of the phases is mapped and derived from ISO/IEC/IEEE 24748-1:2018 [3] and added SCLC unique processes unique to SCLC have been added. It is important to note that each phase exists for individual components, sub-systems and end products. -It is both possible and highly likely that some of the phases are executed more than once for an individual component (for example, integrated e.g. integrated circuits go through wafer fabrication, sorting, assembly and final test and those Those facilities, if separate will, introduce multiple build/transfer phases).

ISO/IEC/IEEE 15288 [2] is a technical standard which ISO/IEC/IEEE 15288 defines the life cycle of a system by four process groups which are 1) agreement process, 2) organizational project-enabling process, 3) technical management process and 4) technical process. Since this document describes the life cycle from a different perspective that is, the "product life cycle", there is no conflict or inconsistency between ISO/IEC/IEEE 15288/IEEE 15288 and this document. -Another systems approach to security is the NIST SP 800-160V1 that aligns with this approach. [5], [3]

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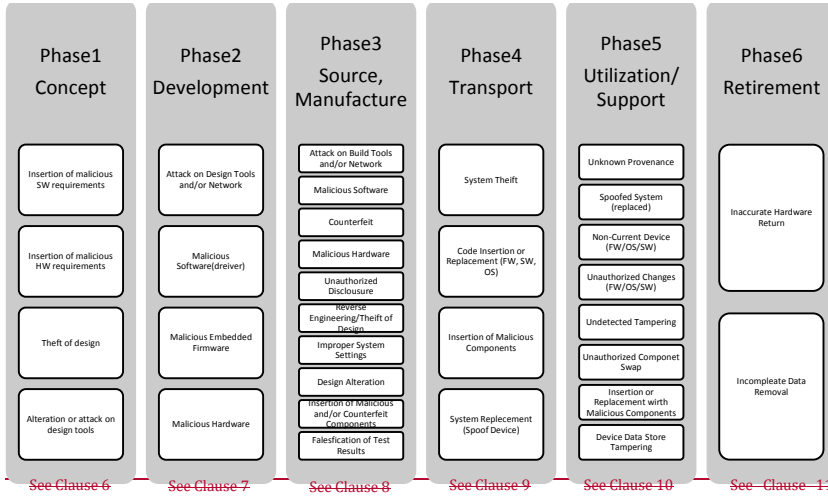
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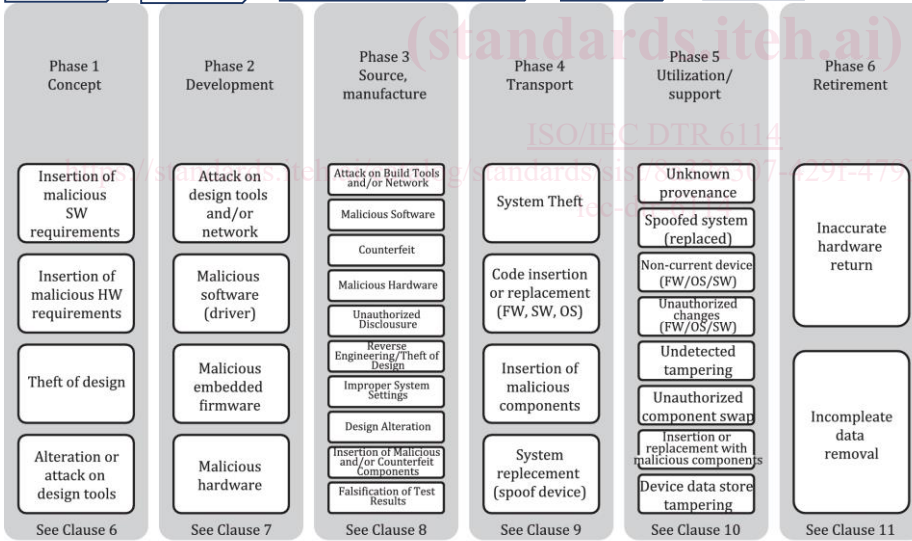
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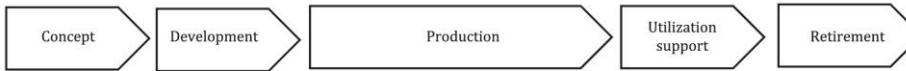


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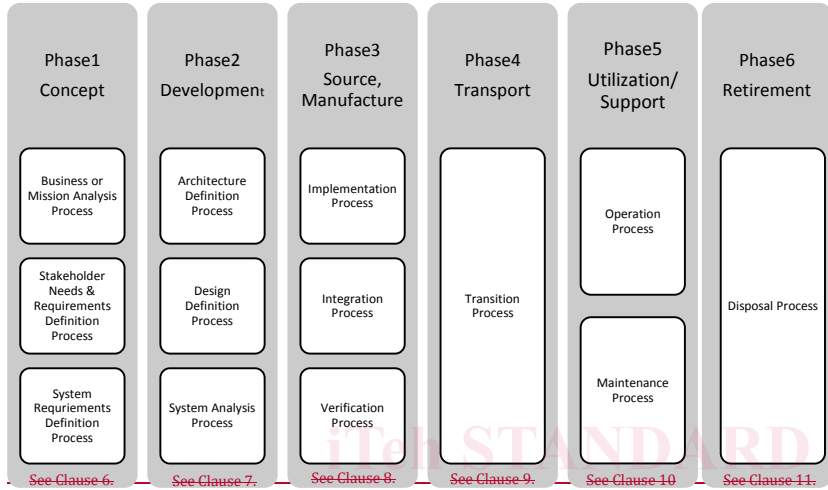
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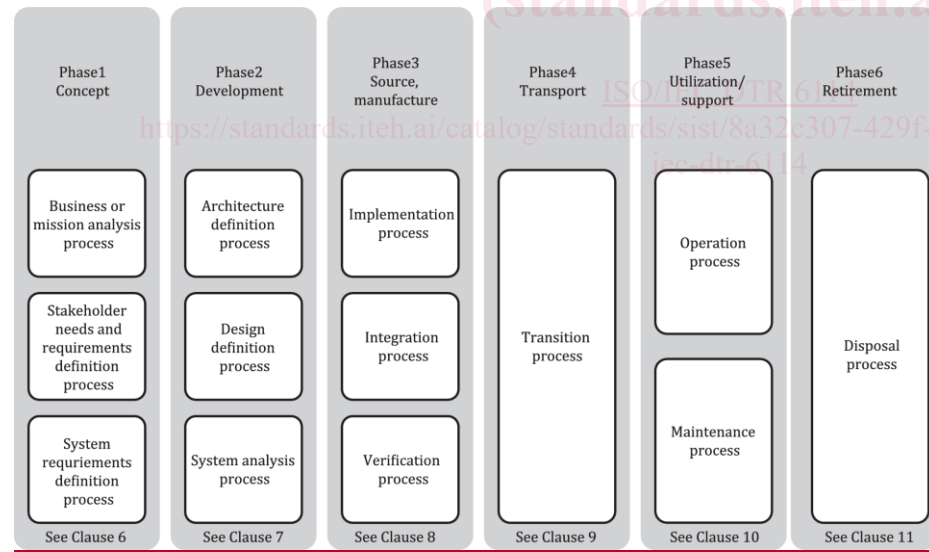
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Figure 2 — ICT Product life cycle and threat vectors

Figure-3 shows a relationship of technical processes in ISO/IEC/IEEE 15288 [2] and SCLC phases.



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Figure 3 — Mapping ISO/IEC/IEEE 15288 [2] technical processes of ISO/IEC/IEEE 15288 and SCLC life cycle phases

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5.2 Information and communication technology threat model

Security threats against ICT products exist not only in the utilization/support phase but in all six phases shown in Figure 2-6, 2-4. Each phase has different threat vectors, but some vectors such as theft occur in multiple phases. This means responsible parties are expected to apply proper measures in each phase for each threat vector applicable to the phase.

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From Clause 6.6 to Clause 11.11, the key characteristics of each ICT product's life cycle phase and typical threat vectors are described.

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5.3 Classes of threats

There are multiple classes of security threats. The first type is an attack which targets unintentional vulnerability, such as ICT product security vulnerability in the manufacturing process or in the physical storage and transportation processes. The second type of security threats are intentional attacks by insider personnel who can access the process and change or replace tools, products, or components. The focus of this document is to identify and protect ICT products from the first type of threats. However, the methods described in this document can also be used to mitigate the risks of the second type of threats. In many cases, additional protection measures are necessary, such as employee/personnel management and access control in all phases for all stakeholders who participate in the product life cycle.

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5.4 Structure of the report

From clause Clauses 6 to 11, give an overview of each phase, threats, and controls are described. Additionally, annexes [see Annex Annexes A, B, C and D] are included for practitioners on provide specific best practice procedures and controls for practitioners that they can be implemented/implemented within their operation to mitigate against potential threats.

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6 Phase 1: Concept

6.1 General

This is the first phase of the ICT product life cycle. In the concept phase, user needs are identified, and system concepts are described and evaluated. In addition to the users' user needs, companies are expected to consider regulatory controls and security objectives to establish a comprehensive set of requirements to be met in development.

Given the complexity of ICT products, many companies will combine internal design, 3rd third party design and open source design to deliver a single component on a system where a system which can be comprised of/comprise hundreds of components. For example, the System/system on a Chip/chip (SOC) component can contain hardware blocks sourced from many third parties, as well as blocks designed by the product manufacturer. This complexity introduces additional risks as unneeded features from external components which are incorporated into the end product, possibly creating a vulnerability that can be attacked. For this reason, the product flows back through the concept phase to add additional security control requirements based upon decisions made in development.

It is best practice for ICT product organizations to adopt a Security Development Lifesecurity development life cycle (SDL) process that conforms with ISO/IEC 27034-1:2011 [7]. This process ensures the identification of unique threats at the start of the concept phase and turns those into a set of requirements to be met during the development phase. Additionally, SDL requires the training of the